

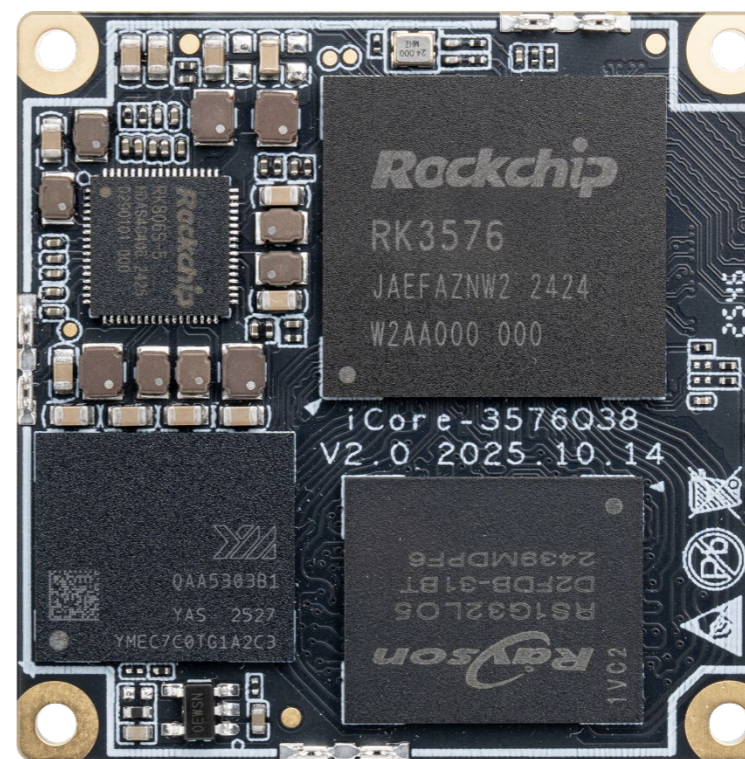


迷你 AI 核心板

- ICORE-3576Q38 (商规级)
- ICORE-3576JQ38 (工规级)
- ICORE-3576MQ38 (车规级)

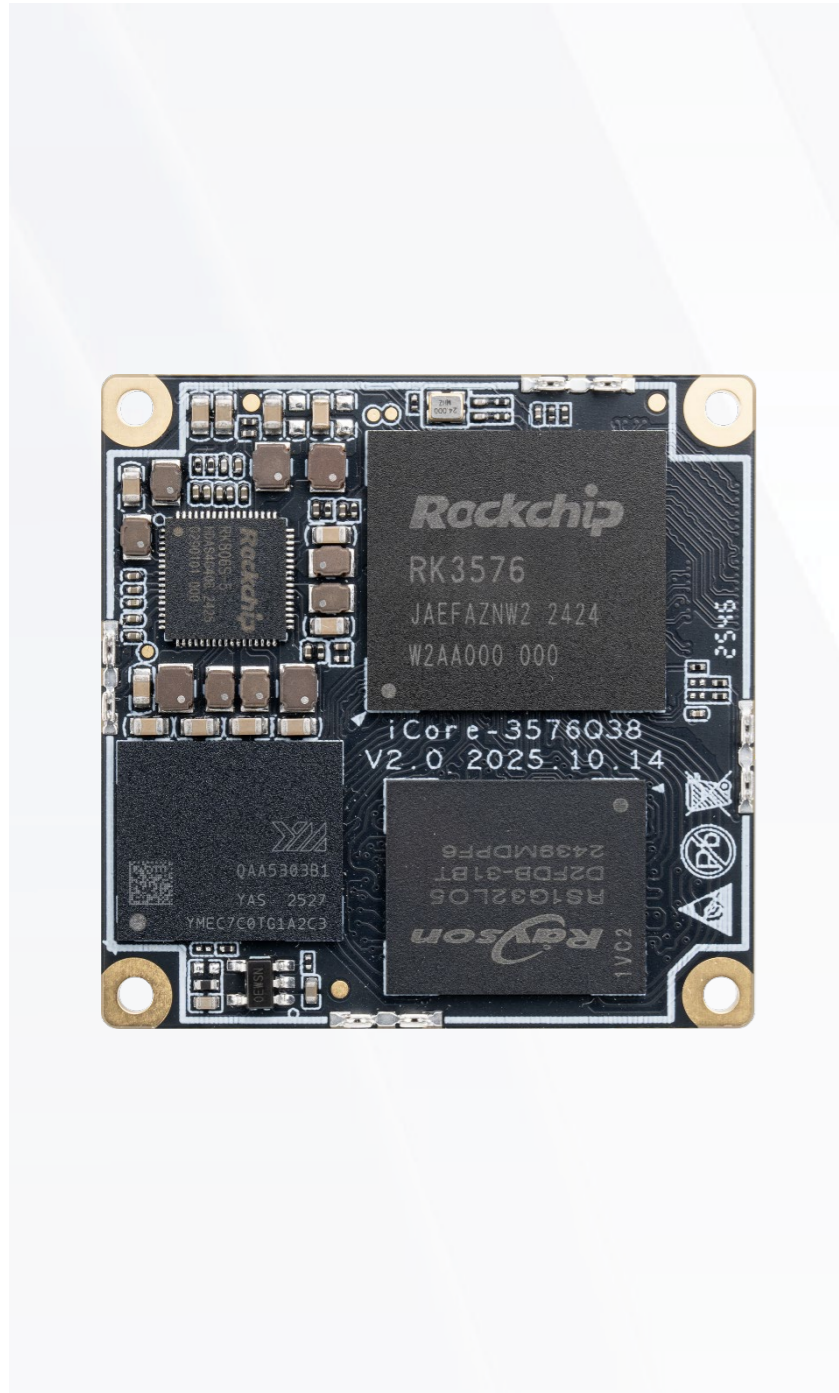
V2.0 2026-4-2

天启智能科技





产品特点 Product features



八核64位AIOT处理器RK3576

新一代八核64位高性能AIOT处理器RK3576, 采用大小核构架(4xA72+4xA53), 先进工艺制程, 主频高达2.2GHz



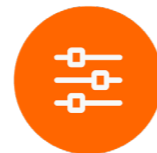
内置6TOPS算力NPU

NPU算力高达6TOPS, 支持INT4/INT8等操作, 支持双核协同工作或独立工作, 支持多任务、多场景并行; 支持YOLO、语言大模型私有化部署



工业新特性, 满足工业应用需求

具有实时网络、Flexbus、硬件资源隔离、DSMC等工业新特性, 满足不同的工业应用需求



丰富的扩展接口

拥有CAN FD、I3C、UART、I2C、SAI、PWM、SPI、PCIe2.1、SATA3.0、USB3.0等扩展接口, 满足不同场景的外设扩展需求



4K@120fps高帧率视频解码

8K@30fps/4K@120fps视频解码(H.265/HEVC、VP9、AVS2、AV1), 4K@60fps视频编码(H.265/HEVC、H.264/AVC)



BTB接口, 38mmx38mm小尺寸

核心板采用BTB接口设计, 配用高速工业级连接器, 具有超强的传输能力、高频传输稳定性、无需焊接的便捷性; 核心板仅38mmx38mm, 尺寸小巧, 节约更多宝贵的空间



支持RTLinux、多种操作系统

支持RTLinux内核, 实时性能优秀; 支持Android14、Linux OS、Buildroot, 为产品研发提供安全稳定的系统环境



广泛的应用场景

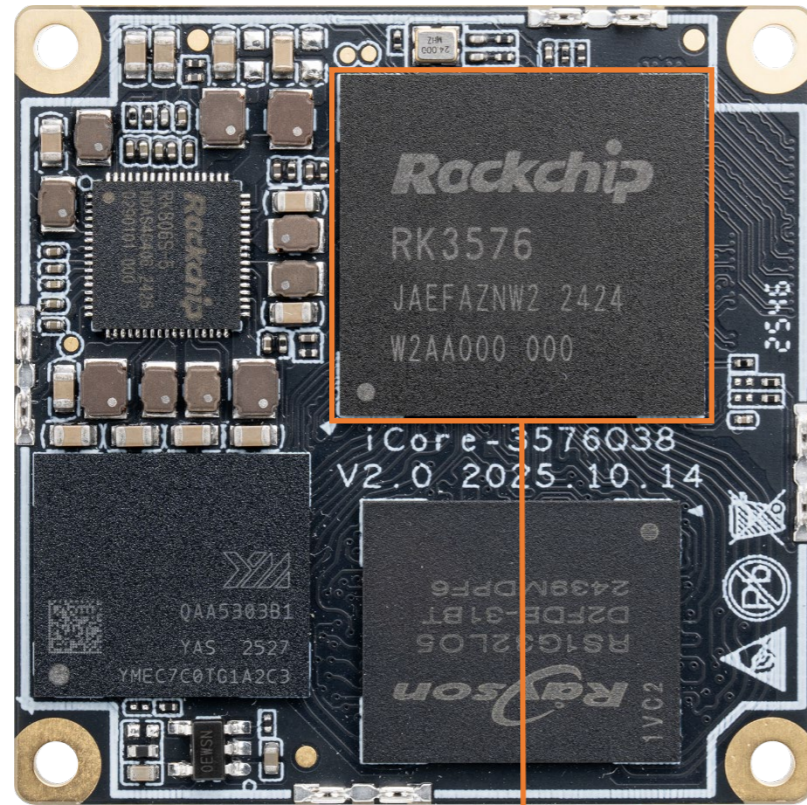
广泛适用于: 边缘计算、大模型本地化、机器视觉、工业摄像头、工控主机、汽车电子等行业领域

规格参数 Specifications



		ICORE-3576Q38 (商规级)	ICORE-3576JQ38 (工规级)	ICORE-3576MQ38 (车规级)
基本参数	SOC	Rockchip RK3576	Rockchip RK3576J	Rockchip RK3576M
	CPU	八核64位处理器 (4xA72 + 4xA53), 主频最高 2.2GHz	八核64位处理器 (4xA72 + 4xA53), 主频最高 1.6GHz	
	GPU	G52 MC3 @ 1GHz, 支持 OpenGL ES 1.1/2.0/3.2, OpenCL 2.0, Vulkan 1.1, 内嵌高性能 2D 加速硬件		
	NPU	6 TOPS算力 NPU, 支持 INT4/INT8/INT16/FP16/BF16/TF32操作, 支持双核协同工作或独立工作, 支持多任务、多场景并行		
	ISP	内置1600万像素 ISP, 支持低光降噪, 支持RGB-IR sensor, 支持最高120dB HDR, AI-ISP提升低噪声的图像效果		
	编解码	视频解码: 8K@30fps/4K@120fps (H.265/HEVC、VP9、AVS2、AV1)、4K@60fps (H.264/AVC) 视频编码: 4K@60fps (H.265/HEVC、H.264/AVC) 图片编解码: 4K@60fps MJPG		
	内存	LPDDR4/LPDDR4x/LPDDR5 (4GB/8GB/16GB 可选)		
	存储	eMMC (16GB/32GB/64GB/128GB/256GB 可选)		
	电源	5.0V (电压误差 ± 5%)		
	系统	支持RTLinux 内核, 实时性能优秀, 广泛应用于工业应用场景 支持Android14、Linux OS、Buildroot, 为产品研产提供安全稳定的系统环境 具有实时网络、Flexbus、硬件资源隔离、DSMC等工业新特性, 满足不同的工业应用需求		
	AI性能	支持Transformer架构下超大规模参数模型的私有化部署, 如Gemma系列、ChatGLM系列、Qwen系列、Phi系列等大型语言模型 支持CNN、RNN、LSTM等传统网络架构, 支持RKNN模型导入导出, 支持多种深度学习框架, 包括TensorFlow、TensorFlow Lite、PyTorch、Caffe、ONNX和Darknet, 并支持自定义算子开发 支持Docker容器化管理技术		
		支持基于深度学习的实时目标检测算法YOLO (You Only Look Once), 相比于传统的目标检测方法, YOLO具有快速和实时性的优势, 能够在图像或视频中准确地识别和定位多个目标物体, 强势赋能AI应用		
	接口类型	BTB (2 × BTB座(100Pin))		
	尺寸	38.0mm × 38.0mm × 5.68mm		
	重量	≈10g		
环境	工作温度: -20°C ~ 60°C 存储湿度: 10% ~ 90%RH (无凝露)		工作温度: -40°C ~ 85°C 存储湿度: 10% ~ 90%RH (无凝露)	工作温度: -40°C ~ 85°C 存储湿度: 10% ~ 90%RH (无凝露)
	网络			
	2 × GMAC (提供RMII 或 RGMII 接口连接外置的 Ethernet PHY; 支持10/100/1000 Mbps速率), 通过USB、PCIe、SDIO、UART可扩展WiFi、蓝牙、4G、5G			
接口参数	视频输入	1 × MIPI DPHY CSI (支持MIPI V1.2 版本; 1 × 4 Lanes 或 2 × 2 Lanes)、 1 × MIPI DCPHY CSI RX (DPHY 支持 V2.0 版本 支持4Lane/2Lane/1Lane模式; CPHY 支持 V1.1 版本 支持0/1/2 Trio模式)		
	视频输出	1 × HDMI2.1(4096×2160@120Hz)/eDP1.3(4096×2160@60Hz支持1Lane/2Lane/4Lane 模式) 1 × DP1.4 (4096×2160@120Hz) 1 × EBC 输出接口 (支持 E-ink EPD (Electronic Paper Display), 2560×1920)		
	音频	4 × SAI (4T/4R), 支持 I2S/TDM/PCM 模式, 支持高达 192KHz 的采样率		
	PCIe/SATA	1 × PCIe 2.1/SATA 3.1/USB 3.2 Gen1 组合接口 1 × PCIe 2.1/SATA 3.1 组合接口		
	USB	1 × USB3.2 Gen1 OTG0 (与DP1.4复用) 1 × USB3.2 Gen1 OTG1 (与PCIe 2.1/SATA 3.1复用)		
	SDIO	2 × SDIO3.0		
	PWM	14 × PWM		
	SPI	4 × SPI (支持串行主站和串行从模式, 软件可配置)		
	I2C	9 × I2C (支持 7 位和 10 位地址模式, 标准模式下数据速率高达100kbps, 快速模式下数据速率高达400kbps)		
	I3C	1 × I3C (符合 I2C 标准, 支持 SDR 模式、支持多达 10 个设备)		
	UART	12 × UART (支持自动流量控制模式、支持 RS485 功能)		
	CAN	2 × CAN FD (支持 8192 bit 接收 FIFO)		
	SARADC	3 × SARADC + 1 × SARADC (仅用于 boot), 支持 12 位分辨率, 高达 1MS/s 的采样率		

核心板接口描述 Core board Interface description



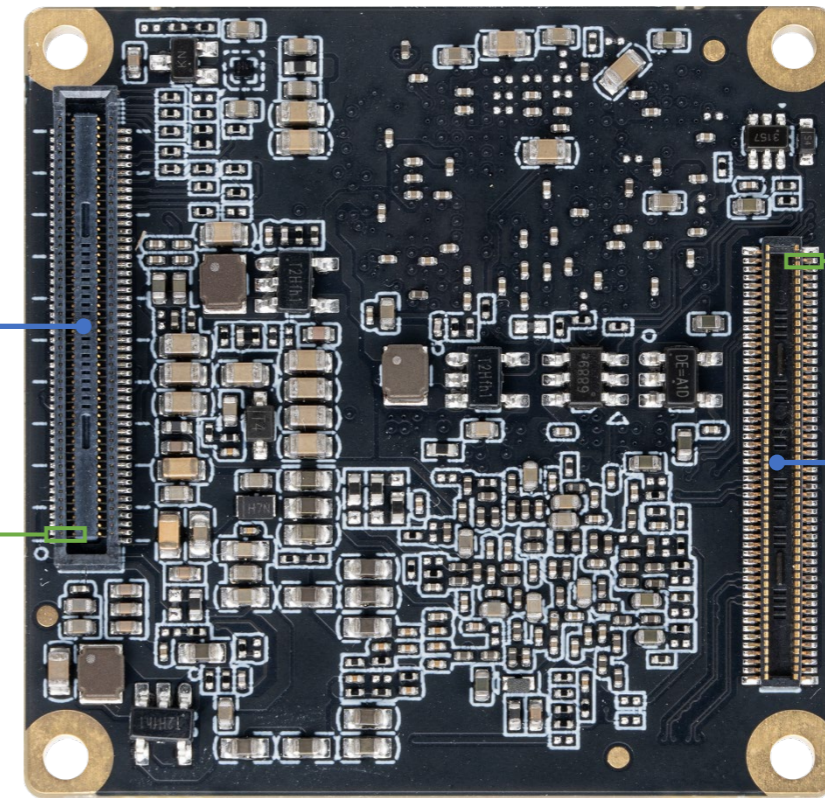
Rockchip RK3576 (商规级) /
Rockchip RK3576J (工规级) /
Rockchip RK3576M (车规级)

J2-BTB座 (100Pin)

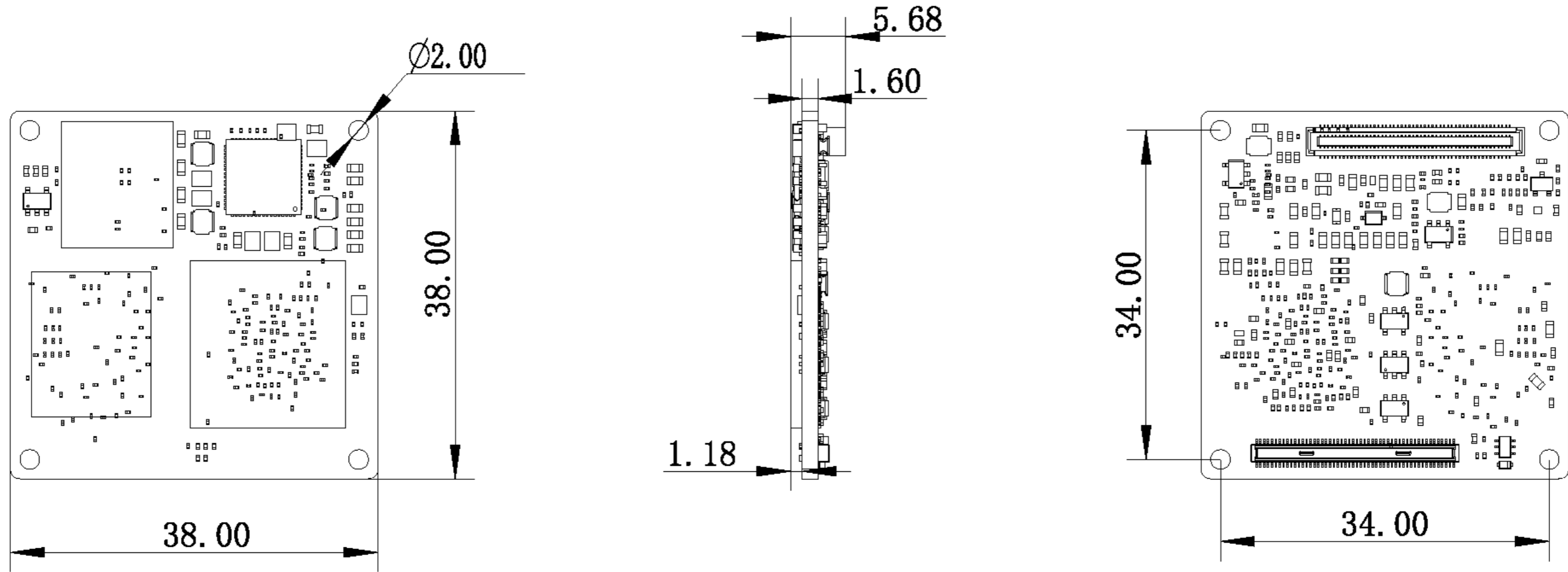
Pin1

Pin1

J1-BTB座 (100Pin)



核心板尺寸 Core board Dimension





接口定义 Interface definition

① : Pin types: I = input, O = output, I/O = input/output (bidirectional), G= Ground ,
P = power supply , DOWN = Internal pull down , UP = Internal pull UP L = Lowe Level H = High level”

PIN	ICORE-3576Q38 pin definition (J1)	RK3576 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
1	CLK_32K_IN/CLK0_32K_OUT/I2C6_SCL_M0/GPIO0_A2_d	1U23	I/O	1.8V	DOWN	32KOUT_RTC2SOC	32KOUT_RTC2SOC
3	UART0_RX_M0/JTAG_TMS_M1/GPIO0_D5_u	AA28	I/O	3.3V	UP	UART0_RX_M0_DEBUG	UART0_RX_M0_DEBUG
5	UART0_TX_M0/JTAG_TCK_M1/GPIO0_D4_u	1U24	I/O	3.3V	UP	UART0_TX_M0_DEBUG	UART0_TX_M0_DEBUG
7	SARADC_IN0_BOOT	A25	I	1.8V		SARADC_VIN0_BOOT	SARADC_VIN0_BOOT
9	SARADC_IN1	1A22	I	1.8V		SARADC_VIN1_KEY/RECOVERY	SARADC_VIN1_KEY/RECOVERY
11	SARADC_IN3	1C19	I	1.8V		SARADC_VIN3_HP_HOOK	SARADC_VIN3_HP_HOOK
13	SARADC_IN4	1E18	I	1.8V		SARADC_VIN4	SARADC_VIN4
15	DSM_AUD_RP_M1/HDMI_TX_SCL/I2C2_SCL_M3/CAN0_TX_M1/UART9_TX_M2/PWM2_CH0_M1/GPIO4_C2_d	AL2	I/O	3.3V	DOWN	POW_HOLD	POW_HOLD
17	DSM_AUD_LN_M1/HDMI_TX_HPDIN_M0/PCIE1_CLKREQ_N_M3/I2C7_SDA_M3/EDP_TX_HPDIN_M0/UART11_RX_M2/PWM0_CH1_M1/GPIO4_C1_d	AK2	I	3.3V	DOWN	UART11_RX_M2	UART11_RX_M2
19	DSM_AUD_LP_M1/SAI4_MCLK_M2/HDMI_TX_CEC_M0/I2C7_SCL_M3/SPI4_CSN1_M0/UART11_TX_M2/PWM1_CH5_M1/GPIO4_C0_d	AK3	I/O	3.3V	DOWN	UART11_TX_M2	UART11_TX_M2
21	DSM_AUD_RN_M1/HDMI_TX_SDA/I2C2_SDA_M3/CAN0_RX_M1/UART9_RX_M2/PWM2_CH1_M1/GPIO4_C3_d	1AE2	I/O	3.3V	DOWN	USBCC_INT_L	USBCC_INT_L
23	SAI4_SCLK_M2/VP2_SYNC_OUT/I2C6_SDA_M3/SPI4_CLK_M0/CAN1_RX_M1/PWM2_CH3_M1/GPIO4_C7_d	AJ1	I/O	3.3V	DOWN	I2C6_SDA_M3	I2C6_SDA_M3
25	SAI4_SDI_M2/VP1_SYNC_OUT/PCIE0_CLKREQ_N_M3/SATA0_ACTLED_M1/I2C6_SCL_M3/SPI4_MISO_M0/CAN1_TX_M1/PWM2_CH2_M1/GPIO4_C6_d	1AE1	I/O	3.3V	DOWN	I2C6_SCL_M3	I2C6_SCL_M3
27	GND		G			GND	GND



接口定义 Interface definition

29	PCIE0_REFCLKN	1N23	O	-		NC	NC
31	PCIE0_REFCLKP	1N22	O	-		NC	NC
33	PCIE0_RXN/SATA0_RXN	R29	I	-		NC	NC
35	PCIE0_RXP/SATA0_RXP	R28	I	-		NC	NC
37	PCIE0_TXN/SATA0_TXN	P28	O	-		NC	NC
39	PCIE0_TXP/SATA0_TXP	P29	O	-		NC	NC
41	GND		G			GND	GND
43	PCIE1_TXP/SATA1_TXP/USB3_OTG1_SSTXP	N28	O	-		NC	NC
45	PCIE1_TXN/SATA1_TXN/USB3_OTG1_SSTXN	N29	O	-		NC	NC
47	PCIE1_RXP/SATA1_RXP/USB3_OTG1_SSRXP	M28	I	-		NC	NC
49	PCIE1_RXN/SATA1_RXN/USB3_OTG1_SSRXN	M29	I	-		NC	NC
51	PCIE1_REFCLKP	1L23	O	-		NC	NC
53	PCIE1_REFCLKN	1M23	O	-		NC	NC
55	GND		G			GND	GND
57	MIPI_DPHY_CSI0_RX_D0P/MIPI_CPHY_CSI_RX_TRIO0_B	AK20	I/O	-		NC	NC
59	MIPI_DPHY_CSI0_RX_D0N/MIPI_CPHY_CSI_RX_TRIO0_A	AL20	I/O	-		NC	NC
61	MIPI_DPHY_CSI0_RX_D1P/MIPI_CPHY_CSI_RX_TRIO1_A	AK21	I/O	-		NC	NC
63	MIPI_DPHY_CSI0_RX_D1N/MIPI_CPHY_CSI_RX_TRIO0_C	AL21	I/O	-		NC	NC



接口定义 Interface definition

65	MIPI_DPHY_CSI0_RX_CLKP/MIPI_CPHY_CSI_RX_TRIO1_C	AK22	I/O	-		NC	NC
67	MIPI_DPHY_CSI0_RX_CLKN/MIPI_CPHY_CSI_RX_TRIO1_B	AL22	I/O	-		NC	NC
69	MIPI_DPHY_CSI0_RX_D2P/MIPI_CPHY_CSI_RX_TRIO2_B	AK23	I/O	-		NC	NC
71	MIPI_DPHY_CSI0_RX_D2N/MIPI_CPHY_CSI_RX_TRIO2_A	AL23	I/O	-		NC	NC
73	MIPI_DPHY_CSI0_RX_D3P/NO_USE	AK24	I/O	-		NC	NC
75	MIPI_DPHY_CSI0_RX_D3N/MIPI_CPHY_CSI_RX_TRIO2_C	AL24	I/O	-		NC	NC
77	GND		G			GND	GND
79	HDMI_TX_SBDN/EDP_TX_AUXN	2U12	O	-		NC	NC
81	HDMI_TX_SBDP/EDP_TX_AUXP	2T12	O	-		NC	NC
83	HDMI_TX_D3N/EDP_TX_D3N	AK26	O	-		NC	NC
85	HDMI_TX_D3P/EDP_TX_D3P	AL26	O	-		NC	NC
87	HDMI_TX_D0N/EDP_TX_D0N	AK27	O	-		NC	NC
89	HDMI_TX_D0P/EDP_TX_D0P	1AE24	O	-		NC	NC
91	HDMI_TX_D1N/EDP_TX_D1N	AL28	O	-		NC	NC
93	HDMI_TX_D1P/EDP_TX_D1P	AK28	O	-		NC	NC
95	HDMI_TX_D2N/EDP_TX_D2N	AK29	O	-		NC	NC
97	HDMI_TX_D2P/EDP_TX_D2P	AJ28	O	-		NC	NC
99	GND		G			GND	GND



接口定义 Interface definition

2	GND		G			GND	GND
4	SPDIF_RX0_M0/FLEXBUS0_CSN_M4/UART2_RX_M1/I2C3_SDA_M0/CAN1_RX_M2/GPIO4_B4_d	B8	I/O	3.3V	DOWN	GPIO4_B4_d	PCIE0_WAKEN_M2
6	SPDIF_TX0_M0/FLEXBUS0_D15_M1/UART2_TX_M1/I2C3_SCL_M0/PCIE0_CLKREQN_M2/CAN1_TX_M2/GPIO4_B5_d	1A4	I/O	3.3V	DOWN	GPIO4_B5_d	PCIE0_CLKREQN_M2
8	SAI4_LRCK_M0/PDM1_CLK0_M1/FLEXBUS0_D14_M1/SPI3_MISO_M2/UART6_RX_M0/I2C4_SDA_M1/CAN0_RX_M2/GPIO4_A6_d	1B5	I/O	3.3V	DOWN	GPIO4_A6_d	UART6_RX_M0
10	SAI4_SCLK_M0/PDM1_SDI3_M1/FLEXBUS0_D13_M1/SPI3_MOSI_M2/UART6_TX_M0/I2C4_SCL_M1/CAN0_TX_M2/GPIO4_A4_d	1C5	I/O	3.3V	DOWN	CAN0_TX_M2	UART6_TX_M0
12	SAI1_SCLK_M0/FLEXBUS1_CSN_M4/SPI3_CSN0_M2/UART5_RTSN_M1/I2C2_SCL_M2/PWM2_CH4_M1/GPIO4_A3_d	1C6	I/O	3.3V	DOWN	BL_PWM1	PCIE1_WAKEN_M2
14	SAI1_LRCK_M0/FLEXBUS1_D12_M1/SPI4_CSN1_M2/UART5_CTSN_M1/I2C2_SDA_M2/PCIE1_CLKREQN_M2/GPIO4_A5_d	1B6	I/O	3.3V	DOWN	GMAC1_RSTn	PCIE1_CLKREQN_M2
16	USB2_OTG0_VBUSDET	2P3	I	3.3V		USB2_OTG0_VBUSDET	USB2_OTG0_VBUSDET
18	USB2_OTG0_ID	2R6	I	1.8V		NC	NC
20	SAI0_MCLK_M1/PDM0_CLK0_M0/UART10_TX_M2/PWM0_CH0_M0/GPIO0_C4_d	1W22	I/O	3.3V	DOWN	GPIO0_C4_d	GPIO0_C4_d
22	SAI0_SDO0_M1/DP_HPDI_M1/UART10_RX_M2/I3C0_SDA_PU_M0/GPIO0_C5_d	1AA22	I/O	3.3V	DOWN	SPK_CTRL	SPK_CTRL
24	SAI0_SDI3_M1/SAI0_SDO1_M1/PDM0_SDI3_M0/I2C4_SDA_M0/GPU_AV5/PWM2_CH0_M0/UART1_RTSEN_M0/GPIO0_D3_d	1AA23	I/O	3.3V	DOWN	GPIO0_D3_d	GPIO0_D3_d
26	SAI0_SDI2_M1/SAI0_SDO2_M1/PDM0_SDI2_M0/I2C4_SCL_M0/CUBIG_AV5/PWM1_CH5_M0/UART1_CTSN_M0/GPIO0_D2_d	1Y22	I/O	3.3V	DOWN	SYS_LED	SYS_LED
28	SAI0_SCLK_M1/I2C3_SCL_M1/SPI0_CSN0_M0/GPIO0_C6_d	1Y21	I/O	3.3V	DOWN	SPI0_CSN0_M0	SPI0_CSN0_M0
30	SAI0_SDI0_M1/PDM0_SDI0_M0/SPI0_MOSI_M0/GPIO0_D0_d	1W23	I/O	3.3V	DOWN	SPI0_MOSI_M0	SPI0_MOSI_M0
32	SAI0_LRCK_M1/I2C3_SDA_M1/SPI0_CLK_M0/GPIO0_C7_d	1Y23	I/O	3.3V	DOWN	SPI0_CLK_M0	SPI0_CLK_M0
34	SAI0_SDI1_M1/SAI0_SDO3_M1/PDM0_SDI1_M0/SPI0_MISO_M0/GPIO0_D1_d	AC28	I/O	3.3V	DOWN	SPI0_MISO_M0	SPI0_MISO_M0



接口定义 Interface definition

36	I2C2_SCL_M0/UART1_TX_M0/NPU_AVS/PWM1_CH4_M0/GPIO0_B7_d	1W24	I/O	3.3V	DOWN	I2C2_SCL_M0	I2C2_SCL_M0
38	I2C2_SDA_M0/UART1_RX_M0/CPULIT_AVS/PWM1_CH3_M0/GPIO0_C0_d	AB29	I/O	3.3V	DOWN	I2C2_SDA_M0	I2C2_SDA_M0
40	REF_CLK1_OUT/I2C1_SCL_M1/UART4_TX_M2/PWM1_CH0_M0/GPIO0_B4_d	AD28	I/O	3.3V	DOWN	GPIO0_B4_d	UART4_TX_M2
42	REF_CLK2_OUT/I2C1_SDA_M1/UART4_RX_M2/PWM1_CH1_M0/GPIO0_B5_d	AD29	I/O	3.3V	DOWN	GPIO0_B5_d	UART4_RX_M2
44	GND		G			GND	GND
46	USB2_OTG1_DP	2T4		-		USB2_HOST1_DP	USB2_HOST1_DP
48	USB2_OTG1_DM	2T5		-		USB2_HOST1_DM	USB2_HOST1_DM
50	USB2_OTG0_DP	AK9		-		USB2_OTG0_DP	USB2_OTG0_DP
52	USB2_OTG0_DM	AL9		-		USB2_OTG0_DM	USB2_OTG0_DM
54	GND		G			GND	GND
56	DP_TX_AUXP	2T2		-		NC	NC
58	DP_TX_AUXN	2T3		-		NC	NC
60	USB3_OTG0_SSRX1P/DP_TX_D0P	AK10	I/O	-		USB3_OTG0_SSRX1P/DP_TX_D0P	USB3_OTG0_SSRX1P/DP_TX_D0P
62	USB3_OTG0_SSRX1N/DP_TX_D0N	AL10	I/O	-		USB3_OTG0_SSRX1N/DP_TX_D0N	USB3_OTG0_SSRX1N/DP_TX_D0N
64	USB3_OTG0_SSTX1P/DP_TX_D1P	AL11	I/O	-		USB3_OTG0_SSTX1P/DP_TX_D1P	USB3_OTG0_SSTX1P/DP_TX_D1P
66	USB3_OTG0_SSTX1N/DP_TX_D1N	AK11	I/O	-		USB3_OTG0_SSTX1N/DP_TX_D1N	USB3_OTG0_SSTX1N/DP_TX_D1N
68	USB3_OTG0_SSRX2P/DP_TX_D2P	AK12	I/O	-		USB3_OTG0_SSRX2P/DP_TX_D2P	USB3_OTG0_SSRX2P/DP_TX_D2P
70	USB3_OTG0_SSRX2N/DP_TX_D2N	AL12	I/O	-		USB3_OTG0_SSRX2N/DP_TX_D2N	USB3_OTG0_SSRX2N/DP_TX_D2N



接口定义 Interface definition

72	USB3_OTG0_SSTX2P/DP_TX_D3P	AK13	I/O	-		USB3_OTG0_SSTX2P/DP_TX_D3P	USB3_OTG0_SSTX2P/DP_TX_D3P
74	USB3_OTG0_SSTX2N/DP_TX_D3N	AL13	I/O	-		USB3_OTG0_SSTX2N/DP_TX_D3N	USB3_OTG0_SSTX2N/DP_TX_D3N
76	GND		G			GND	GND
78	MIPI_DPHY_CSI1_RX_D2P/MIPI_DPHY_CSI2_RX_D0P	AG29	I/O	-		MIPI_DPHY_CSI1_RX_D2P/MIPI_DPHY_CSI2_RX_D0P	MIPI_DPHY_CSI1_RX_D2P/MIPI_DPHY_CSI2_RX_D0P
80	MIPI_DPHY_CSI1_RX_D2N/MIPI_DPHY_CSI2_RX_D0N	AG28	I/O	-		MIPI_DPHY_CSI1_RX_D2N/MIPI_DPHY_CSI2_RX_D0N	MIPI_DPHY_CSI1_RX_D2N/MIPI_DPHY_CSI2_RX_D0N
82	MIPI_DPHY_CSI1_RX_D3P/MIPI_DPHY_CSI2_RX_D1P	AH29	I/O	-		MIPI_DPHY_CSI1_RX_D3P/MIPI_DPHY_CSI2_RX_D1P	MIPI_DPHY_CSI1_RX_D3P/MIPI_DPHY_CSI2_RX_D1P
84	MIPI_DPHY_CSI1_RX_D3N/MIPI_DPHY_CSI2_RX_D1N	AH28	I/O	-		MIPI_DPHY_CSI1_RX_D3N/MIPI_DPHY_CSI2_RX_D1N	MIPI_DPHY_CSI1_RX_D3N/MIPI_DPHY_CSI2_RX_D1N
86	MIPI_DPHY_CSI2_RX_CLKP	1AD21	I/O	-		NC	NC
88	MIPI_DPHY_CSI2_RX_CLKN	1AD22	I/O	-		NC	NC
90	MIPI_DPHY_CSI1_RX_CLKP	1AC22	I/O	-		MIPI_DPHY_CSI1_RX_CLKP	MIPI_DPHY_CSI1_RX_CLKP
92	MIPI_DPHY_CSI1_RX_CLKN	1AC23	I/O	-		MIPI_DPHY_CSI1_RX_CLKN	MIPI_DPHY_CSI1_RX_CLKN
94	MIPI_DPHY_CSI1_RX_D1P	AF29	I/O	-		MIPI_DPHY_CSI1_RX_D1P	MIPI_DPHY_CSI1_RX_D1P
96	MIPI_DPHY_CSI1_RX_D1N	AF28	I/O	-		MIPI_DPHY_CSI1_RX_D1N	MIPI_DPHY_CSI1_RX_D1N
98	MIPI_DPHY_CSI1_RX_D0P	AE29	I/O	-		MIPI_DPHY_CSI1_RX_D0P	MIPI_DPHY_CSI1_RX_D0P
100	MIPI_DPHY_CSI1_RX_D0N	AE28	I/O	-		MIPI_DPHY_CSI1_RX_D0N	MIPI_DPHY_CSI1_RX_D0N
PIN	ICORE-3576Q38 pin definition (J2)	RK3576 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
1	VCC5V0_SYS_S5		P	5.0V		VCC5V0_SYS Normal: 5V/150mA Max.:5V/1500mA Min.:5V/5mA	VCC5V0_SYS INPUT Voltage 5.0V +/-5%
3	VCC5V0_SYS_S5		P	5.0V			



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5	VCC5V0_SYS_S5		P	5.0V		VCC5V0_SYS	VCC5V0_SYS INPUT Voltage 5.0V +/-5%
7	VCC5V0_SYS_S5		P	5.0V		Normal: 5V/150mA Max.:5V/1500mA Min.:5V/5mA	
9	VCC5V0_SYS_S5		P	5.0V			
11	GND		G	GND		GND	GND
13	GND		G	GND			
15	GND		G	GND			
17	GND		G	GND			
19	PWRON_L		I	5.0V		PWRON_L	PWERON KEY INPUT,Active L
21	VCCA_3V3_S0 (OUTPUT)		P	3.3V		VCCA_3V3_S0	VCCA_3V3_S0 OUTPUT (3.3V, Total Max:100mA)
23	VDC_EXT		I	5.0V		VDC_EXT	PMIC_EN INPUT, Active H
25	VCC_3V3_S3 (OUTPUT)		P	3.3V		VCC_3V3_S3	VCC_3V3_S3 OUTPUT (3.3V, Total Max:500mA)
27	VCC_3V3_S3 (OUTPUT)		P	3.3V		VCC_3V3_S3	
29	RESET_L	W28	I/O	1.8V		RESET_L	SYSTEM RESET INPUT, Active L
31	PMIC_EXT_EN_OUT		O	5.0V		NC	NC
33	REF_CLK0_OUT/AUPLL_CLK_IN_M0/GPIO0_A0_d	V29	I/O	1.8V	DOWN	RTC_INT_L	RTC_INT INPUT, Active L
35	VI_CIF_CLKO/ETH1_PPSCLK_M0/ETH0_RXCTL_M1/SAI3_SDO_M2/SPDIF_RX1_M1/UART3_CTSN_M0/SPI3_MISO_M0/CAN1_TX_M3/MIPI_TE_M1/GPIO3_A2_d	1A20	I/O	VCCIO4 (1.8V)	DOWN	NC	NC
37	VI_CIF_CLKI/ETH1_PTP_REFCLK_M0/ETH0_RXD1_M1/SAI3_SDI_M2/SPDIF_TX1_M1/UART3_RTSN_M0/SPI3_CSNO_M0/CAN1_RX_M3/GPIO3_A3_d	1A19	I/O	VCCIO4 (1.8V)	DOWN	NC	NC



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39	VI_CIF_VSYNC/ETH1_PPSTRIG_M0/ETH0_MDC_M1/SAI3_LRCK_M2/UART3_RX_M0/SPI3_MOSI_M0/I2C7_SDA_M1/GPIO3_A1_d	1B18	I/O	VCCIO4 (1.8V)	DOWN	NC	NC
41	VI_CIF_HREF/ETH0_MDIO_M1/SAI3_SCLK_M2/UART3_TX_M0/SPI3_CLK_M0/I2C7_SCL_M1/GPIO3_A0_d	1D16	I/O	VCCIO4 (1.8V)	DOWN	WIFI_PWR_EN	WIFI_PWR_EN
43	GND		G	GND		GND	GND
45	SDMMC0_D2/FSPI1_D2_M0/DSM_AUD_RP_M0/SAI3_LRCK_M3/JTAG_TCK_M0/UART5_RTSN_M2/SPI0_CSN1_M1/CAN1_RX_M0/I3C1_SCL_M1/GPIO2_A2_d	A23	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_D2 (To SD CARD)	SDMMC0_D2
47	SDMMC0_D3/FSPI1_D3_M0/DSM_AUD_RN_M0/SAI3_SDI_M3/JTAG_TMS_M0/UART5_CTSN_M2/CAN1_TX_M0/I3C1_SDA_M1/GPIO2_A3_d	B23	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_D3 (To SD CARD)	SDMMC0_D3
49	SDMMC0_CLK/FSPI1_CLK_M0/SAI3_SCLK_M3/TEST_CLK_OUT/UART5_TX_M2/I2C5_SCL_M0/SPI0_CLK_M1/I3C1_SDA_PU_M1/GPIO2_A5_d	1B21	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_CLK (To SD CARD)	SDMMC0_CLK (Core board series resistance 22R)
51	SDMMC0_CMD/FSPI1_CSN0_M0/SAI3_SDO_M3/UART5_RX_M2/I2C5_SDA_M0/SPI0_CSN0_M1/PWM2_CH4_M0/GPIO2_A4_d	1A21	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_CMD (To SD CARD)	SDMMC0_CMD
53	SDMMC0_D1/FSPI1_D1_M0/DSM_AUD_LN_M0/SAI3_MCLK_M3/UART0_TX_M1/UART7_TX_M2/I2C8_SDA_M0/SPI0_MISO_M1/CAN0_TX_M0/PWM2_CH3_M0/GPIO2_A1_d	B25	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_D1 (To SD CARD)	SDMMC0_D1
55	SDMMC0_D0/FSPI1_D0_M0/DSM_AUD_LP_M0/UART0_RX_M1/UART7_RX_M2/I2C8_SCL_M0/SPI0_MOSI_M1/CAN0_RX_M0/PWM2_CH2_M0/GPIO2_A0_d	B24	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_D0 (To SD CARD)	SDMMC0_D0
57	GND		G			GND	GND
59	SDMMC0_PWREN/SDMMC1_DET_N_M2/HDMI_TX_HPDIN_M1/EDP_TX_HPDIN_M1/PWM1_CH2_M0/GPIO0_B6_d	1Y24	I/O	3.3V	DOWN	SDMMC0_PWREN_H	SDMMC0_PWREN_H
61	SDMMC0_DET_N/SPI2_CSN1_M0/GPIO0_A7_u	1U21	I/O	1.8V	UP	SDMMC0_DET_L	SDMMC0_DET, Active L
63	SPDIF_RX0_M1/CAM_CLK1_OUT_M0/SAI4_LRCK_M1/DSMC_INT0/FLEXBUS0_D13_M0/FLEXBUS1_D14_M0/FLEXBUS1_CSN_M3/UART3_TX_M1/SPI1_CSN1_M2/I2C7_SCL_M2/MIPI_TE_M2/GPIO4_A0_d	1B12	I/O	VCCIO5 (3.3V)	DOWN	NC	NC
65	VO_POST_EMPTY/SPDIF_TX0_M1/CAM_CLK2_OUT_M0/SAI4_SDO_M1/DSMC_INT2/FLEXBUS0_D14_M0/FLEXBUS1_D13_M0/FLEXBUS0_CSN_M1/UART3_RX_M1/I2C7_SDA_M2/GPIO4_A1_d	1B7	I/O	VCCIO5 (3.3V)	DOWN	GPIO4_A1_d	GPIO4_A1_d
67	VO_LCDC_CLK/VO_EBC_SDOE/CAM_CLK0_OUT_M0/SAI4_SCLK_M1/DSMC_RESETN/FLEXBUS0_D15_M0/FLEXBUS1_D12_M0/FLEXBUS1_CSN_M1/UART5_RTSN_M0/SPI3_CSN1_M1/PWM2_CH7_M3/GPIO3_D7_d	1E7	I/O	VCCIO5 (3.3V)	DOWN	CAM0_MCLK	CAM0_MCLK OUTPUT



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69	GND		G			GND	GND
71	ETH1_RXD0_M0/SAI4_SDO_M3/UART4_RX_M0/I2C6_SDA_M2/PWM2_CH1_M2/GPIO2_D1_d	1A16	I/O	VCCIO4 (1.8V)	DOWN	UART4_RX_M0	UART4_RX_M0
73	CAM_CLK0_OUT_M1/ETH1_RXD1_M0/SAI4_MCLK_M3/UART6_TX_M1/I3C1_SCL_M0/PWM2_CH2_M2/GPIO2_D2_d	B17	I/O	VCCIO4 (1.8V)	DOWN	WIFI_REG_ON_H	WIFI_REG_ON_H
75	ETH1_RXCTL_M0/UART6_RX_M1/I3C1_SDA_M0/PWM2_CH3_M2/GPIO2_D3_d	B18	I/O	VCCIO4 (1.8V)	DOWN	HOST_WAKE_BT_H	HOST_WAKE_BT_H
77	VI_CIF_D4/ETH1_RXD3_M0/ETH0_PPCLK_M1/SAI2_MCLK_M1/PDM1_CLK1_M0/UART9_TX_M0/SPI1_CSN1_M1/PWM1_CH1_M2/GPIO2_C1_d	1A15	I/O	VCCIO4 (1.8V)	DOWN	BT_WAKE_HOST_H	BT_WAKE_HOST_H
79	VI_CIF_D5/ETH1_RXD2_M0/ETH0_PTP_REFCLK_M1/PDM1_SDI1_M0/UART9_RX_M0/PWM1_CH0_M2/GPIO2_C0_d	A17	I/O	VCCIO4 (1.8V)	DOWN	WIFI_WAKE_HOST_H	WIFI_WAKE_HOST_H
81	VI_CIF_D3/ETH1_RXCLK_M0/ETH0_PPSTRIG_M1/SAI2_SCLK_M1/PDM1_SDI2_M0/UART11_CTSN_M1/SPI1_MOSI_M1/PWM1_CH2_M2/GPIO2_C2_d	1D15	I/O	VCCIO4 (1.8V)	DOWN	HOST_WAKE_WIFI_H	HOST_WAKE_WIFI_H
83	CAM_CLK2_OUT_M1/ETH1_MCLK_M0/ETH_CLK0_25M_OUT_M1/SAI0_SDO3_M0/SPDIF_TX0_M2/UART9_CTSN_M0/SPI3_CSN1_M0/PWM2_CH7_M2/GPIO2_D7_d	1E15	I/O	VCCIO4 (1.8V)	DOWN	GPIO2_D7_d	GPIO2_D7_d
85	VI_CIF_D1/ETH1_TXD3_M0/SAI2_SDO_M1/PDM1_SDI0_M0/UART11_TX_M1/SPI1_CSN0_M1/PWM1_CH3_M2/GPIO2_C4_d	1A13	I/O	VCCIO4 (1.8V)	DOWN	GPIO2_C4_d	GPIO2_C4_d
87	VI_CIF_D0/ETH1_TXCLK_M0/SAI2_SDI_M1/PDM1_CLK0_M0/UART11_RX_M1/SPI1_CLK_M1/PWM1_CH4_M2/GPIO2_C5_d	1C15	I/O	VCCIO4 (1.8V)	DOWN	GPIO2_C5_d	GPIO2_C5_d
89	VI_CIF_D2/ETH1_TXD2_M0/SAI2_LRCK_M1/PDM1_SDI3_M0/UART11_RTSN_M1/SPI1_MISO_M1/PWM0_CH0_M2/GPIO2_C3_d	A15	I/O	VCCIO4 (1.8V)	DOWN	GPIO2_C3_d	GPIO2_C3_d
91	ETH1_TXD1_M0/SAI4_LRCK_M3/UART4_RTSN_M0/I2C5_SDA_M2/PWM0_CH1_M2/GPIO2_C7_d	B15	I/O	VCCIO4 (1.8V)	DOWN	UART4_RTSN_M0	UART4_RTSN_M0
93	ETH1_TXD0_M0/SAI4_SCLK_M3/UART4_CTSN_M0/I2C5_SCL_M2/PWM1_CH5_M2/GPIO2_C6_d	1A14	I/O	VCCIO4 (1.8V)	DOWN	UART4_CTSN_M0	UART4_CTSN_M0
95	ETH1_TXCTL_M0/SAI4_SDI_M3/UART4_TX_M0/I2C6_SCL_M2/PWM2_CH0_M2/GPIO2_D0_d	B16	I/O	VCCIO4 (1.8V)	DOWN	UART4_TX_M0	UART4_TX_M0
97	ISP_PRELIGHT_TRIG_M0/ETH1_MDC_M0/UART6_RTSN_M1/I2C9_SDA_M2/PWM2_CH4_M2/GPIO2_D4_d	1B13	I/O	VCCIO4 (1.8V)		I2C9_SDA_M2	I2C9_SDA_M2
99	ISP_FLASH_TRIGOUT_M0/ETH1_MDIO_M0/UART6_CTSN_M1/I2C9_SCL_M2/PWM2_CH5_M2/GPIO2_D5_d	1B15	I/O	VCCIO4 (1.8V)		I2C9_SCL_M2	I2C9_SCL_M2



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2	VCC5V0_SYS_S5		P	5.0V		VCC5V0_SYS Normal: 5V/150mA Max.:5V/1500mA Sleep:5V/5mA	VCC5V0_SYS INPUT Voltage 5.0V +/-5%
4	VCC5V0_SYS_S5		P	5.0V			
6	VCC5V0_SYS_S5		P	5.0V			
8	VCC5V0_SYS_S5		P	5.0V			
10	GND		G	GND		GND	GND
12	GND		G	GND			
14	GND		G	GND			
16	GND		G	GND			
18	VCCA_RK806S		P	5.0V		VCCA_RK806S (此信号为预留, 实际核心板默认内部供电, 此脚NC处理)	RK806S startup circuit power supply
20	VCC_1V8_S3 (OUTPUT)	2K11	P	1.8V		VCC_1V8_S3	VCC_1V8_S3 OUTPUT (1.8V, Total Max:500mA)
22	GND		G			GND	GND
24	VI_CIF_D8/SDMMC1_DET_N_M1/ETH0_RXCLK_M1/SAI0_MCLK_M0/PDM0_CLK0_M3/UART7_RTSN_M0/SPI4_MISO_M3/SATA1_ACTLED_M0/GPIO2_B5_d	1C18	I/O	VCCIO4 (1.8V)	DOWN	NC	NC
26	VI_CIF_D10/SDMMC1_CLK_M1/ETH0_TXCLK_M1/SAI0_SDO2_M0/PDM0_CLK1_M3/UART1_RTSN_M1/SPI4_CLK_M3/PCIE1_CLKREQ_N_M0/GPIO2_B3_d	1B16	I/O	VCCIO4 (1.8V)	DOWN	NC	NC
28	VI_CIF_D11/SDMMC1_CMD_M1/ETH0_TXD3_M1/SAI0_SDI2_M0/PDM0_SDI1_M3/UART1_CTSN_M1/SPI4_CSN0_M3/PCIE0_CLKREQ_N_M0/GPIO2_B2_d	1A17	I/O	VCCIO4 (1.8V)	DOWN	NC	NC
30	VI_CIF_D9/SDMMC1_PWREN_M1/ETH0_TXD2_M1/SAI0_SDI3_M0/PDM0_SDI0_M3/UART7_CTSN_M0/SPI4_MOSI_M3/SATA0_ACTLED_M0/GPIO2_B4_d	A19	I/O	VCCIO4 (1.8V)	DOWN	NC	NC
32	GND		G			GND	GND



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34	ETH1_RXD2_M1/SDMMC1_D0_M0/SAI3_SCLK_M1/I2C9_SDA_M1/SPI1_CLK_M0/PCIE1_CLKREQN_M1/PWM1_CH0_M1/GPIO1_B4_d	A28	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_D0_M0 (To SDIO WIFI)	SDMMC1_D0_M0
36	ETH1_TXCLK_M1/SDMMC1_CLK_M0/SAI3_MCLK_M1/PDM0_CLK0_M2/UART3_RX_M2/GPIO1_C1_d	1B22	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_CLK_M0 (To SDIO WIFI)	SDMMC1_CLK_M0
38	ETH1_TXD2_M1/SDMMC1_D3_M0/SAI3_SDI_M1/UART3_RTSN_M2/SPI1_CSN0_M0/GPIO1_B7_d	A27	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_D3_M0 (To SDIO WIFI)	SDMMC1_D3_M0
40	ETH1_RXD3_M1/SDMMC1_D1_M0/SAI3_LRCK_M1/I2C9_SCL_M1/SPI1_MOSI_M0/PWM1_CH1_M1/GPIO1_B5_d	B27	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_D1_M0 (To SDIO WIFI)	SDMMC1_D1_M0
42	ETH1_RXCLK_M1/SDMMC1_D2_M0/SAI3_SDO_M1/UART3_CTSN_M2/SPI1_MISO_M0/PCIE0_CLKREQN_M1/GPIO1_B6_d	1A23	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_D2_M0 (To SDIO WIFI)	SDMMC1_D2_M0
44	ETH1_TXD3_M1/SDMMC1_CMD_M0/PDM0_SDI2_M2/UART3_TX_M2/SPI1_CSN1_M0/PWM0_CH0_M1/GPIO1_C0_d	B26	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_CMD_M0 (To SDIO WIFI)	SDMMC1_CMD_M0
46	GND		G			GND	GND
48	ETH1_PPCLK_M1/SDMMC1_PWREN_M0/FSPI1_RSTN_M1/FSPI1_CSN1_M1/UART4_RTSN_M1/I2C6_SCL_M1/SPI2_CSN1_M1/PWM1_CH2_M1/GPIO1_C2_u	B29	I/O	VCCIO3 (1.8V)	UP	NC	NC
50	ETH1_PPSTRIG_M1/SDMMC1_DET_N_M0/FSPI1_CSN0_M1/UART4_CTSN_M1/I2C6_SDA_M1/SPI2_CSN0_M1/GPIO1_C3_u	1C23	I/O	VCCIO3 (1.8V)	UP	NC	NC
52	ETH1_RXD0_M1/FSPI1_D3_M1/PDM0_SDI1_M2/UART2_RX_M0/I2C8_SDA_M1/SATA_CPDET/GPIO1_C7_d	1C22	I/O	VCCIO3 (1.8V)	DOWN	NC	NC
54	ETH1_TXCTL_M1/FSPI1_D2_M1/PDM0_SDI0_M2/UART2_TX_M0/I2C8_SCL_M1/SATA_CPPOD/GPIO1_C6_d	A26	I/O	VCCIO3 (1.8V)	DOWN	NC	NC
56	VO_LCDC_D5/VO_EBC_SDDO5/SAI1_LRCK_M1/DSMC_DATA3/FLEXBUS1_D5/UART8_TX_M0/SPI1_MOSI_M2/GPIO3_C6_d	1D7	I/O	VCCIO5 (3.3V)	DOWN	UART8_TX_M0	UART8_TX_M0
58	VO_LCDC_D4/VO_EBC_SDDO4/SAI1_SCLK_M1/DSMC_DATA2/FLEXBUS1_D4/UART8_RTSN_M0/SPI1_CLK_M2/GPIO3_C7_d	1C7	I/O	VCCIO5 (3.3V)	DOWN	UART8_RTSN_M0	UART8_RTSN_M0
60	VO_LCDC_D6/VO_EBC_SDDO6/SAI1_SDO0_M1/DSMC_DATA4/FLEXBUS1_D6/UART8_RX_M0/SPI1_MISO_M2/PWM2_CH2_M3/GPIO3_C5_d	1B9	I/O	VCCIO5 (3.3V)	DOWN	UART8_RX_M0	UART8_RX_M0
62	VO_LCDC_D3/VO_EBC_SDDO3/SAI1_MCLK_M1/DSMC_DATA1/FLEXBUS1_D3/UART8_CTSN_M0/SPI1_CSN0_M2/PWM2_CH3_M3/GPIO3_D0_d	1C12	I/O	VCCIO5 (3.3V)	DOWN	UART8_CTSN_M0	UART8_CTSN_M0
64	VI_CIF_D6/ETH0_RXD2_M1/SAI0_LRCK_M0/UART7_RX_M0/UART8_CTSN_M1/I2C8_SDA_M2/GPIO2_B7_d	B21	I/O	VCCIO4 (1.8V)	DOWN	NC	NC
66	VI_CIF_D7/ETH1_PTP_REFCLK_M1/ETH0_RXD3_M1/SAI0_SCLK_M0/UART7_TX_M0/UART8_RTSN_M1/I2C8_SCL_M2/GPIO2_B6_d	A21	I/O	VCCIO4 (1.8V)	DOWN	NC	NC



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68	GND		G			GND	GND
70	VO_LCDC_D2/VO_EBC_SDDO2/ETH0_RXCLK_M0/SAI2_MCLK_M2/DSMC_CSN2/FLEXBUS0_D11/FLEXBUS1_CSN_M2/SPI4_CLK_M1/I3C1_SDA_PU_M2/GPIO3_D1_d	1A12	I/O	VCCIO5 (3.3V)	DOWN	I2S0_MCLK_M0	I2S0_MCLK_M0
72	VO_LCDC_D17/VO_EBC_SDCE1/ETH0_RXD0_M0/PDM1_SDI1_M2/DSMC_DATA11/FLEXBUS0_D3/UART9_RX_M1/I2C8_SDA_M3/GPIO3_B2_d	A13	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_RXD0_M0	GMAC0_RXD0_M0
74	VO_LCDC_D20/VO_EBC_VCOM/ETH0_RXCTL_M0/PDM1_CLK1_M2/DSMC_DATA13/FLEXBUS0_D5/UART1_TX_M2/UART10_RTSN_M0/GPIO3_A7_d	B13	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_RXCTL_M0	GMAC0_RXCTL_M0
76	VO_LCDC_D18/VO_EBC_SDCE2/ETH0_RXD1_M0/PDM1_CLK0_M2/DSMC_DATA12/FLEXBUS0_D4/UART10_TX_M0/SPI4_CSN0_M1/PWM1_CH3_M3/GPIO3_B1_d	1A11	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_RXD1_M0	GMAC0_RXD1_M0
78	VO_LCDC_D0/VO_EBC_SDDO0/ETH0_RXD2_M0/SAI2_SDO_M2/DSMC_CSN0/FLEXBUS1_D2/UART2_CTSN_M2/I3C1_SCL_M2/PWM2_CH5_M3/GPIO3_D3_d	B12	I/O	VCCIO5 (3.3V)	DOWN	I2S0_SDO0_M0	I2S0_SDO0_M0
80	VO_LCDC_D1/VO_EBC_SDDO1/ETH0_RXD3_M0/SAI2_SDI_M2/DSMC_CSN3/FLEXBUS0_D12/FLEXBUS1_D15_M0/FLEXBUS0_CSN_M3/UART2_RTSN_M2/SPI4_CSN1_M1/I3C1_SDA_M2/PWM2_CH4_M3/GPIO3_D2_d	1A10	I/O	VCCIO5 (3.3V)	DOWN	I2S0_SDI0_M0	I2S0_SDI0_M0
82	VO_LCDC_D19/VO_EBC_SDCE3/ETH0_MCLK_M0/SAI4_MCLK_M1/DSMC_CSN1/FLEXBUS0_D8/UART10_RX_M0/SPI2_MOSI_M2/PWM0_CH0_M3/GPIO3_B0_d	B14	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_MCLK_M0	GMAC0_MCLK_M0
84	VO_LCDC_D13/VO_EBC_SDDO13/ETH0_TXCLK_M0/DSMC_DQS1/FLEXBUS0_CLK/SPI3_CSN0_M1/PWM0_CH1_M3/GPIO3_B6_d	B11	I/O	VCCIO5 (3.3V)	DOWN	PHY_INT/GPIO0_B3	PHY_INT/GPIO0_B3
86	VO_LCDC_D16/VO_EBC_SDCE0/ETH0_TXCTL_M0/PDM1_SDI0_M2/DSMC_DATA10/FLEXBUS0_D2/UART9_TX_M1/I2C8_SCL_M3/GPIO3_B3_d	A11	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_TXCTL_M0	GMAC0_TXCTL_M0
88	VO_LCDC_D14/VO_EBC_SDDO14/ETH0_TXD0_M0/SPDIF_TX1_M0/DSMC_DATA8/FLEXBUS0_D0/UART9_CTSN_M1/PWM1_CH5_M3/GPIO3_B5_d	1A9	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_TXD0_M0	GMAC0_TXD0_M0
90	VO_LCDC_D8/VO_EBC_SDDO8/ETH0_TXD2_M0/SAI2_LRCK_M2/DSMC_INT3/FLEXBUS0_D10/FLEXBUS0_CSN_M2/UART11_CTSN_M0/SPI4_MOSI_M1/I2C9_SDA_M3/PWM2_CH1_M3/GPIO3_C3_d	1A8	I/O	VCCIO5 (3.3V)	DOWN	I2S0_LRCK_TX_M0	I2S0_LRCK_TX_M0
92	VO_LCDC_D15/VO_EBC_SDDO15/ETH0_TXD1_M0/SPDIF_RX1_M0/DSMC_DATA9/FLEXBUS0_D1/UART9_RTSN_M1/PWM1_CH4_M3/GPIO3_B4_d	B10	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_TXD1_M0	GMAC0_TXD1_M0
94	VO_LCDC_D9/VO_EBC_SDDO9/ETH0_TXD3_M0/SAI2_SCLK_M2/DSMC_INT1/FLEXBUS0_D9/UART11_RTSN_M0/SPI4_MISO_M1/I2C9_SCL_M3/PWM2_CH0_M3/GPIO3_C2_d	B9	I/O	VCCIO5 (3.3V)	DOWN	I2S0_SCLK_TX_M0	I2S0_SCLK_TX_M0
96	SAI1_SDO2_M0/SAI1_SDI2_M0/PDM1_SDI2_M1/FLEXBUS1_D14_M1/SPI4_MOSI_M2/UART5_RX_M1/UART6_CTSN_M0/UART2_CTSN_M1/GPIO4_B1_d	B7	I/O	3.3V	DOWN	GMAC0_RSTn	GMAC0_RSTn
98	VO_LCDC_D21/VO_EBC_GDOE/ETH0_MDC_M0/PDM1_SDI2_M2/DSMC_DATA14/FLEXBUS0_D6/UART1_RX_M2/UART10_CTSN_M0/PWM1_CH2_M3/GPIO3_A6_d	1A7	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_MDC_M0	GMAC0_MDC_M0



接口定义 Interface definition

100	VO_LCDC_D22/VO_EBC_GDSP/ETH0_MDIO_M0/PDM1_SDI3_M2/DSMC_DATA15/FLEXBUS0_D7/UART1_RTSM2/SPI2_CSN1_M2/PWM1_CH1_M3/GPIO3_A5_d	A9	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_MDIO_M0	GMAC0_MDIO_M0
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