

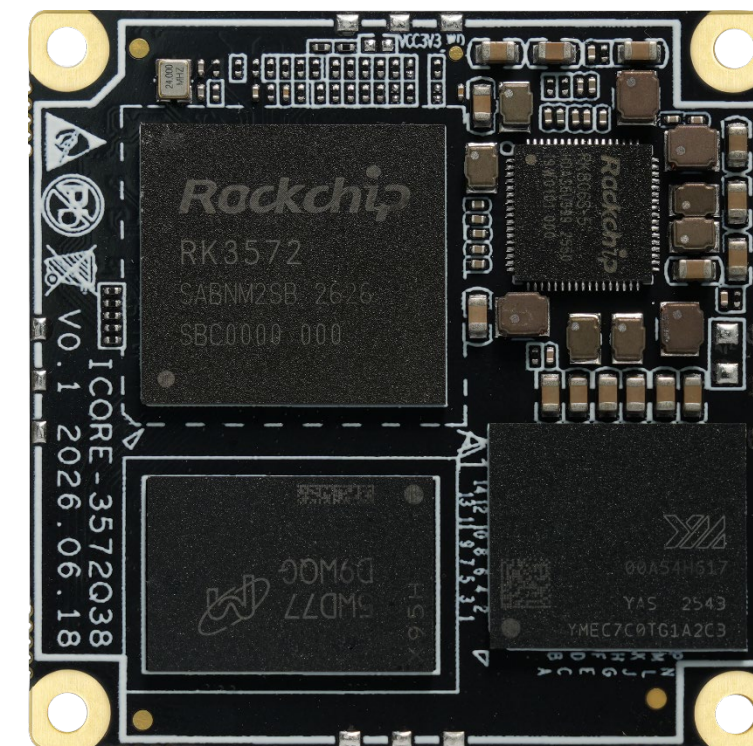


ICORE-3572Q38

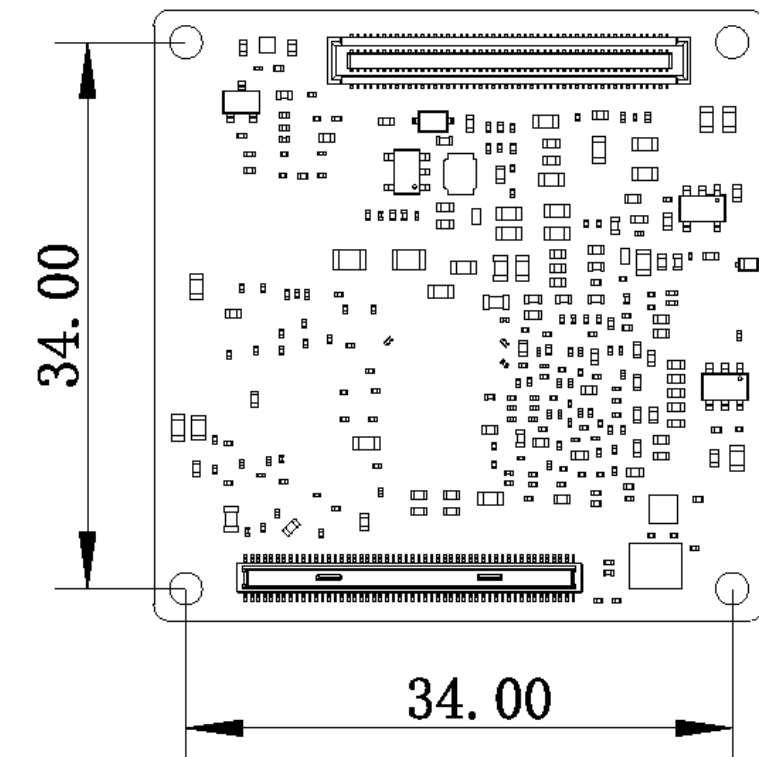
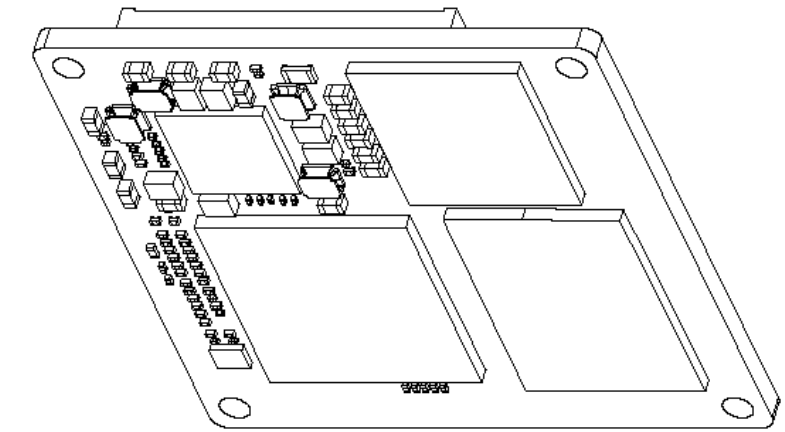
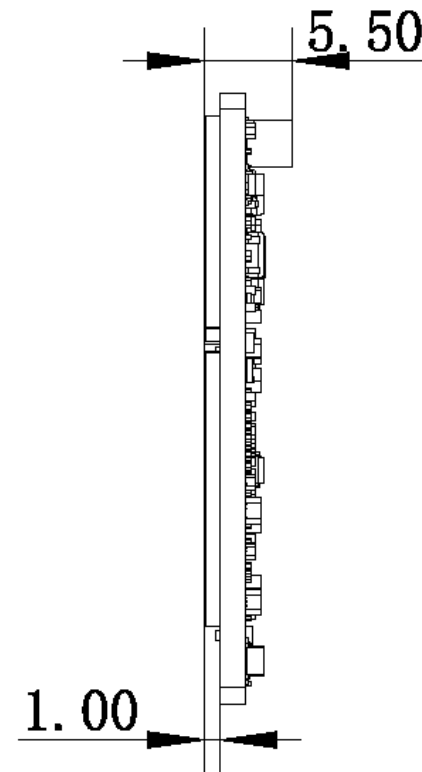
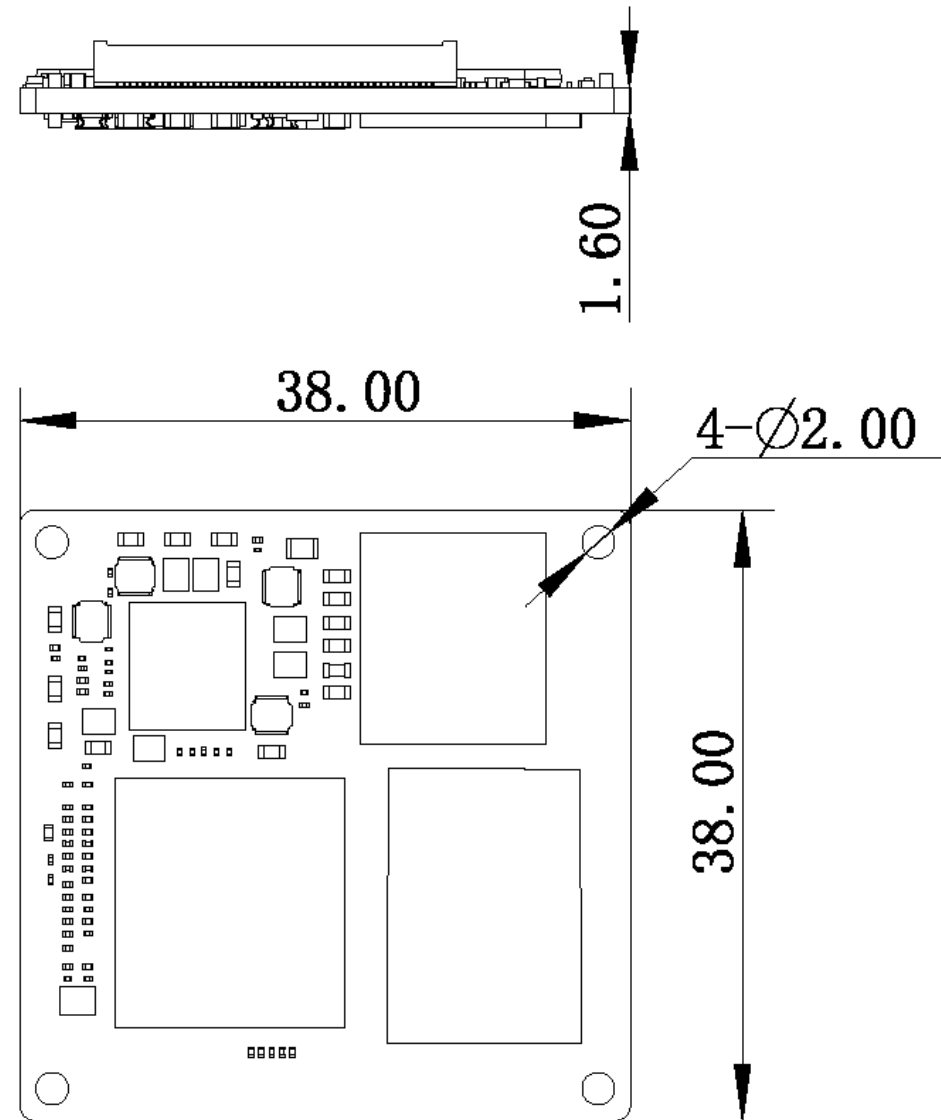
■ RK3572 SoM

V0.1 2026-9-14

FIREFLY TECHNOLOGY



Dimension





Interface definition

PIN	iCORE-3572Q38 pin definition (J1)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
29	PCIE1_REFCLKN	1H22	O	-		NC	PCIE1_REFCLKN
31	PCIE1_REFCLKP	1H21	O	-		NC	PCIE1_REFCLKP
33	PCIE1_RXN/SATA1_RXN	K21	I	-		NC	PCIE1_RXN/SATA1_RXN
35	PCIE1_RXP/SATA1_RXP	K33	I	-		NC	PCIE1_RXP/SATA1_RXP
37	PCIE1_TXN/SATA1_TXN	L32	O	-		NC	PCIE1_TXN/SATA1_TXN
39	PCIE1_TXP/SATA1_TXP	L33	O	-		NC	PCIE1_TXP/SATA1_TXP
41	PCIE0_REFCLKP	1F21	G			GND	PCIE0_REFCLKP_M1
43	PCIE0_TXP_M1/SATA0_TXP_M1/USB_DRD1_SSTXP	G32	O	-		NC	PCIE0_TXP_M1/SATA0_TXP_M1/USB_DRD1_SSTXP
45	PCIE0_TXN_M1/SATA0_TXN_M1/USB_DRD1_SSTXN	G33	O	-		NC	PCIE0_TXN_M1/SATA0_TXN_M1/USB_DRD1_SSTXN
47	PCIE0_RXP_M1/SATA0_RXP_M1/USB_DRD1_SSRXP	H32	I	-		NC	PCIE0_RXP_M1/SATA0_RXP_M1/USB_DRD1_SSRXP
49	PCIE0_RXN_M1/SATA0_RXN_M1/USB_DRD1_SSRXN	H33	I	-		NC	PCIE0_RXN_M1/SATA0_RXN_M1/USB_DRD1_SSRXN
51	MIPI_DPHY_CSI3_RX_CLKP	1B12	O	-		NC	MIPI_DPHY_CSI3_RX_CLKP
53	MIPI_DPHY_CSI3_RX_CLKN	1C12	O	-		NC	MIPI_DPHY_CSI3_RX_CLKN
55	GND2		G			GND	GND
57	MIPI_DPHY_CSI2_RX_D0P	A13	I/O	-		NC	MIPI_DPHY_CSI2_RX_D0P
59	MIPI_DPHY_CSI2_RX_D0N	B13	I/O	-		NC	MIPI_DPHY_CSI2_RX_D0N
61	MIPI_DPHY_CSI2_RX_D1P	1B10	I/O	-		NC	MIPI_DPHY_CSI2_RX_D1P

