

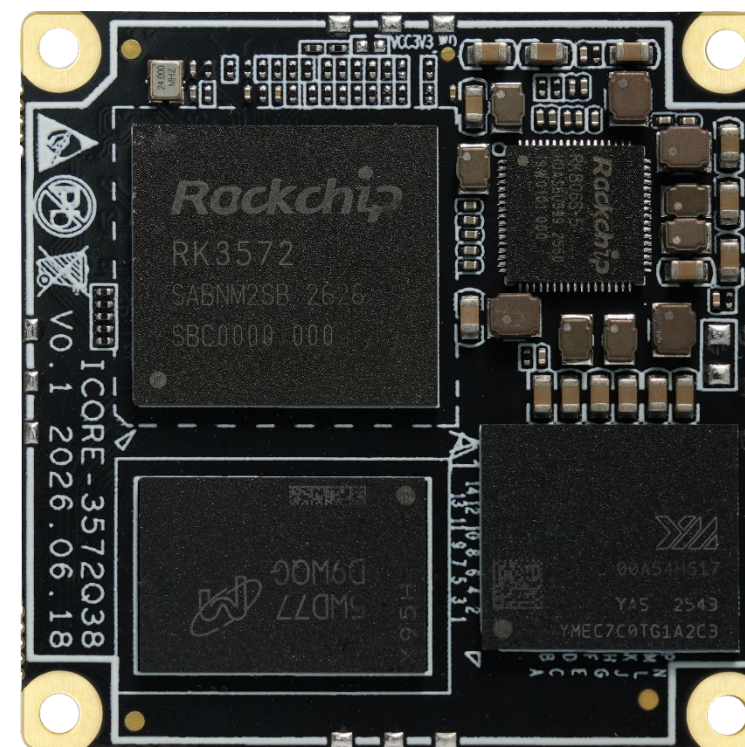


ICORE-3572Q38

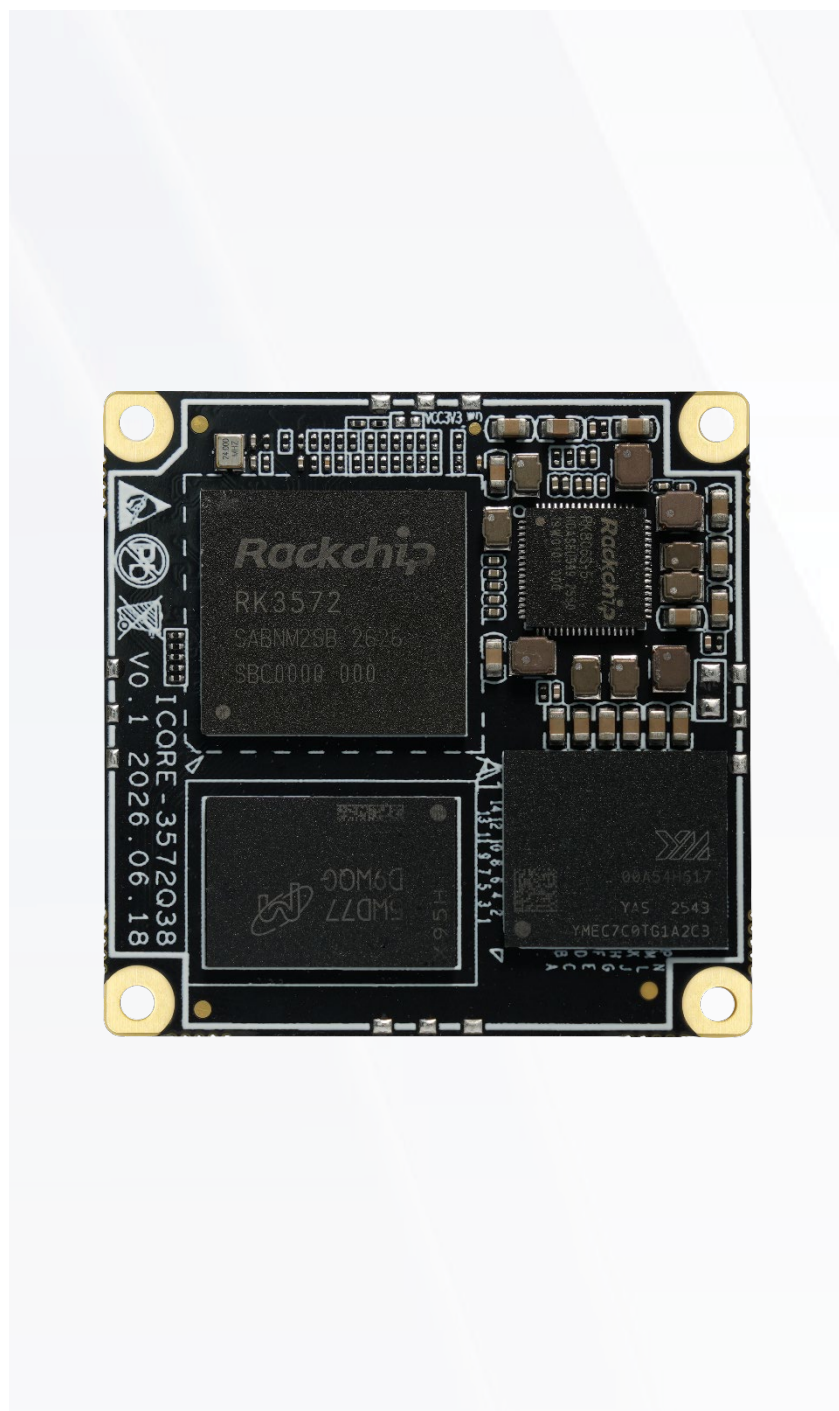
■ RK3572 核心板

V0.1 2026-9-14

天启集团



产品特点 Product features



八核64位 AIoT 处理器 RK3572

搭载瑞芯微 RK3572 八核64位 AIoT 处理器（2×A73 + 6×A53），配备 NEON 协处理器，主频最高2.2GHz，采用 8nm 先进制程，具备优异的低功耗特性



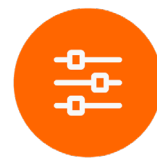
集成 Mali G310 GPU

集成 Mali G310 GPU，支持 OpenGL ES 1.1/2.0/3.2，OpenCL 3.0，Vulkan 1.4，内嵌高性能 2D 加速硬件



支持 5 路摄像头输入、双屏异显

支持5路摄像头输入（2路MIPI CSI DPHY（1*4Lanes+2*2Lanes）+ 1×DVP），可实现 4K@60fps+2K@60fps 双屏异显输出



丰富的扩展接口

拥有PCIe2.1、SATA3.1、MIPI-CSI、DVP、MIPI-DSI、PWM、SPI、I3C、UART、CAN 等扩展接口，满足不同场景的外设扩展需求



8K 视频解码，4K 视频编码

支持 8K@30fps/4K@120fps(H.265/HEVC、VP9、AVS2、AV1) 高帧率视频解码，以及 4K@40fps (H.265/HEVC、H.264/AVC) 视频编码



内置 12MP ISP

集成 1200 万像素 ISP 图像处理单元，内置 Bayer-3DNR、HDR-Merge、DPCC、LDC、3A 等硬件算法加速器



BTB 接口，38mm×38mm 小尺寸

核心板采用BTB接口设计，尺寸小巧，仅 38mm×38mm，可便捷嵌入各类整机设备，快速完成产品集成



广泛的应用场景

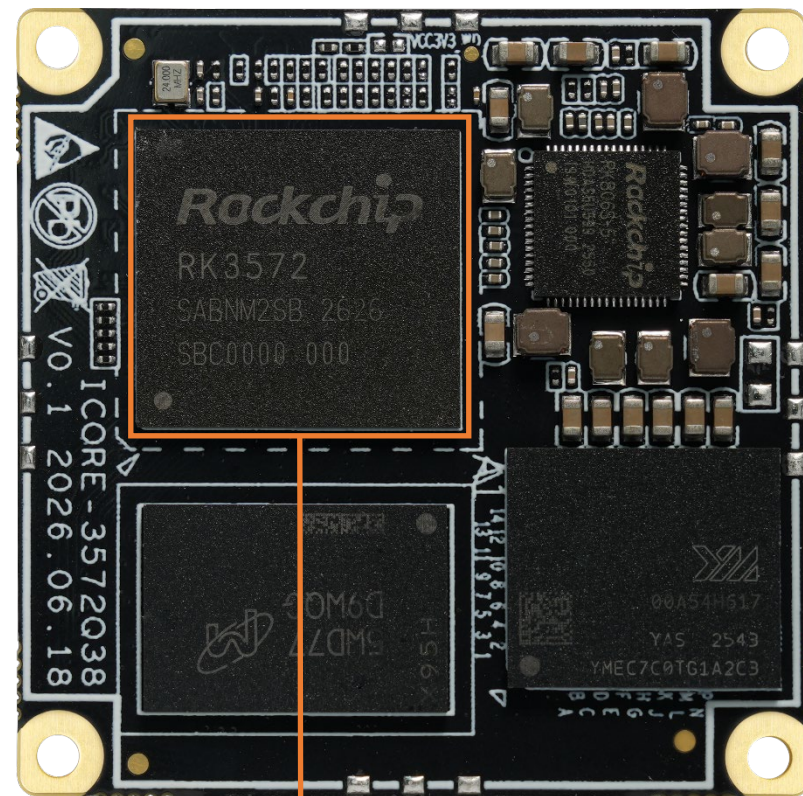
广泛适用于：边缘计算、平板电脑、移动终端、AI摄像头、智能监控、NAS、零售设备、数字标牌等行业领域

规格参数 Specifications



规格参数		
基本参数	SOC	Rockchip RK3572
	CPU	八核64位处理器（2×A73 + 6×A53），配备 NEON 协处理器，主频最高 2.2GHz
	GPU	ARM Mali G310V2 MC1 GPU，支持 OpenGL ES 1.1/2.0/3.2，OpenCL 3.0，Vulkan 1.4，内嵌高性能 2D 加速硬件
	NPU	NPU 算力 4 TOPS（稀疏INT8），支持 INT4/INT8/INT16/FP4/FP8/FP16/BF16 混合运算，支持 W4A16 非对称 MAC 运算
	ISP	内置1200万像素 ISP，集成多种算法加速器，如 Bayer-3DNR、HDR-Merge、DPCC、LDC、3A、LSC、CAC、去雾增强等
	编解码	视频解码：8K@30fps/4K@120fps（H.265/HEVC、VP9、AVS2、AV1）、4K@60fps（H.264/AVC） 视频编码：4K@40fps（H.265/HEVC、H.264/AVC） 图片编解码：4K@60fps MJPG
	内存	LPDDR4/LPDDR4X（2GB/4GB/8GB/16GB 可选）
	存储	eMMC（16GB/32GB/64GB/128GB 可选）
	电源	5V（电压误差 ± 5%）
	功耗	典型功耗：1W(5V/200mA)，最大功耗：3.5W(5V/700mA)，休眠功耗：0.225W(5V/45mA)
	系统	Debian、Ubuntu
	软件支持	支持YOLO目标检测算法 支持Transformer架构下超大规模参数模型的私有化部署，如Gemma系列、ChatGLM系列、Qwen系列、Phi系列等大语言模型 支持CNN、RNN、LSTM等传统网络架构，支持RKNN模型导入导出，支持多种深度学习框架，包括TensorFlow、TensorFlow Lite、Pytorch、Caffe、ONNX和MXNet 支持自定义算子开发、Docker容器化管理技术
	接口类型	BTB（2 × BTB座(100Pin)）
	尺寸	38.0mm × 38.0mm × 5.5mm
	重量	≈9g
	环境	工作温度：-20℃ ~ 60℃，存储温度：-20℃ ~ 70℃，存储湿度：10% ~ 90%RH（无凝露）
接口参数	网络	2 × GMAC (提供RMII 或 RGMII 接口连接外置的 Ethernet PHY；支持10/100/1000 Mbps速率)，通过USB、PCIE、SDIO、UART可扩展WiFi、蓝牙、4G、5G
	视频输入	2 × MIPI-CSI DPHY（支持MIPI V1.2 版本；1 × 4 Lanes 或 2 × 2 Lanes） 1 × DVP（BT.601/BT.656 和 BT.1120，BT.656/BT.1120 接口支持 2 路 / 4 路复用字节交织格式，BT.656/BT.1120 接口支持双边沿采样，8/10/12/16 位）
	视频输出	1 × MIPI-DSI DPHY 1.2（1×4Lanes 或 2×2Lanes，2.5Gbps/Lane）
	音频	3 × SAI (4T/4R)、2 × SAI (1T/1R)、1 × SAI (4R)，支持 I2S/TDM/PCM 音频协议，采样率最高可达 192KHz
	PCle/SATA	1 × PCIe 2.1/SATA 3.1/USB 3.0 组合接口（功能三选二） 1 × PCIe 2.1/SATA 3.1 组合接口（功能二选一）
	USB	2 × USB3.0 DRD/USB2.0
	SDIO	2 × SDIO3.0
	PWM	16 × PWM
	SPI	5 × SPI（支持串行主站和串行从模式，软件可配置）
	I2C	10 × I2C（支持 7 位和 10 位地址模式，标准模式下数据速率高达100kbps，快速模式下数据速率高达400kbps）
	I3C	1 × I3C（符合 I2C 标准，支持 SDR 模式、支持多达 11 个设备）
	UART	12 × UART（支持自动流量控制模式、支持 RS485 功能）
	CAN	4 × CAN（支持 8192 bit 接收 FIFO）

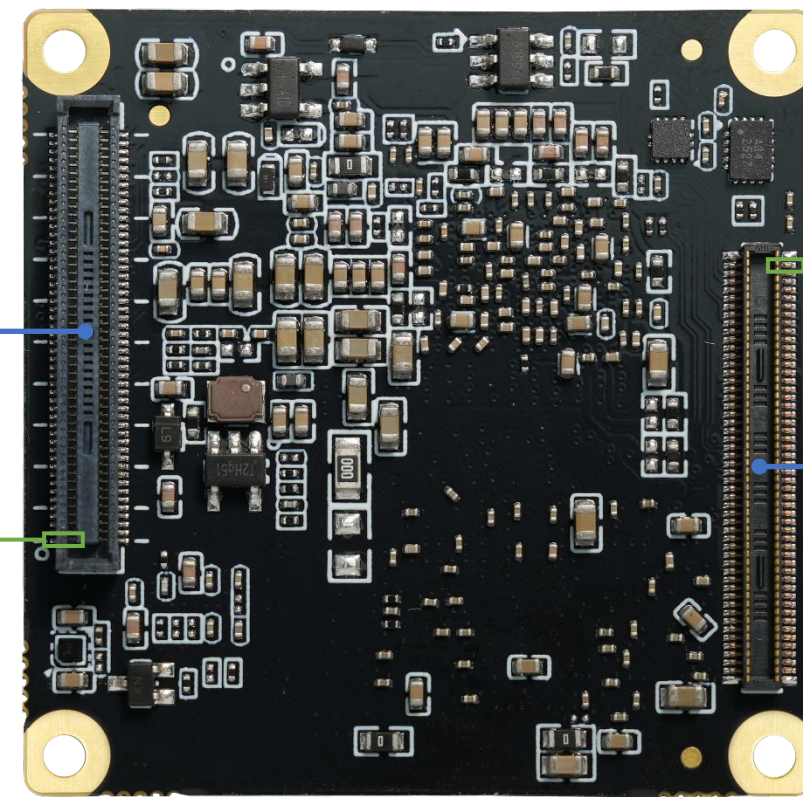
接口描述 Interface description



Rockchip RK3572
(主频最高2.2GHz)

J3-BTB座 (100Pin)

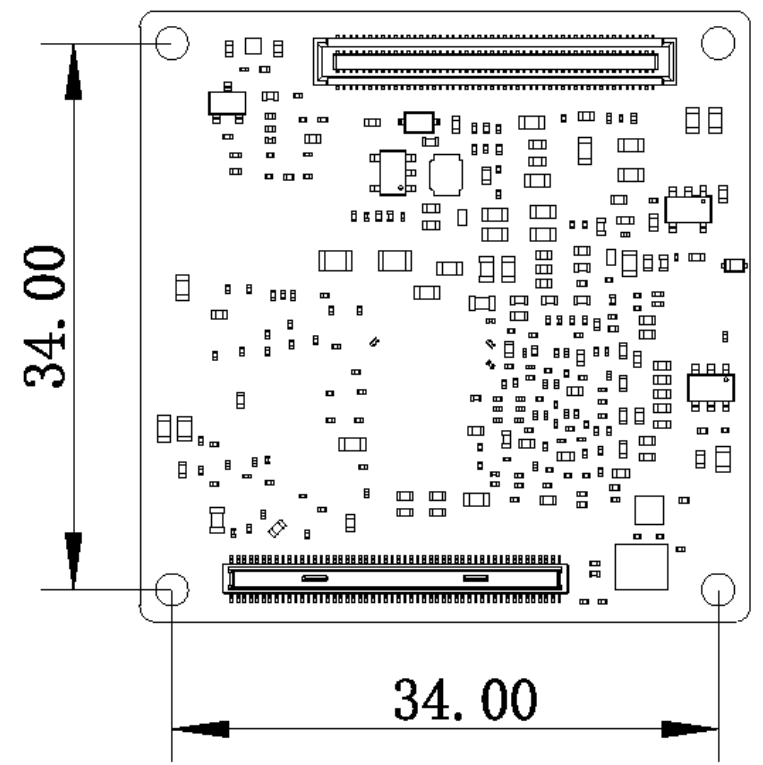
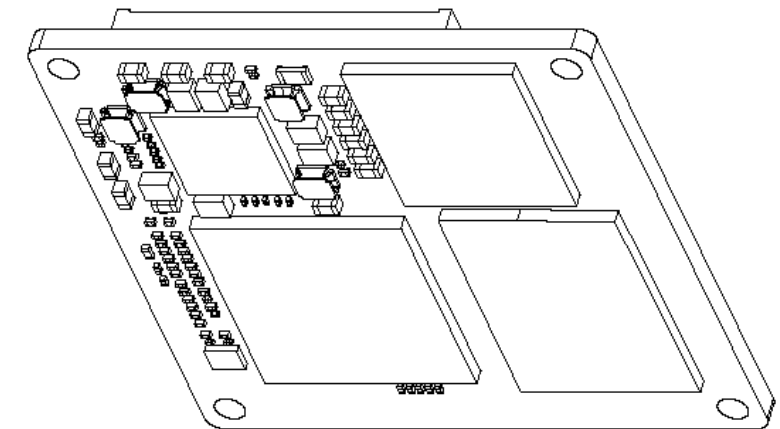
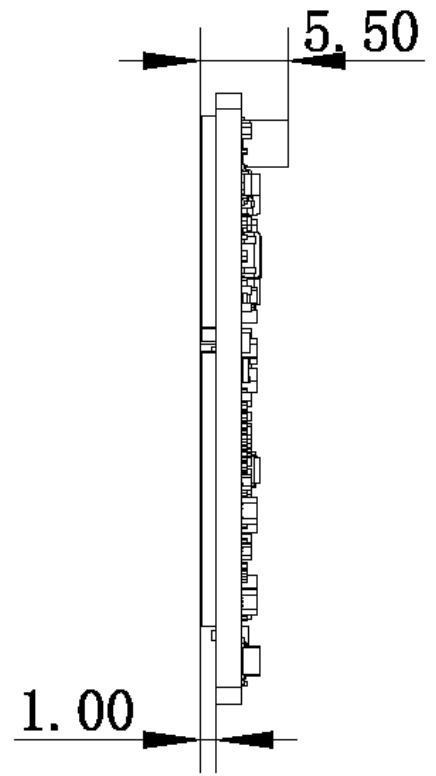
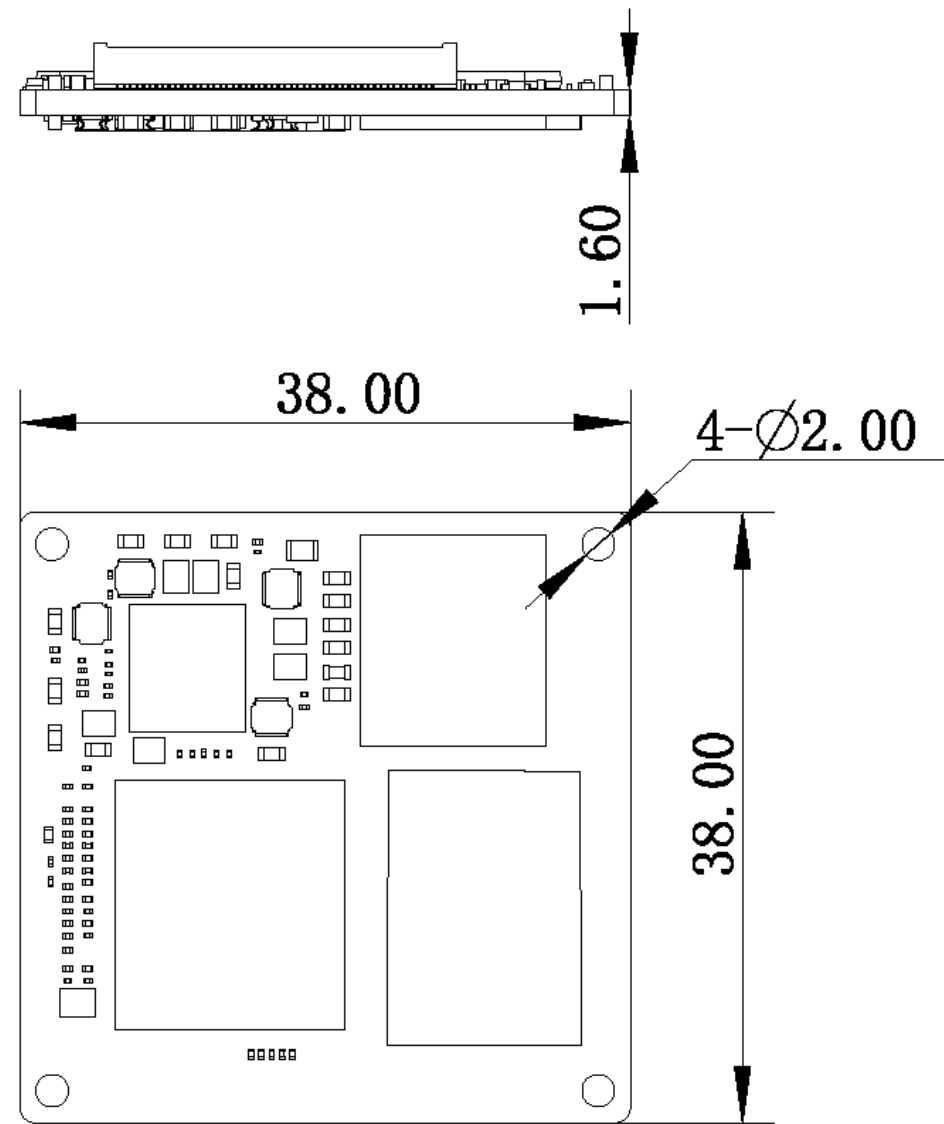
Pin1



Pin1

J1-BTB座 (100Pin)

产品尺寸 Dimension





接口定义 Interface definition

Notes1: Pin type: I = input, O = output, I/O = input/output (bidirectional), G= Ground , P = power supply , DOWN = Internal pull down , UP = Internal pull UP , L = Low Level , H = High level							
PIN	iCORE-3572Q38 pin definition (J1)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
1	I2C6_SCL_M0/CLK0_32K_OUT/CLK_32K_IN/GPIO0_A2_d	1K22	I/O	1.8V	DOWN	32KOUT_RTC2SOC	32KOUT_TO_SOC
3	JTAG_TMS_M1/UART0_RX_M0/GPIO0_D5_u	V32	I/O	3.3V	UP	UART0_RX_M0_DEBUG	UART0_RX_M0_DEBUG
5	JTAG_TCK_M1/UART0_TX_M0/GPIO0_D4_u	U33	I/O	3.3V	UP	UART0_RX_M0_DEBUG	UART0_RX_M0_DEBUG
7	SARADC_IN0_BOOT	1AF21	I	1.8V		SARADC_VIN0_BOOT	SARADC_VIN0_BOOT
9	SARADC_IN1	1AD19	I	1.8V		SARADC_VIN1_KEY/RECOVERY	SARADC_VIN1_KEY/RECOVERY
11	SARADC_IN3	1AC19	I	1.8V		SARADC_VIN3_HP_HOOK	SARADC_VIN3_HP_HOOK
13	SARADC_IN4	1AD17	I	1.8V		SARADC_VIN4	SARADC_VIN4
15	CAN0_TX_M1/HDMI_TX_SCL_M0/PWM2_CH0_M1/I2C2_SCL_M3/UART6_RTSN_M1/VO_EBC_SDDO18/DSM_AUD_RP_M1/SAI3_LRCK_M2/GPIO4_B2_d	AT32	I/O	3.3V	DOWN	POW_HOLD	HDMI_TX_SCL
17	PWM1_CH2_M0/SDMMC1_DET_N_M1/SDMMC0_PWREN_M0/HDMI_TX_HPDIN_M1/EDP_TX_HPDIN_M1/GPIO0_B6_d	AB33	I	3.3V	DOWN	UART11_RX_M2	HDMI_TX_HPDIN_M1
19	HDMI_TX_CEC_M0/PWM1_CH5_M1/I2C3_SCL_M3/SPI4_CSN1_M0/UART6_TX_M1/VO_EBC_SDDO16/DSM_AUD_LP_M1/SAI3_MCLK_M2/GPIO4_B0_d	1AD21	I/O	3.3V	DOWN	UART11_TX_M2	HDMI_TX_CEC_M0
21	CAN0_RX_M1/HDMI_TX_SDA_M0/PWM2_CH1_M1/I2C2_SDA_M3/UART6_CTSN_M1/VO_EBC_SDDO19/DSM_AUD_RN_M1/SAI3_SCLK_M2/GPIO4_B3_d	AR32	I/O	3.3V	DOWN	USBCC_INT_L	HDMI_TX_SDA
23	CAN1_RX_M1/PWM2_CH3_M1/I2C6_SDA_M3/SPI4_CLK_M0/UART9_RTSN_M0/VO_EBC_SDDO23/GPIO4_B7_d	1N32	I/O	3.3V	DOWN	I2C6_SDA_M3	I2C6_SDA_M3
25	PCIE1_CLKREQ_N_M3/CAN1_TX_M1/PWM2_CH2_M1/I2C6_SCL_M3/SPI4_MISO_M0/UART9_CTSN_M0/VO_EBC_SDDO22/VP1_SYNC_OUT/SATA30_PORT1_ACTLED_M1/GPIO4_B6_d	AP33	I/O	3.3V	DOWN	I2C6_SCL_M3	I2C6_SCL_M3
27	GND1		G			GND	GND



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J1)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
29	PCIE1_REFCLKN	1H22	O	-		NC	PCIE1_REFCLKN
31	PCIE1_REFCLKP	1H21	O	-		NC	PCIE1_REFCLKP
33	PCIE1_RXN/SATA1_RXN	K21	I	-		NC	PCIE1_RXN/SATA1_RXN
35	PCIE1_RXP/SATA1_RXP	K33	I	-		NC	PCIE1_RXP/SATA1_RXP
37	PCIE1_TXN/SATA1_TXN	L32	O	-		NC	PCIE1_TXN/SATA1_TXN
39	PCIE1_TXP/SATA1_TXP	L33	O	-		NC	PCIE1_TXP/SATA1_TXP
41	PCIE0_REFCLKP	1F21	G			GND	PCIE0_REFCLKP_M1
43	PCIE0_TXP_M1/SATA0_TXP_M1/USB_DRD1_SSTXP	G32	O	-		NC	PCIE0_TXP_M1/SATA0_TXP_M1/USB_DRD1_SSTXP
45	PCIE0_TXN_M1/SATA0_TXN_M1/USB_DRD1_SSTXN	G33	O	-		NC	PCIE0_TXN_M1/SATA0_TXN_M1/USB_DRD1_SSTXN
47	PCIE0_RXP_M1/SATA0_RXP_M1/USB_DRD1_SSRXP	H32	I	-		NC	PCIE0_RXP_M1/SATA0_RXP_M1/USB_DRD1_SSRXP
49	PCIE0_RXN_M1/SATA0_RXN_M1/USB_DRD1_SSRXN	H33	I	-		NC	PCIE0_RXN_M1/SATA0_RXN_M1/USB_DRD1_SSRXN
51	MIPI_DPHY_CSI3_RX_CLKP	1B12	O	-		NC	MIPI_DPHY_CSI3_RX_CLKP
53	MIPI_DPHY_CSI3_RX_CLKN	1C12	O	-		NC	MIPI_DPHY_CSI3_RX_CLKN
55	GND2		G			GND	GND
57	MIPI_DPHY_CSI2_RX_D0P	A13	I/O	-		NC	MIPI_DPHY_CSI2_RX_D0P
59	MIPI_DPHY_CSI2_RX_D0N	B13	I/O	-		NC	MIPI_DPHY_CSI2_RX_D0N
61	MIPI_DPHY_CSI2_RX_D1P	1B10	I/O	-		NC	MIPI_DPHY_CSI2_RX_D1P



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J1)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
63	MIPI_DPHY_CSI2_RX_D1N	1C10	I/O	-		NC	MIPI_DPHY_CSI2_RX_D1N
65	MIPI_DPHY_CSI2_RX_CLKP	A14	I/O	-		NC	MIPI_DPHY_CSI2_RX_CLKP
67	MIPI_DPHY_CSI2_RX_CLKN	B14	I/O	-		NC	MIPI_DPHY_CSI2_RX_CLKP
69	MIPI_DPHY_CSI2_RX_D2P/MIPI_DPHY_CSI3_RX_D0P	A16	I/O	-		NC	MIPI_DPHY_CSI3_RX_D0P
71	MIPI_DPHY_CSI2_RX_D2N/MIPI_DPHY_CSI3_RX_D0N	B16	I/O	-		NC	MIPI_DPHY_CSI3_RX_D0N
73	MIPI_DPHY_CSI2_RX_D3P/MIPI_DPHY_CSI3_RX_D1P	A17	I/O	-		NC	MIPI_DPHY_CSI3_RX_D1P
75	MIPI_DPHY_CSI2_RX_D3N/MIPI_DPHY_CSI3_RX_D1N	B17	I/O	-		NC	MIPI_DPHY_CSI3_RX_D1N
77	GND3		G			GND	GND
79	HDMI_TX_SBDN/EDP_TX_AUXN	1AD13	O	-		NC	HDMI_TX_SBDN
81	HDMI_TX_SBDP/EDP_TX_AUXP	1AE13	O	-		NC	HDMI_TX_SBDP
83	HDMI_TX_D3N/EDP_TX_D3N	AR20	O	-		NC	HDMI_TX_D3N
85	HDMI_TX_D3P/EDP_TX_D3P	AT20	O	-		NC	HDMI_TX_D3P
87	HDMI_TX_D2N/EDP_TX_D2N	AT21	O	-		NC	HDMI_TX_D0N
89	HDMI_TX_D2P/EDP_TX_D2P	AR21	O	-		NC	HDMI_TX_D0P
91	HDMI_TX_D1N/EDP_TX_D1N	AR23	O	-		NC	HDMI_TX_D1N
93	HDMI_TX_D1P/EDP_TX_D1P	AT23	O	-		NC	HDMI_TX_D1P
95	HDMI_TX_D0N/EDP_TX_D0N	AT24	O	-		NC	HDMI_TX_D2N



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J1)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
97	HDMI_TX_D0P/EDP_TX_D0P	AR24	O	-		NC	HDMI_TX_D2P
99	GND4		G			GND	GND
2	GND5		G			GND	GND
4	I2C4_SCL_M1/SPI3_MOSI_M2/UART9_TX_M2/PDM_CLK0_M1/SAI0_SCLK_M2/VI_CIF_D15/UART6_RTSN_M0/GPIO2_B2_d	B3	I/O	3.3V	DOWN	GPIO4_B4_d	PCIE1_WAKEN_M2
6	CAN0_RX_M2/PCIE1_CLKREQN_M2/I2C2_SDA_M2/SPI4_CSN1_M2/UART9_RX_M2/SAI0_SDI2_M2/SAI1_LRCK_M0/VI_CIF_D14/UART6_CTSN_M0/GPIO2_B3_d	1D2	I/O	3.3V	DOWN	GPIO4_B5_d	PCIE1_CLKREQN_M2
8	PWM2_CH5_M0/UART6_RX_M0/SAI0_MCLK_M2/SAI1_MCLK_M0/GPIO2_B0_d	1D1	I/O	3.3V	DOWN	GPIO4_A6_d	UART6_RX_M0
10	CAN0_TX_M2/PWM2_CH4_M1/I2C2_SCL_M2/SPI3_CSN0_M2/UART6_TX_M0/SAI0_SDI1_M2/SAI1_SCLK_M0/GPIO2_B1_d	1A2	I/O	3.3V	DOWN	GPIO4_A4_d	UART6_TX_M0
12	I3C_SDA_M0/I2C0_SDA_M1/UART8_RX_M1/GPIO0_C2_d	W32	I/O	3.3V	DOWN	GPIO4_A3_d	PCIE0_WAKEN_M2
14	PCIE0_CLKREQN_M3/HDMI_TX_HPDIN_M0/PWM0_CH1_M1/I2C3_SDA_M3/EDP_TX_HPDIN_M0/UART6_RX_M1/VO_EBC_SDDO17/DSM_AUD_LN_M1/SAI3_SDI_M2/GPIO4_B1_d	1AE21	I/O	3.3V	DOWN	GPIO4_A5_d	PCIE0_CLKREQN_M3
16	USB_DRD0_VBUSDET	1C18	I	3.3V		USB2_OTG0_VBUSDET	USB_DRD0_VBUSDET
18	USB_DRD0_ID	1B18	I	1.8V		NC	USB_DRD0_ID (RK3576 internal pull-up)
20	PWM0_CH0_M0/UART10_TX_M2/PDM_CLK0_M0/SAI0_MCLK_M1/GPIO0_C4_d	1T20	I/O	3.3V	DOWN	GPIO0_C4_d	UART10_TX_M2
22	I3C_SDA_PU_M0/UART10_RX_M2/SAI0_SDO0_M1/GPIO0_C5_d	1T21	I/O	3.3V	DOWN	SPK_CTRL	UART10_RX_M2
24	PWM1_CH5_M0/I2C4_SCL_M0/UART1_CTSN_M0/PDM_SDI2_M0/SAI0_SDO2_M1/SAI0_SDI2_M1/CPUBIG_AVS/GPIO0_D2_d	1P23	I/O	3.3V	DOWN	GPIO0_D3_d	I2C4_SDA_M0
26	PWM2_CH0_M0/I2C4_SDA_M0/UART1_RTSN_M0/PDM_SDI3_M0/SAI0_SDO1_M1/SAI0_SDI3_M1/GPU_AVS/GPIO0_D3_d	1P22	I/O	3.3V	DOWN	SYS_LED	I2C4_SCL_M0
28	I2C3_SCL_M1/UART8_RTSN_M1/SAI0_SCLK_M1/SPI0_CSN0_M0/GPIO0_C6_d	1T22	I/O	3.3V	DOWN	SPI0_CSN0_M0	SPI0_CSN0_M0



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J1)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
30	UART11_TX_M0/PDM_SDI0_M0/SAI0_SDI0_M1/SPI0_MOSI_M0/GPIO0_D0_d	1P20	I/O	3.3V	DOWN	SPI0_MOSI_M0	SPI0_MOSI_M0
32	I2C3_SDA_M1/UART8_CTSN_M1/SAI0_LRCK_M1/SPI0_CLK_M0/GPIO0_C7_d	1T23	I/O	3.3V	DOWN	SPI0_CLK_M0	SPI0_CLK_M0
34	UART11_RX_M0/PDM_SDI1_M0/SAI0_SDO3_M1/SAI0_SDI1_M1/SPI0_MISO_M0/GPIO0_D1_d	1P21	I/O	3.3V	DOWN	SPI0_MISO_M0	SPI0_MISO_M0
36	PWM1_CH4_M0/I2C2_SCL_M0/UART1_TX_M0/NPU_AV5/GPIO0_B7_d	AA32	I/O	3.3V	DOWN	I2C2_SCL_M0	I2C2_SCL_M0
38	PWM1_CH3_M0/I2C2_SDA_M0/UART1_RX_M0/CPULIT_AV5/GPIO0_C0_d	Y32	I/O	3.3V	DOWN	I2C2_SDA_M0	I2C2_SDA_M0
40	PWM1_CH0_M0/I2C1_SCL_M1/UART4_TX_M2/REF_CLK1_OUT/GPIO0_B4_d	AC33	I/O	3.3V	DOWN	GPIO0_B4_d	UART4_TX_M2
42	PWM1_CH1_M0/I2C1_SDA_M1/UART4_RX_M2/REF_CLK2_OUT/GPIO0_B5_d	AB32	I/O	3.3V	DOWN	GPIO0_B5_d	UART4_RX_M2
44	GND6		G			GND	PCIE0_REFCLKN_M1
46	USB_DRD1_DP	B26		-		USB2_HOST1_DP	USB_DRD1_DP
48	USB_DRD1_DM	A26		-		USB2_HOST1_DM	USB_DRD1_DM
50	USB_DRD0_DP	B25		-		USB2_OTG0_DP	USB_DRD0_DP
52	USB_DRD0_DM	A25		-		USB2_OTG0_DM	USB_DRD0_DM
54	GND7		G			GND	GND
56	NC	-		-		NC	NC
58	NC	-		-		NC	NC
60	TYPEC_SSRX1P	D33	I/O	-		USB3_OTG0_SSRX1P/DP_TX_D0P	TYPEC_SSRX1P
62	TYPEC_SSRX1N	D32	I/O	-		USB3_OTG0_SSRX1N/DP_TX_D0N	TYPEC_SSRX1N



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J1)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
64	TYPEC_SSTX1P	E32	I/O	-		USB3_OTG0_SSTX1P/DP_TX_D1P	TYPEC_SSTX1P
66	TYPEC_SSTX1N	E33	I/O	-		USB3_OTG0_SSTX1N/DP_TX_D1N	TYPEC_SSTX1N
68	TYPEC_SSRX2P	D33	I/O	-		USB3_OTG0_SSRX2P/DP_TX_D2P	TYPEC_SSRX2P
70	TYPEC_SSRX2N	D32	I/O	-		USB3_OTG0_SSRX2N/DP_TX_D2N	TYPEC_SSRX2N
72	TYPEC_SSTX2P	E32	I/O	-		USB3_OTG0_SSTX2P/DP_TX_D3P	TYPEC_SSTX2P
74	TYPEC_SSTX2N	E33	I/O	-		USB3_OTG0_SSTX2N/DP_TX_D3N	TYPEC_SSTX2N
76	GND8		G			GND	GND
78	MIPI_DPHY_CSI0_RX_D2P/MIPI_DPHY_CSI1_RX_D0P	A22	I/O	-		MIPI_DPHY_CSI1_RX_D2P/MIPI_DPHY_CSI2_RX_D0P	MIPI_DPHY_CSI1_RX_D0P
80	MIPI_DPHY_CSI0_RX_D2N/MIPI_DPHY_CSI1_RX_D0N	B22	I/O	-		MIPI_DPHY_CSI1_RX_D2N/MIPI_DPHY_CSI2_RX_D0N	MIPI_DPHY_CSI1_RX_D0N
82	MIPI_DPHY_CSI0_RX_D3P/MIPI_DPHY_CSI1_RX_D1P	A23	I/O	-		MIPI_DPHY_CSI1_RX_D3P/MIPI_DPHY_CSI2_RX_D1P	MIPI_DPHY_CSI1_RX_D1P
84	MIPI_DPHY_CSI0_RX_D3N/MIPI_DPHY_CSI1_RX_D1N	B23	I/O	-		MIPI_DPHY_CSI1_RX_D3N/MIPI_DPHY_CSI2_RX_D1N	MIPI_DPHY_CSI1_RX_D1N
86	MIPI_DPHY_CSI1_RX_CLKP	1B16	I/O	-		NC	MIPI_DPHY_CSI1_RX_CLKP
88	MIPI_DPHY_CSI1_RX_CLKN	1C16	I/O	-		NC	MIPI_DPHY_CSI1_RX_CLKN
90	MIPI_DPHY_CSI0_RX_CLKP	A20	I/O	-		MIPI_DPHY_CSI1_RX_CLKP	MIPI_DPHY_CSI0_RX_CLKP
92	MIPI_DPHY_CSI0_RX_CLKN	B20	I/O	-		MIPI_DPHY_CSI1_RX_CLKN	MIPI_DPHY_CSI0_RX_CLKN
94	MIPI_DPHY_CSI0_RX_D1P	1B14	I/O	-		MIPI_DPHY_CSI1_RX_D1P	MIPI_DPHY_CSI0_RX_D1P
96	MIPI_DPHY_CSI0_RX_D1N	1C14	I/O	-		MIPI_DPHY_CSI1_RX_D1N	MIPI_DPHY_CSI0_RX_D1N



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J1)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
98	MIPI_DPHY_CSI0_RX_D0P	A19	I/O	-		MIPI_DPHY_CSI1_RX_D0P	MIPI_DPHY_CSI0_RX_D0P
100	MIPI_DPHY_CSI0_RX_D0N	B19	I/O	-		MIPI_DPHY_CSI1_RX_D0N	MIPI_DPHY_CSI0_RX_D0N
PIN	iCORE-3572Q38 pin definition (J2)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
1	VCC4V0_SYS_S5-1		P	5.0V		VCC5V0_SYS Normal: 5V/150mA Max.:5V/1500mA Min.:5V/5mA	VCC5V0_SYS INPUT Voltage 5.0V +/-5%
3	VCC4V0_SYS_S5-2		P	5.0V			
5	VCC4V0_SYS_S5-3		P	5.0V			
7	VCC4V0_SYS_S5-4		P	5.0V			
9	VCC4V0_SYS_S5-5		P	5.0V			
11	GND1		G	GND		GND	GND
13	GND2		G	GND			
15	GND3		G	GND			
17	GND4		G	GND			
19	PWRON_L		I	5.0V		PWRON_L	PWERON KEY INPUT,Active L
21	VCCIO_SD_S0		P	3.3V		VCCA_3V3_S0	VCC3V3_SD_S0 OUTPUT (3.3V, Total Max:100mA)
23	VDC_EXT (Automatic startup INPUT)		I	5.0V		VDC_EXT	PMIC_EN INPUT, Active H
25	VCC_3V3_S3-1		P	3.3V		VCC_3V3_S3	VCC_3V3_S3 OUTPUT (3.3V, Total Max:500mA)
27	VCC_3V3_S3-2		P	3.3V		VCC_3V3_S3	



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J2)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
29	RESET_L	P32	I/O	1.8V		RESET_L	SYSTEM RESET INPUT, Active L
31	PMIC_EXT_EN_H		O	5.0V		NC	Mainboard pins PMIC_EXT_EN OUTPUT, Active H
33	AUPLL_CLK_IN/REF_CLK0_OUT/GPIO0_A0_d	1K20	I/O	1.8V	DOWN	RTC_INT_L	RTC_INT INPUT, Active L
35	SATA0_MPSWIT_M0/CAN3_RX_M0/PWM1_CH2_M1/SPDIF_TX1_M1/ETH1_PTP_REFCLK_M1/GPIO1_D6_d	1AF7	I/O	VCCIO4 (1.8V)	DOWN	NC	UART7_CTSN_M0
37	CLK1_32K_OUT/CAN3_TX_M0/I2C5_SDA_M1/SPI2_CLK_M1/UART7_CTSN_M0/PDM_CLK1_M2/SPDIF_TX0_M1/ETH_CLK1_25M_OUT_M1/GPIO1_D5_d	AR18	I/O	VCCIO4 (1.8V)	DOWN	NC	UART7_RTSN_M0
39	SATA1_CPPOD_M0/I3C_SDA_PU_M1/UART7_RX_M0/SAI2_SCLK_M0/ETH1_RXCTL_M1/GPIO1_D1_d	1AD11	I/O	VCCIO4 (1.8V)	DOWN	NC	UART7_RX_M0
41	UART7_TX_M0/SAI2_SDO_M0/ETH1_RXD1_M1/GPIO1_D0_d	1AF9	I/O	VCCIO4 (1.8V)	DOWN	WIFI_PWR_EN	UART7_TX_M0
43	GND5		G	GND		GND	GND
45	CAN0_RX_M0/JTAG_TCK_M0/SPI0_CSN1_M1/UART5_RTSN_M2/SAI3_LRCK_M1/DSM_AUD_RP_M0/SDMMC0_D2_M0/FSPI1_D2_M0/GPIO2_A2_d	B11	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_D2 (To SD CARD)	SDMMC0_D2
47	CAN0_TX_M0/JTAG_TMS_M0/UART5_CTSN_M2/SAI3_SDI_M1/DSM_AUD_RN_M0/SDMMC0_D3_M0/FSPI1_D3_M0/GPIO2_A3_d	1A8	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_D3 (To SD CARD)	SDMMC0_D3
49	CAN2_TX_M0/I2C5_SCL_M0/SPI0_CLK_M1/UART5_TX_M2/TEST_CLK_OUT/SAI3_SCLK_M1/SDMMC0_CLK_M0/FSPI1_CLK_M0/GPIO2_A5_d	1C8	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_CLK (To SD CARD)	SDMMC_CLK (Core board series resistance 22R)
51	CAN2_RX_M0/PWM2_CH4_M0/I2C5_SDA_M0/SPI0_CSN0_M1/UART5_RX_M2/SAI3_SDO_M1/SDMMC0_CMD_M0/FSPI1_CS_N0_M0/GPIO2_A4_z	1B8	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_CMD (To SD CARD)	SDMMC0_CMD
53	CAN1_TX_M0/PWM2_CH3_M0/I2C8_SDA_M0/SPI0_MISO_M1/UART0_TX_M1/UART7_TX_M2/SAI3_MCLK_M1/DSM_AUD_LN_M0/SDMMC0_D1_M0/FSPI1_D1_M0/GPIO2_A1_d	1A10	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_D1 (To SD CARD)	SDMMC0_D1
55	CAN1_RX_M0/PWM2_CH2_M0/I2C8_SCL_M0/SPI0_MOSI_M1/UART0_RX_M1/UART7_RX_M2/DSM_AUD_LP_M0/SDMMC0_D0_M0/FSPI1_D0_M0/GPIO2_A0_d	1D8	I/O	VCCIO_SD (3.3V)	DOWN	SDMMC0_D0 (To SD CARD)	SDMMC0_D0



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J2)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
57	GND6		G			GND	GND
59	PWM0_CH1_M0/PDM_CLK1_M0/HDMI_TX_CEC_M1/SPI0_CSN1_M0/GPIO0_C3_d	W33	I/O	3.3V	DOWN	SDMMC0_PWREN_H	GPIO0_C3_d
61	SPI2_CSN1_M0/SDMMC0_DET_N_M0/GPIO0_A7_u	T33	I/O	1.8V	UP	SDMMC0_DET_L	SDMMC0_DET_L
63	SATA1_CPPOD_M1/SPDIF_RX0_M4/I2C7_SCL_M2/SPI1_CSN1_M2/UART9_TX_M1/SAI1_SDO0_M1/VO_EBC_BORDER0/MIPI_TE_M2/DSMC_INT0/CAM_CLK1_OUT_M0/GPIO4_A0_d	1AD23	I/O	VCCIO5 (3.3V)	DOWN	NC	CAM1_MCLK
65	SATA1_CPDET_M1/SPDIF_TX0_M4/I2C7_SDA_M2/UART9_RX_M1/SAI1_LRCK_M1/VO_EBC_BORDER1/VO_POST_EMPTY/DSMC_INT2/CAM_CLK2_OUT_M0/GPIO4_A1_d	1AD22	I/O	VCCIO5 (3.3V)	DOWN	GPIO4_A1_d	CAM2_MCLK
67	SATA1_MPSWIT_M1/PWM2_CH7_M2/UART5_RTSN_M0/SAI1_SDO1_M1/VO_EBC_SDOE/VO_LCDC_CLK/DSMC_RESETN/CAM_CLK0_OUT_M0/GPIO3_D7_d	AM32	I/O	VCCIO5 (3.3V)	DOWN	CAM0_MCLK	CAM0_MCLK
69	GND7		G			GND	GND
71	PWM1_CH4_M2/I2C6_SDA_M2/SPI1_CLK_M1/UART1_RX_M1/VI_CIF_D0/SAI0_SDI0_M0/SDMMC0_DET_N_M1/FSPI1_D0_M1/ETH0_TXCTL/GPIO2_D3_d	1C6	I/O	VCCIO4 (1.8V)	DOWN	UART4_RX_M0	GMAC0_TXCTL
73	CAN3_RX_M1/I2C5_SDA_M2/SPI3_MOSI_M0/UART3_RX_M0/VI_CIF_VSYNC/SPDIF_TX1_M2/SAI0_SDO3_M0/CAM_CLK1_OUT_M1/FSPI1_CLK_M1/ETH0_TXD1/GPIO2_D5_d	1B6	I/O	VCCIO4 (1.8V)	DOWN	WIFI_REG_ON_H	GMAC0_TXD1
75	PWM2_CH5_M2/I2C8_SDA_M2/UART10_RX_M1/VI_CIF_D6/PDM_CLK0_M3/SAI0_SDI1_M0/SDMMC0_D1_M1/FSPI1_D6_M1/ETH0_MDIO/GPIO2_C5_d	B10	I/O	VCCIO4 (1.8V)	DOWN	HOST_WAKE_BT_H	GMAC0_MDIO
77	SPI4_MISO_M2/UART5_RTSN_M1/PDM_SDI1_M1/SAI1_SDI1_M0/SAI1_SDO3_M0/VI_CIF_D9/ETH0_RXD3/GPIO2_C0_d	1C4	I/O	VCCIO4 (1.8V)	DOWN	BT_WAKE_HOST_H	GMAC0_RXD3
79	PWM2_CH7_M0/SPI3_CSN1_M2/SPI4_CSN0_M2/UART5_CTSN_M1/PDM_SDI0_M1/SAI0_SDO0_M2/SAI1_SDI0_M0/VI_CIF_D8/ETH0_RXD2/GPIO2_C1_d	1B4	I/O	VCCIO4 (1.8V)	DOWN	WIFI_WAKE_HOST_H	GMAC0_RXD2
81	CAN1_RX_M3/PWM2_CH3_M2/I2C4_SDA_M2/SPI3_CSN0_M0/UART3_RTSN_M0/VI_CIF_CLKI/SPDIF_TX0_M2/FSPI1_RST/FSPI1_CSN1_M1/ETH0_RXCLK/GPIO2_D7_z	B7	I/O	VCCIO4 (1.8V)	DOWN	HOST_WAKE_WIFI_H	GMAC0_RXCLK



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J2)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
83	CAN3_TX_M1/I2C5_SCL_M2/SPI3_CLK_M0/UART3_TX_M0/VI_CIF_HREF/SAI0_SDO2_M0/CAM_CLK0_OUT_M1/FSPI1_DQS_M1/ETH0_TXD0/GPIO2_D4_d	1A6	I/O	VCCIO4 (1.8V)	DOWN	GPIO2_D7_d	GMAC0_TXD0
85	CAN1_TX_M2/PCIE0_CLKREQN_M2/I2C3_SCL_M0/UART5_TX_M1/SPDIF_TX0_M0/ETH0_TXD3/GPIO2_C3_d	B5	I/O	VCCIO4 (1.8V)	DOWN	GPIO2_C4_d	GMAC0_TXD3
87	MIPI_TE_M1/CAN1_TX_M3/PWM2_CH2_M2/I2C4_SCL_M2/SPI3_MISO_M0/UART3_CTSN_M0/VI_CIF_CLKO/SPDIF_RX0_M2/SAI0_SDI3_M0/CAM_CLK2_OUT_M1/FSPI1_CSN0_M1/ETH0_TXCLK/GPIO2_D6_z	B8	I/O	VCCIO4 (1.8V)	DOWN	GPIO2_C5_d	GMAC0_TXCLK
89	CAN1_RX_M2/MIPI_TE_M0/I2C3_SDA_M0/UART5_RX_M1/SAI0_SDI3_M2/SPDIF_RX0_M0/ETH0_TXD2/GPIO2_C2_d	A5	I/O	VCCIO4 (1.8V)	DOWN	GPIO2_C3_d	GMAC0_TXD2
91	PCIE1_CLKREQN_M0/SATA30_PORT1_ACTLED_M0/PWM0_CH0_M2/I2C7_SDA_M1/SPI1_MISO_M1/UART1_RTSN_M1/VI_CIF_D2/PDM_SDI3_M3/SAI0_LRC_K_M0/SDMMC0_CLK_M1/FSPI1_D2_M1/ETH0_RXD1/GPIO2_D1_d	A7	I/O	VCCIO4 (1.8V)	DOWN	UART4_RTSN_M0	GMAC0_RXD1
93	PCIE0_CLKREQN_M0/ISP_FLASH_TRIGOUT_M0/PWM1_CH2_M2/I2C7_SCL_M1/SPI1_MOSI_M1/UART1_CTSN_M1/VI_CIF_D3/PDM_SDI2_M3/SAI0_SCLK_M0/SDMMC0_CMD_M1/FSPI1_D3_M1/ETH0_MCLK/GPIO2_D0_d	A8	I/O	VCCIO4 (1.8V)	DOWN	UART4_CTSN_M0	GMAC0_MCLK
95	SATA30_PORT0_ACTLED_M0/PWM1_CH3_M2/I2C6_SCL_M2/SPI1_CSN0_M1/UART1_TX_M1/VI_CIF_D1/SAI0_SDO0_M0/SDMMC0_PWREN_M1/FSPI1_D1_M1/ETH0_RXD0/GPIO2_D2_d	1D6	I/O	VCCIO4 (1.8V)	DOWN	UART4_TX_M0	GMAC0_RXD0
97	UART11_TX_M2/I2C9_SDA_M2/SPI3_CSN1_M0/UART10_CTSN_M1/VI_CIF_D5/PDM_SDI1_M3/SAI0_SDO1_M0/SDMMC0_D2_M1/FSPI1_D5_M1/ETH0_MDC/GPIO2_C6_d	B9	I/O	VCCIO4 (1.8V)	DOWN	I2C9_SDA_M2	GMAC0_MDC
99	ISP_PRELIGHT_TRIG_M0/UART11_RX_M2/I2C9_SCL_M2/SPI1_CSN1_M1/UART10_RTSN_M1/VI_CIF_D4/PDM_CLK1_M3/SAI0_MCLK_M0/SDMMC0_D3_M1/FSPI1_D4_M1/ETH0_RXCTL/GPIO2_C7_d	A10	I/O	VCCIO4 (1.8V)	DOWN	I2C9_SCL_M2	GMAC0_RXCTL
2	VCC4V0_SYS_S5-6		P	5.0V		VCC5V0_SYS Normal: 5V/150mA Max.:5V/1500mA Sleep:5V/5mA	VCC5V0_SYS INPUT Voltage 5.0V +/-5%
4	VCC4V0_SYS_S5-7		P	5.0V			
6	VCC4V0_SYS_S5-8		P	5.0V			
8	VCC4V0_SYS_S5-9		P	5.0V			
10	GND08		G	GND		GND	GND



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J2)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
12	GND09		G	GND		GND	GND
14	GND10		G	GND			
16	GND11		G	GND			
18	VCCA_RK806S (INPUT) --NC		P	5.0V		VCCA_RK806S (此信号为预留，实际核心板默认内部供电，此脚NC处理)	RK806S startup circuit power supply
20	VCC_1V8_S3	2G11	P	1.8V		VCC_1V8_S3	VCC_1V8_S3 OUTPUT (1.8V, Total Max:500mA)
22	GND12		G			GND	GND
24	SPI2_MOSI_M1/UART4_TX_M1/UART2_RTSN_M0/ETH1_TXD0_M1/GPIO1_C4_d	AT17	I/O	VCCIO4 (1.8V)	DOWN	NC	GPIO1_C4_d
26	SPI2_MISO_M1/UART4_RX_M1/UART2_CTSN_M0/ETH1_TXD1_M1/GPIO1_C5_d	AT18	I/O	VCCIO4 (1.8V)	DOWN	NC	GPIO1_C5_d
28	SATA1_CPDET_M0/PWM1_CH3_M1/I3C_SCL_M1/SAI2_LRCK_M0/ETH1_MDC_M1/GPIO1_D2_d	1AD9	I/O	VCCIO4 (1.8V)	DOWN	NC	GPIO1_D2_d
30	SATA1_MPSWIT_M0/PWM1_CH4_M1/I3C_SDA_M1/SAI2_SDI_M0/ETH1_MDIO_M1/GPIO1_D3_d	1AE9	I/O	VCCIO4 (1.8V)	DOWN	NC	GPIO1_D3_d
32	GND13		G			GND	GND
34	PCIE1_CLKREQN_M1/PWM1_CH0_M1/I2C9_SDA_M1/SPI1_CLK_M0/SAI4_MCLK_M1/SDMMC1_D0/ETH1_RXD2_M1/GPIO1_B4_d	AR14	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_D0_M0 (To SDIO WIFI)	SDMMC1_D0
36	UART3_RX_M2/PDM_CLK0_M2/SDMMC1_CLK/ETH1_TXCLK_M1/GPIO1_C1_d	AR15	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_CLK_M0 (To SDIO WIFI)	SDMMC1_CLK
38	SPI1_CSN0_M0/UART3_RTSN_M2/SAI4_SDO_M1/SDMMC1_D3/ETH1_TXD2_M1/GPIO1_B7_d	AT15	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_D3_M0 (To SDIO WIFI)	SDMMC1_D3
40	PWM1_CH1_M1/I2C9_SCL_M1/SPI1_MOSI_M0/SAI4_LRCK_M1/SDMMC1_D1/ETH1_RXD3_M1/GPIO1_B5_d	AR13	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_D1_M0 (To SDIO WIFI)	SDMMC1_D1
42	PCIE0_CLKREQN_M1/SPI1_MISO_M0/UART3_CTSN_M2/SAI4_SDI_M1/SDMMC1_D2/ETH1_RXCLK_M1/GPIO1_B6_d	AT14	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_D2_M0 (To SDIO WIFI)	SDMMC1_D2



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J2)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
44	PWM0_CH0_M1/SPI1_CSN1_M0/UART3_TX_M2/PDM_SDI2_M2/SAI4_SCLK_M1/SDMMC1_CMD/ETH1_TXD3_M1/GPIO1_C0_d	AR16	I/O	VCCIO3 (1.8V)	DOWN	SDMMC1_CMD_M0 (To SDIO WIFI)	SDMMC1_CMD
46	GND14		G			GND	GND
48	SATA0_CPPOD_M0/CAN2_RX_M1/I2C6_SCL_M1/UART4_RTSN_M1/SDMMC1_PWREN/ETH1_PPSCLK_M1/GPIO1_C2_d	1AE11	I/O	VCCIO3 (1.8V)	UP	NC	SDMMC1_PWREN
50	SATA0_CPDET_M0/CAN2_TX_M1/I2C6_SDA_M1/UART4_CTSN_M1/SDMMC1_DET_N_M0/ETH1_PPSTRIG_M1/GPIO1_C3_d	AR17	I/O	VCCIO3 (1.8V)	UP	NC	SDMMC1_DET_N_M0
52	HDMI_TX_SDA_M1/I2C8_SDA_M1/SPI2_CSN1_M1/UART2_RX_M0/PDM_SDI1_M2/ETH1_RXD0_M1/GPIO1_C7_d	1AF11	I/O	VCCIO3 (1.8V)	DOWN	NC	UART2_RX_M0
54	HDMI_TX_SCL_M1/I2C8_SCL_M1/SPI2_CSN0_M1/UART2_TX_M0/PDM_SDI0_M2/ETH1_TXCTL_M1/GPIO1_C6_d	1AE7	I/O	VCCIO3 (1.8V)	DOWN	NC	UART2_TX_M0
56	SPI1_MOSI_M2/UART8_TX_M0/VO_EBC_SDDO5/VO_LCDC_D5/DSMC_DATA3/GPIO3_C6_d	AJ33	I/O	VCCIO5 (3.3V)	DOWN	UART8_TX_M0	UART8_TX_M0
58	SATA0_CPPOD_M1/SPI1_CLK_M2/UART8_RTSN_M0/VO_EBC_SDDO4/VO_LCDC_D4/DSMC_DATA2/GPIO3_C7_d	AC32	I/O	VCCIO5 (3.3V)	DOWN	UART8_RTSN_M0	UART8_RTSN_M0
60	PWM2_CH2_M3/SPI1_MISO_M2/UART8_RX_M0/VO_EBC_SDDO6/VO_LCDC_D6/DSMC_DATA4/GPIO3_C5_d	AJ32	I/O	VCCIO5 (3.3V)	DOWN	UART8_RX_M0	UART8_RX_M0
62	SATA0_CPDET_M1/PWM2_CH3_M3/SPI1_CSN0_M2/UART8_CTSN_M0/VO_EBC_SDDO3/VO_LCDC_D3/DSMC_DATA1/GPIO3_D0_d	1AB22	I/O	VCCIO5 (3.3V)	DOWN	UART8_CTSN_M0	UART8_CTSN_M0
64	UART2_CTSN_M1/SPI4_MOSI_M2/UART11_RX_M1/PDM_SDI2_M1/SAI1_SDI2_M0/SAI1_SDO2_M0/VI_CIF_D10/ETH0_PPSTRIG/GPIO2_B7_d	A4	I/O	VCCIO4 (1.8V)	DOWN	NC	UART11_RX_M1
66	UART2_RTSN_M1/SPI4_CLK_M2/UART11_TX_M1/PDM_SDI3_M1/SAI1_SDI3_M0/SAI1_SDO1_M0/VI_CIF_D11/ETH0_PPSCLK/GPIO2_B6_d	1D3	I/O	VCCIO4 (1.8V)	DOWN	NC	UART11_TX_M1
68	GND15		G			GND	GND
70	SPI4_CLK_M1/VO_EBC_SDDO2/VO_LCDC_D2/DSMC_CSN2/ETH1_RXCLK_M0/GPIO3_D1_z SATA0_MPSWIT_M1/CAN0_TX_M3/I2C5_SCL_M3/SPI2_CSN0_M2/UART4_TX_M0/SAI2_MCLK_M1/VO_EBC_SDDO7/VO_LCDC_D7/DSMC_DATA5/GPIO3_C4_d	1V20	I/O	VCCIO5 (3.3V)	DOWN	I2S0_MCLK_M0	GMAC1_RXCLK_M0



接口定义 Interface definition

PIN	iCORE-3572Q38 pin definition (J2)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
72	I2C8_SDA_M3/UART3_RX_M1/PDM_SDI1_M4/VO_EBC_S DCE1/VO_LCDC_D17/DSMC_DATA11/ETH1_RXD0_M0/G PIO3_B2_d	1Y22	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_RXD0_M0	GMAC1_RXD0_M0
74	UART10_RTSN_M0/UART1_TX_M2/PDM_CLK1_M4/VO_E BC_VCOM/VO_LCDC_D20/DSMC_DATA13/ETH1_RXCTL_ M0/GPIO3_A7_d	1Y21	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_RXCTL_M0	GMAC1_RXCTL_M0
76	PWM1_CH3_M3/SPI4_CSN0_M1/UART10_TX_M0/PDM_C LK0_M4/VO_EBC_SDCE2/VO_LCDC_D18/DSMC_DATA12/ ETH1_RXD1_M0/GPIO3_B1_d	AG32	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_RXD1_M0	GMAC1_RXD1_M0
78	PWM2_CH5_M3/SPI3_CSN1_M1/UART2_CTSN_M2/SAI2_ SDO_M1/VO_EBC_SDDO0/VO_LCDC_D0/DSMC_CSN0/ET H1_RXD2_M0/GPIO3_D3_z	AF32	I/O	VCCIO5 (3.3V)	DOWN	I2S0_SDO0_M0	GMAC1_RXD2_M0
80	PWM2_CH4_M3/SPI4_CSN1_M1/UART2_RTSN_M2/SAI2_ SDI_M1/VO_EBC_SDDO1/VO_LCDC_D1/DSMC_CSN3/ET H1_RXD3_M0/GPIO3_D2_z	AF33	I/O	VCCIO5 (3.3V)	DOWN	I2S0_SDI0_M0	GMAC1_RXD3_M0
82	PWM0_CH0_M3/SPI2_MOSI_M2/UART10_RX_M0/SAI1_ MCLK_M1/VO_EBC_SDCE3/VO_LCDC_D19/DSMC_CSN1/ ETH1_MCLK_M0/GPIO3_B0_z	1AB21	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_MCLK_M0	GMAC1_MCLK_M0
84	PWM0_CH1_M2/SPI3_CSN0_M1/SPDIF_TX1_M3/VO_EBC _SDDO13/VO_LCDC_D13/DSMC_DQS1/ETH1_TXCLK_M0 /GPIO3_B6_d	1Y23	I/O	VCCIO5 (3.3V)	DOWN	PHY_INT/GPIO0_B3	GMAC1_TXCLK_M0
86	I2C8_SCL_M3/UART3_TX_M1/PDM_SDI0_M4/VO_EBC_SD CE0/VO_LCDC_D16/DSMC_DATA10/ETH1_TXCTL_M0/GP IO3_B3_d	1Y20	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_TXCTL_M0	GMAC1_TXCTL_M0
88	PWM1_CH5_M2/UART3_CTSN_M1/SPDIF_TX0_M3/VO_E BC_SDDO14/VO_LCDC_D14/DSMC_DATA8/ETH1_TXD0_ M0/GPIO3_B5_d	1V21	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_TXD0_M0	GMAC1_TXD0_M0
90	CAN3_TX_M2/PWM2_CH1_M2/I2C9_SDA_M3/SPI4_MOSI _M1/UART4_CTSN_M0/SAI2_LRCK_M1/VO_EBC_SDDO8/ VO_LCDC_D8/DSMC_INT3/ETH1_TXD2_M0/GPIO3_C3_d	AE32	I/O	VCCIO5 (3.3V)	DOWN	I2S0_LRCK_TX_M0	GMAC1_TXD2_M0
92	PWM1_CH4_M3/UART3_RTSN_M1/SPDIF_RX0_M3/VO_E BC_SDDO15/VO_LCDC_D15/DSMC_DATA9/ETH1_TXD1_ M0/GPIO3_B4_d	AE33	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_TXD1_M0	GMAC1_TXD1_M0

接口定义 Interface definition



PIN	iCORE-3572Q38 pin definition (J2)	RK3572 Pin NO.	Pin type	IO Power domain	IO Pull	Function for Main BOARD (EXT-ICORE-3576Q38)	Default function description
94	CAN3_RX_M2/PWM2_CH0_M2/I2C9_SCL_M3/SPI4_MISO_M1/UART4_RTSN_M0/SAI2_SCLK_M1/VO_EBC_SDDO9/VO_LCDC_D9/DSMC_INT1/ETH1_TXD3_M0/GPIO3_C2_d	AD32	I/O	VCCIO5 (3.3V)	DOWN	I2S0_SCLK_TX_M0	GMAC1_TXD3_M0
96	I3C_SCL_M0/I2C0_SCL_M1/UART8_TX_M1/GPIO0_C1_d	Y33	I/O	3.3V	DOWN	GMAC0_RSTn	GMAC1_RSTn
98	PWM1_CH2_M3/UART10_CTSN_M0/UART1_RX_M2/PDM_SDI2_M4/VO_EBC_GDOE/VO_LCDC_D21/DSMC_DATA14/ETH1_MDC_M0/GPIO3_A6_d	1V23	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_MDC_M0	GMAC1_MDC_M0
100	PWM1_CH1_M2/SPI2_CSN1_M2/UART1_RTSN_M2/PDM_SDI3_M4/VO_EBC_GDSP/VO_LCDC_D22/DSMC_DATA15/ETH1_MDIO_M0/GPIO3_A5_d	1V22	I/O	VCCIO5 (3.3V)	DOWN	GMAC0_MDIO_M0	GMAC1_MDIO_M0



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