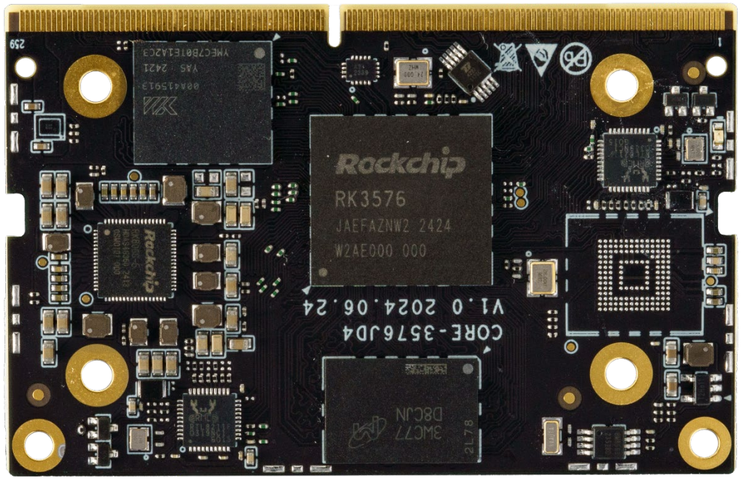
Core-3576JD4

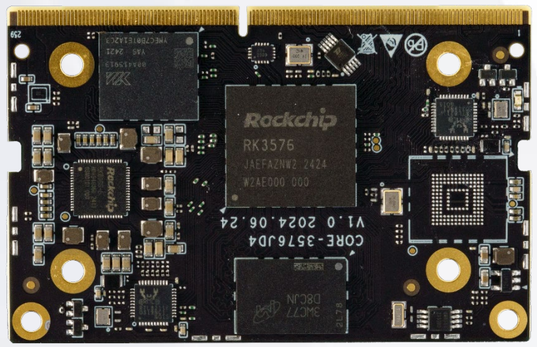
Low-power Large-model Core Board



V1.0 2025-1-21

T-CHIP INTELLIGENCE TECHNOLOGY

Product features



High-performance Octa-core 64-bit AIOT processor, RK3576

RK3576, the new octa-core 64-bit AIOT processor, features a big.LITTLE architecture (4×A72 +4×A53), an advanced lithography process, and a frequency of up to 2.2 GHz. It ensures strong support for high-performance computing and multitasking.

4K@120fps high frame rate video decoding

It supports 4K@120fps decoding (H.265/HEVC, VP9, AVS2, and AV1), 4K@60fps decoding (H.264/AVC), and 4K@60fps encoding (H.265/HEVC and H.264/AVC).

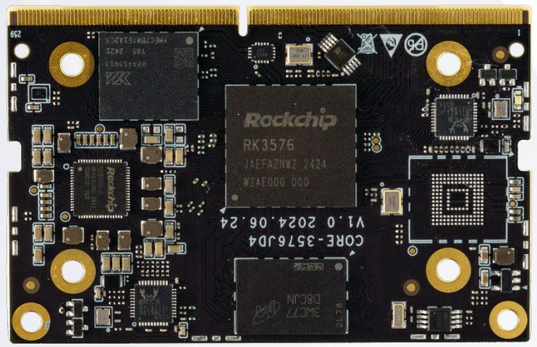
EPD display with algorithms to improve image quality

EBC output interface (E-ink EPD (Electronic Paper Display)) with a resolution of up to 1920×2560 is suitable for EINK e-readers. Combining RK PQ with multiple enhanced image quality algorithms and multiple refresh modes to provide users with a better reading experience.

The private deployment of large language models

Support the private deployment of ultra-large-scale parameter models under the Transformer architecture, including large language models such as Gemma-2B, ChatGLM3-6B, Qwen-1.8B, Phi-3-3.8B. Support Docker container management technology.

Product features



Multiple deep learning frameworks

It supports traditional network architectures such as CNN, RNN, and LSTM, and supports the import and export of RKNN models; a variety of deep learning frameworks, TensorFlow, TensorFlow Lite, PyTorch, Caffe, ONNX and Darknet, as well as custom operator development.

Various operating systems and abundant resources

Support Android 14, Linux OS, and Buildroot. These provide safe and stable systems for product research and production. We offer SDKs, tutorials, technical documentation, and development tools to streamline and improve the development process.

Abundant expansion interfaces

It provides a rich array of expansion interfaces such as PCIe2.1, SATA3.1, SAI, I2C, I3C, CAN, UART, SPDIF, SDIO3.0, MIPI-CSI, USB3.0, USB2.0, SPI, and GPIO to meet peripheral expansion needs for various applications.

Wide range of application scenarios

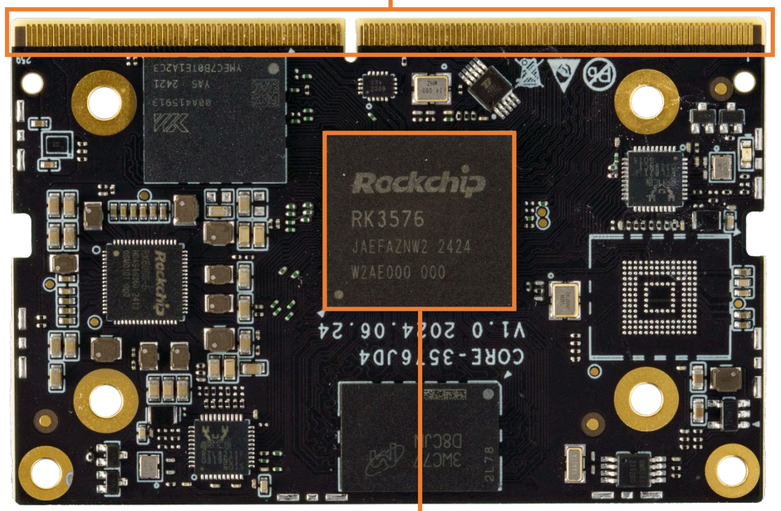
It is widely used in edge computing, local deployment of large models, intelligent digital signage, cloud terminal products, industrial PCs, automotive electronics, and more.

Specifications

Specifications		
Basic Specifications	SOC	Rockchip RK3576
	CPU	Octa-core 64-bit processor (4×A72 + 4×A53) with a maximum frequency of 2.2GHz
	GPU	G52 MC3@1GHz, support OpenGL ES 1.1/2.0/3.2, OpenCL 2.0, Vulkan 1.1, embedded with high-performance 2D acceleration hardware
	NPU	6 TOPS NPU, support mixed operations of INT4/8/16/FP16/BF16/TF32
	ISP	Built-in 16 million pixel ISP, support low-light noise reduction, support RGB-IR sensor, support up to 120dB HDR, AI-ISP to improve low-noise image effect.
	Decoding/ Encoding	Decoding: 4K@120fps (H.265/HEVC, VP9, AVS2, AV1), 4K@60fps (H.264/AVC) Encoding: 4K@60fps (H.265/HEVC, H.264/AVC)
	RAM	LPDDR4/LPDDR4x (4GB/8GB/16GB optional)
	Storage	eMMC (16GB/32GB/64GB/128GB/256GB optional), UFS2.0 (Optional)
	Power	5.0V (voltage tolerance ± 5%)
	OS	Android14, Linux OS, Buildroot
	Power consumption	Max: 7.0W(5V/1400mA), Normal: 1.1W(5V/220mA), Min: 0.025W(5V/5mA)
	Software Support	· Support the privatization deployment of ultra-large-scale parametric models under the Transformer architecture, such as Gemma-2B, ChatGLM3-6B, Qwen-1.8B, Phi-3-3.8B and other large language models · It supports traditional network architectures such as CNN, RNN, and LSTM, and supports the import and export of RKNN models; Support a variety of deep learning frameworks, including TensorFlow, TensorFlow Lite, PyTorch, Caffe, ONNX and Darknet. It also supports the development of custom operators · Support Docker container management technology
	Size	69.6mm × 45mm × 4.15mm
	Weight	≈16g
	Environment	Operating Temperature: -20℃ ~ 60℃, Storage Temperature: -20℃ ~ 70℃, Storage Humidity: 10% ~ 90%RH (non-condensing)
Interface Specifications	Internet	2 × Gigabit Ethernet (MDI interface is provided, and the core board has an onboard Ethernet PHY chip) Expandable WiFi6/Bluetooth 5.2 via SDIO3.0/PCIe3.0 Expandable 5G/4G LTE via USB3.2 Gen1/USB2.0
	Video input	2 × MIPI CSI DPHY (1×4 Lanes or 2×2 Lanes) 1 × MIPI D/CPHY (MIPI DPHY (1×4 Lanes or 2×2 Lanes) or MIPI CPHY (3 Lanes)) 1 × DVP (8/10/12/16-bit, BT.601/BT.656 and BT.1120)
	Video output	1 × HDMI2.1(4K@120fps)/eDP1.3(4K@60fps) 1 × DP1.4 (4K@120fps) 1 × EBC Output interface (Support E-ink EPD (Electronic Paper Display), 2560 ×1920)
	Audio	2 × SAI (4T/4R), 3 × SAI (1T/1R), supports I2S/TDM/PCM mode and supports sampling rates up to 192KHz 2 × SPDIF TX & RX (8ch) 2 × PDM (Up to 8 channels, audio resolution 16~24 bits, sample rate up to 192KHz, support PDM master receive mode)
	PCIe/SATA	1 × PCIe 2.1/SATA 3.1/USB 3.2 Gen1 Combo interface 1 × PCIe 2.1/SATA 3.1 Combo interface
	USB	1 × USB3.2 Gen1 OTG0 (multiplexed with DP1.4) 1 × USB3.2 Gen1 OTG1 (multiplexed with PCIe 2.1/SATA 3.1)
	SDIO	1 × SDIO3.0
	PWM	16 × PWM
	SPI	5 × SPI (Supports serial master and serial slave modes, software configurable)
	I2C	10 × I2C (Supports 7-bit and 10-bit address modes, data rates up to 100kbps in standard mode and up to 400kbps in fast mode)
	I3C	2 × I3C (I2C compliant, SDR mode supported, up to 10 devices supported)
	UART	12 × UART (Support automatic flow control mode, support RS485 function)
	CAN	2 × CAN FD (Supports 8192 bit receive FIFO)
	Watchdog	Support External watchdog (On-board watchdog IC)
	SARADC	7 × SARADC + 1 × SARADC (boot only), supports 12-bit resolution, up to 1MS/s sampling rate

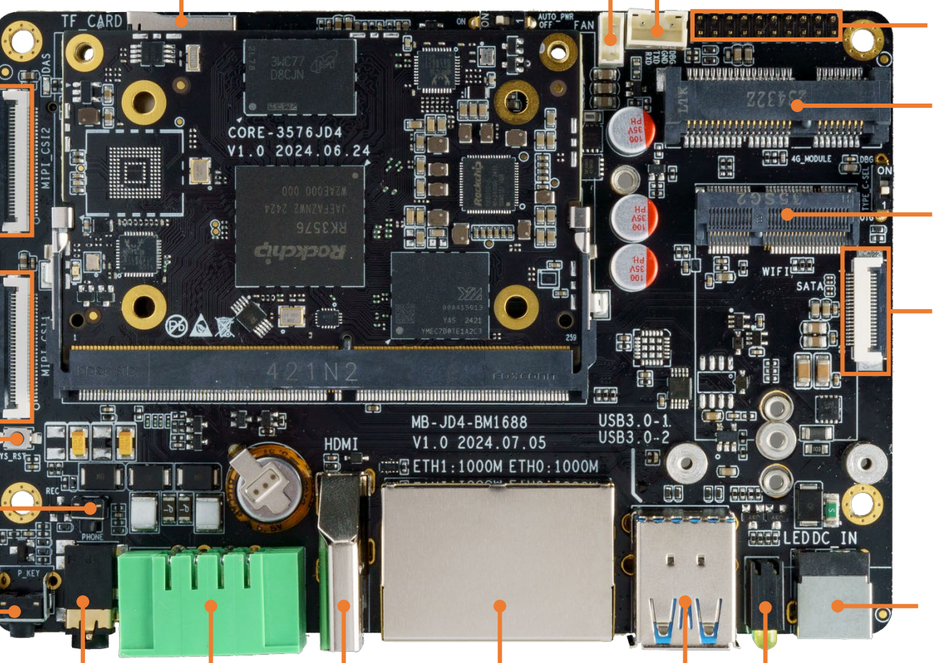
Core board Interface description

Goldfinger
(260 PIN, SODIMM, 0.5mm pitch)



Rockchip RK3576
(Main frequency is up to 2.2GHz)

Mainboard Interface description



TF Card

FAN (4Pin-1.25mm)

Debug (3Pin-2.0mm)

Double-row pin headers
(2×10-20PIN-2.0mm)

Mini PCIe
(4G/3G)

M.2 E-KEY
(Extended WiFi/Bluetooth Module)

No function, no paste by default

MIPI CSI D/C PHY
(DPHY(1×4 Lanes) or CPHY(3 Lanes), 30Pin-0.5mm)

MIPI CSI DPHY
(1×4 lanes/2×2 lanes, 30Pin-0.5mm)

Reset

Recovery

Power button

DC 12V
(5.5mm×2.1mm,
support 12V~24V wide voltage input)

3.5mm Audio jack
(Support Mic recording, CTIA)

Phoenix connector
(2×4Pin, 3.5mm Pitch)

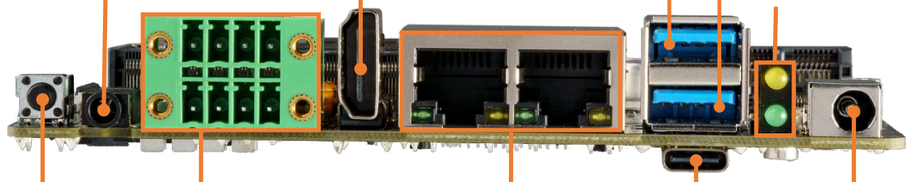
HDMI2.1
(4K@120fps)

2×RJ45 Ethernet port
(1000Mbps)

2×USB3.0

Work indicator

Mainboard Interface description



3.5mm Audio jack
(Support Mic recording, CTIA)

HDMI2.1
(4K@120fps)

USB3.0(1)
(Multiplexing with 5G)

USB3.0(2)
(Max: 1A)

Work indicator

Power button

2×RJ45 Ethernet port
(1000Mbps)

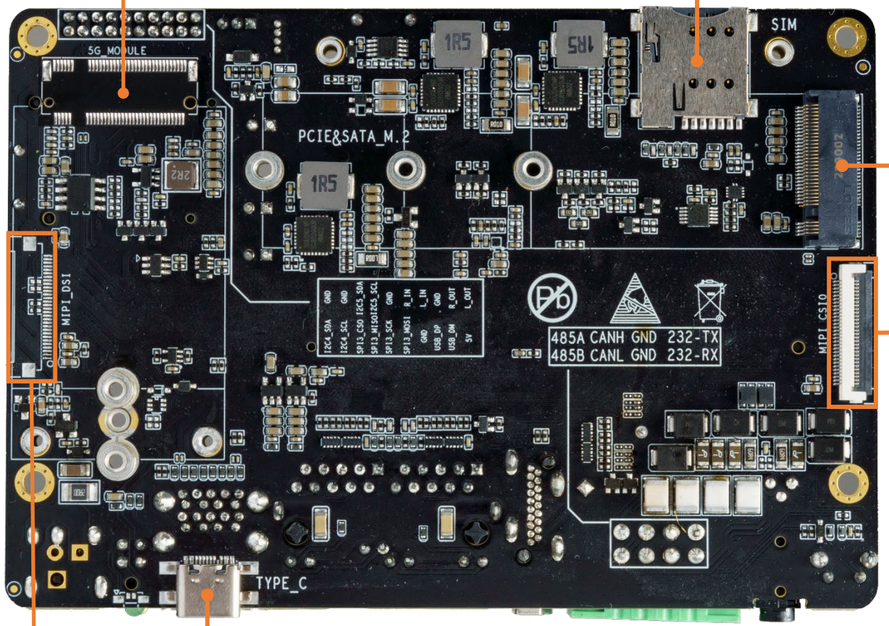
Type-C
(OTG/flash)

DC 12V
(5.5mm×2.1mm, support 12V~24V wide voltage input)

Phoenix connector(2×4Pin, 3.5mm Pitch)
232-TX GND CANH 485A
232-RX GND CANL 485B

M.2 B-KEY
(Expand 5G, not pasted by default, reused with 4G and USB3.0(1))

SIM Card



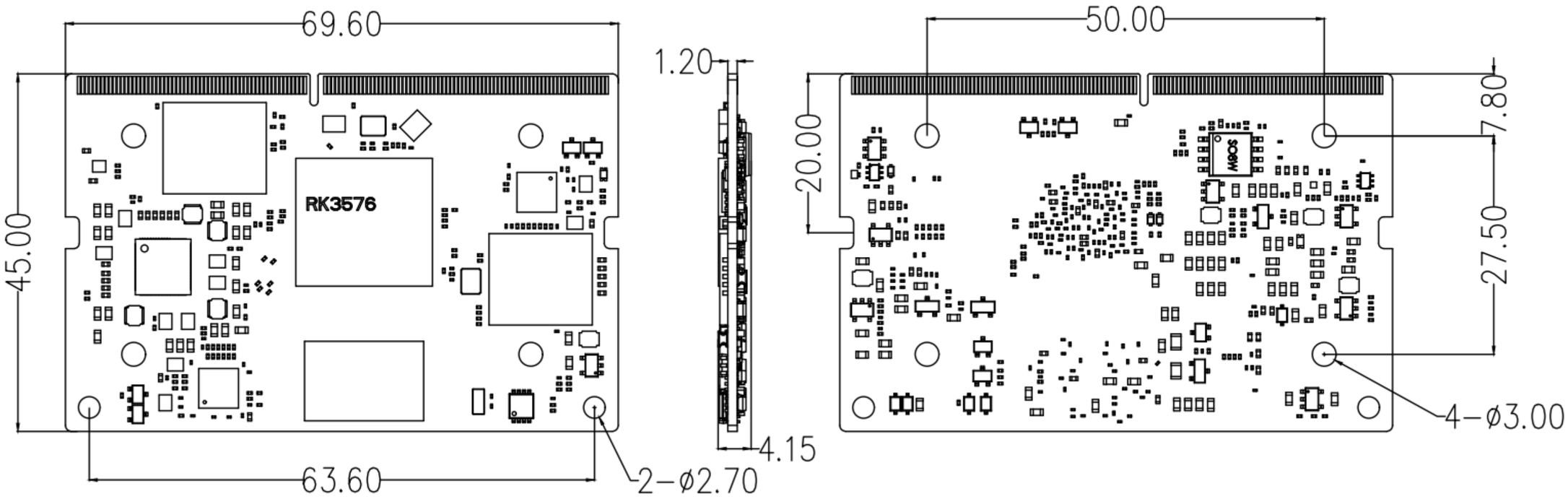
No function, no paste by default

MIPI CSI DPHY
(1×4 lanes/2×2 lanes, 30Pin-0.5mm)

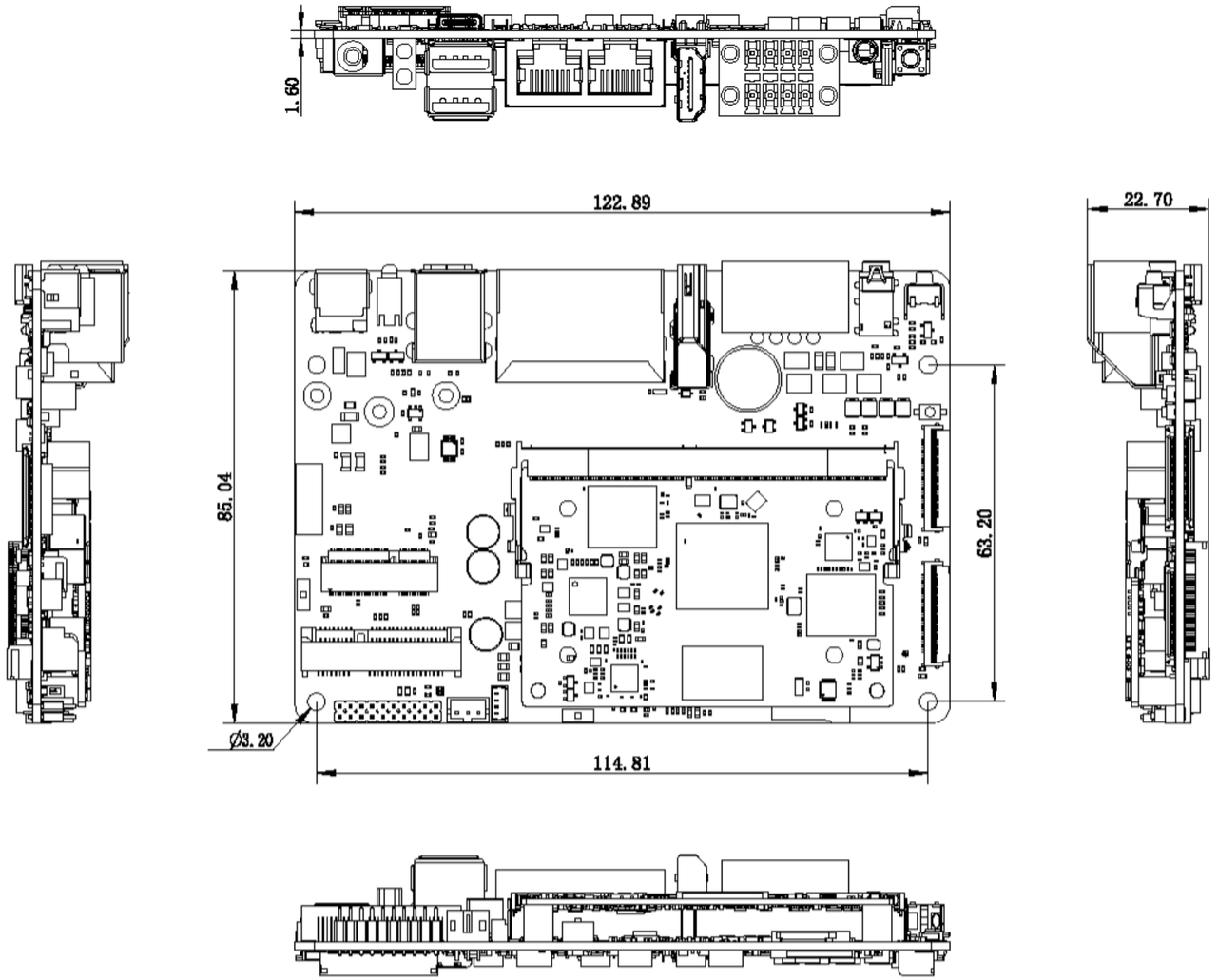
No function, no paste by default

Type-C
(OTG/flash)

Core board Dimension



Mainboard Dimension



Interface definition

① : Pad types: I = input, O = output, I/O = input/output (bidirectional), G= Ground ,
P = power supply , DOWN = Internal pull down , UP = Internal pull UP L = Lowe Level H = High level”

PIN	CORE-3576JD4 pin definition	RK3576 Pin NO.	Pad type	IO Power domain	IO Pull	Function for Main BOARD (MB-JD4-BM1688)	Defual function description
1	GND		G			GND	GND
3	MIPI_DPHY_CSI1_RX_D2N/MIPI_DPHY_CSI2_RX_D0N	AG28	I/O	-		CSI1_D0_N	CSI1_D0_N
5	MIPI_DPHY_CSI1_RX_D2P/MIPI_DPHY_CSI2_RX_D0P	AG29	I/O	-		CSI1_D0_P	CSI1_D0_P
7	GND		G			GND	GND
9	MIPI_DPHY_CSI2_RX_CLKN	1AD22	I/O	-		CSI1_CLK_N	CSI1_CLK_N
11	MIPI_DPHY_CSI2_RX_CLKP	1AD21	I/O	-		CSI1_CLK_P	CSI1_CLK_P
13	GND		G			GND	GND
15	MIPI_DPHY_CSI1_RX_D3N/MIPI_DPHY_CSI2_RX_D1N	AH28	I/O	-		CSI1_D1_N	CSI1_D1_N
17	MIPI_DPHY_CSI1_RX_D3P/MIPI_DPHY_CSI2_RX_D1P	AH29	I/O	-		CSI1_D1_P	CSI1_D1_P
19	GND		G			GND	GND
21	MIPI_DPHY_CSI3_RX_D2N/MIPI_DPHY_CSI4_RX_D0N	K29	I/O	-		CSI3_D0_N	CSI3_D0_N
23	MIPI_DPHY_CSI3_RX_D2P/MIPI_DPHY_CSI4_RX_D0P	K28	I/O	-		CSI3_D0_P	CSI3_D0_P
25	GND		G			GND	GND
27	MIPI_DPHY_CSI4_RX_CLKN	1K23	I/O	-		CSI3_CLK_N	CSI3_CLK_N
29	MIPI_DPHY_CSI4_RX_CLKP	1K22	I/O	-		CSI3_CLK_P	CSI3_CLK_P

Interface definition

31	GND		G			GND	GND
33	MIPI_DPHY_CSI3_RX_D3N/MIPI_DPHY_CSI4_RX_D1N	L29	I/O	-		CSI3_D1_N	CSI3_D1_N
35	MIPI_DPHY_CSI3_RX_D3P/MIPI_DPHY_CSI4_RX_D1P	L28	I/O	-		CSI3_D1_P	CSI3_D1_P
37	GND		G			GND	GND
39	USB3_OTG0_SSRX1N	AL10	I/O	-		USB30_RX_N	USB30_RX_N
41	USB3_OTG0_SSRX1P	AK10	I/O	-		USB30_RX_P	USB30_RX_P
43	GND		G			GND	GND
45	USB3_OTG0_SSTX1N	AK11	I/O	-		USB30_TX_N	USB30_TX_N
47	USB3_OTG0_SSTX1P	AL11	I/O	-		USB30_TX_P	USB30_TX_P
49	GND		G			GND	GND
51	MIPI_DPHY_CSI0_RX_D2N/MIPI_CPHY_CSI_RX_TRIO2_A	AL23	I/O	-		CSI4_D2_N	CSI4_D2_N
53	MIPI_DPHY_CSI0_RX_D2P/MIPI_CPHY_CSI_RX_TRIO2_B	AK23	I/O	-		CSI4_D2_P	CSI4_D2_P
55	GND		G			GND	GND
57	PWM2_CH6_M2/I3C1_SDA_PU_M0/UART9_RTSN_M0 /SPDIF_RX0_M2/ SAI3_MCLK_M2/ETH0_MCLK_M1/ETH_CLK1_25M_OUT_M0/CAM_CLK1_OUT_M1/GPIO2_D6_d	1D18	I/O	1.8V	DOWN	PWM2_CH6_M2	PWM_FAN_TACH
59	PWM2_CH7_M2/SPI3_CSN1_M0/UART9_CTSN_M0/SPDIF_TX0_M2/SAI0_SDO3_M0/ ETH_CLK0_25M_OUT_M1/ETH1_MCLK_M0/CAM_CLK2_OUT_M1/GPIO2_D7_d	1E15	I/O	1.8V	DOWN	CAM5_PWDN	CAM5_PWDN
61	GND		G			GND	GND
63	HDMI_TX_D2N/EDP_TX_D2N	AK29	O	-		HDMI_TX2N	HDMI_TX2N

Interface definition

65	HDMI_TX_D2P/EDP_TX_D2P	AJ28	O	-		HDMI_TX2P	HDMI_TX2P
67	GND		G			GND	GND
69	HDMI_TX_D1N/EDP_TX_D1N	AL28	O	-		HDMI_TX1N	HDMI_TX1N
71	HDMI_TX_D1P/EDP_TX_D1P	AK28	O	-		HDMI_TX1P	HDMI_TX1P
73	GND		G			GND	GND
75	HDMI_TX_D0N/EDP_TX_D0N	AK27	O	-		HDMI_TX0N	HDMI_TX0N
77	HDMI_TX_D0P/EDP_TX_D0P	1AE24	O	-		HDMI_TX0P	HDMI_TX0P
79	GND		G			GND	GND
81	HDMI_TX_D3N/EDP_TX_D3N	AK26	O	-		HDMI_TX_D3N	HDMI_TX_D3N
83	HDMI_TX_D3P/EDP_TX_D3P	AL26	O	-		HDMI_TX_D3P	HDMI_TX_D3P
85	GND		G			GND	GND
87	PWM1_CH0_M3/SPI2_CLK_M2/UART1_CTSN_M2/FLEXBUS0_CSN_M0/VO_EBC_SDSHR VO_LCDC_D23/ GPIO3_A4_d	1D13	I/O	1.8V	DOWN	WD_EN_H	WD_EN_H
89	SATA0_ACTLED_M0/SPI4_MOSI_M3/UART7_CTSN_M0/PDM0_SDI0_M3/SAI0_SDI3_M0 / ETH0_TXD2_M1/SDMMC1_PWREN_M1/VI_CIF_D9/GPIO2_B4_d	A19	I/O	1.8V	DOWN	SPI4_MOSI	SPI4_MOSI
91	PCIE1_CLKREQN_M0/ SPI4_CLK_M3/UART1_RTSN_M1/PDM0_CLK1_M3/SAI0_SDO2_M0 / ETH0_TXCLK_M1 / SDMMC1_CLK_M1/ VI_CIF_D10 /GPIO2_B3_d	1B16	I/O	1.8V	DOWN	SPI4_CLK	SPI4_CLK
93	SATA1_ACTLED_M0/SPI4_MISO_M3 /UART7_RTSN_M0 / PDM0_CLK0_M3 /SAI0_MCLK_M0 / ETH0_RXCLK_M1 / SDMMC1_DETN_M1 / VI_CIF_D8/ GPIO2_B5_d	1C18	I/O	1.8V	DOWN	SPI4_MISO	SPI4_MISO
95	PCIE0_CLKREQN_M0 / SPI4_CSN0_M3 / UART1_CTSN_M1 / PDM0_SDI1_M3 / SAI0_SDI2_M0 / ETH0_TXD3_M1 / SDMMC1_CMD_M1/ VI_CIF_D11 /GPIO2_B2_d	1A17	I/O	1.8V	DOWN	SPI4_CSN0	SPI4_CSN0

Interface definition

97	I2C4_SCL_M2 / SPI4_CSN1_M3 /UART8_TX_M1/SAI0_SDO0_M0 / ETH0_RXD0_M1 / SDMMC1_D0_M1 / VI_CIF_D15 / GPIO2_A6_d	B22	I/O	1.8V	DOWN	CAM3_PWDN	CAM3_PWDN
99	I2C7_SCL_M1/SPI3_CLK_M0/UART3_TX_M0/SAI3_SCLK_M2 / ETH0_MDIO_M1/VI_CIF_HREF / GPIO3_A0_d	1D16	I/O	1.8V	DOWN	UART3_TX	UART3_TX
101	I2C7_SDA_M1 /SPI3_MOSI_M0 /UART3_RX_M0/SAI3_LRCK_M2 /ETH0_MDC_M1/ETH1_PPSTRIG_M0 /VI_CIF_VSYNC /GPIO3_A1_d	1B18	I/O	1.8V	DOWN	UART3_RX	UART3_RX
103	CAN1_RX_M3 / SPI3_CSN0_M0 /UART3_RTSN_M0 /SPDIF_TX1_M1 / SAI3_SDI_M2 / ETH0_RXD1_M1 / ETH1_PTP_REFCLK_M0 /VI_CIF_CLKI/ GPIO3_A3_d	1A19	I/O	1.8V	DOWN	UART3_RTSN	UART3_RTSN
105	MIPI_TE_M1 / CAN1_TX_M3 / SPI3_MISO_M0 / UART3_CTSN_M0 / SPDIF_RX1_M1 /SAI3_SDO_M2 / ETH0_RXCTL_M1 / ETH1_PPSCLK_M0 / VI_CIF_CLKO / GPIO3_A2_d	1A20	I/O	1.8V	DOWN	NC	UART3_CTSN
107	GND		G			GND	GND
109	USB2_OTG0_DM	AL9		-		USB0_D_N	USB0_D_N
111	USB2_OTG0_DP	AK9		-		USB0_D_P	USB0_D_P
113	GND		G			GND	GND
115	USB2_HOST1_DM (GPIO0_A5: L)	2T5		-		USB1_D_N	USB1_D_N (default USB1)
117	USB2_HOST1_DP (GPIO0_A5: L)	2T4		-		USB1_D_P	USB1_D_P N (default USB1)
119	GND		G			GND	GND
121	USB2_HOST1_DM (GPIO0_A5: H) (optional) Default:NC	2T5	I/O	-		USB2_DN(Options)	USB2_DN (Options) --GPIO178=H
123	USB2_HOST1_DP (GPIO0_A5: H) (optional) Default:NC	2T4	I/O	-		USB2_DP(Options)	USB2_DP (Options) --GPIO178=H
125	GND		G			GND	GND
127	UART3_RX_M2/PDM0_CLK0_M2/SAI3_MCLK_M1/SDMMC1_CLK_M0/ETH1_TXCLK_M1/GPIO1_C1_d	1B22	I/O	1.8V	DOWN	CAM0_RESET	CAM0_RESET OUTPUT ,Active L
129	GND		G			GND	GND

Interface definition

131	PCIE0_RXN /SATA0_RXN	R29	I	-		PCIE0_RXN /SATA0_RXN	PCIE0_RXN
133	PCIE0_RXP / SATA0_RXP	R28	I	-		PCIE0_RXP / SATA0_RXP	PCIE0_RXP
135	GND		G			GND	GND
137	NC		I	-		NC	NC
139	NC		I	-		NC	NC
141	GND		G			GND	GND
143	CAN0_RX_M2/I2C4_SDA_M1/UART6_RX_M0/SPI3_MISO_M2/FLEXBUS0_D14_M1/PDM1_CLK0_M1/SAI4_LRCK_M0/GPIO4_A6_d	1B5	I/O	3.3V	DOWN	CAN0_RX	CAN0_RX
145	CAN0_TX_M2/I2C4_SCL_M1/UART6_TX_M0/SPI3_MOSI_M2/FLEXBUS0_D13_M1/PDM1_SDI3_M1/SAI4_SCLK_M0/GPIO4_A4_d	1C5	I/O	3.3V	DOWN	CAN0_TX	CAN0_TX
147	SARADC_VIN4	1E18	I	1.8V		ADC4	ADC4 INPUT
149	I2C8_SCL_M1/UART2_TX_M0/PDM0_SDI0_M2/FSPI1_D2_M1/ETH1_TXCTL_M1/GPIO1_C6_d	A26	I/O	1.8V	DOWN	NC	UART2_TX
151	I2C8_SDA_M1/UART2_RX_M0/PDM0_SDI1_M2/FSPI1_D3_M1/ETH1_RXD0_M1/GPIO1_C7_d	1C22	I/O	1.8V	DOWN	NC	UART2_RX
153	GND		G			GND	GND
155	NC			-		NC	NC
157	NC			-		NC	NC
159	GND		G			GND	GND
161	PCIE1_RXN/SATA1_RXN/USB3_HOST1_SSRXN (Default: GPIO0_D1 :H)	M29	I	-		USBSS0_RX_N	USBSS0_RX_N (Default:)
163	PCIE1_RXP /SATA1_RXP/USB3_HOST1_SSRXP (Default: GPIO0_D1 :H)	M28	I	-		USBSS0_RX_P	USBSS0_RX_P (Default:)
165	GND		G			GND	GND

Interface definition

167	PCIE1_RXN/SATA1_RXN/USB3_HOST1_SSRXN (GPIO0_D1 :L) (optional) Default:NC	M29	I	-		PCIE1_RXN/SATA1_RXN	PCIE1_RXN/SATA1_RXN (optional)
169	PCIE1_RXP /SATA1_RXP/USB3_HOST1_SSRXP (GPIO0_D1 :L) (optional) Default:NC	M28	I	-		PCIE1_RXP/SATA1_RXP	PCIE1_RXP/SATA1_RXP (optional)
171	GND		G			GND	GND
173	PCIE1_REFCLKN	1M23	O	-		PCIE1_REFCLKN	PCIE1_REFCLKN
175	PCIE1_REFCLKP	1L23	O	-		PCIE1_REFCLKP	PCIE1_REFCLKP
177	GND		G			GND	GND
179	PCIE_WAKE* (PCIE0_WAKEN_M2/PCIE1_WAKEN_M2)	D8/1C6	O	3.3V		PCIE_WAKE*	PCIE_WAKE*
181	PWM2_CH7_M0/SPI3_CSN1_M2/SPI4_CSN0_M2/PDM1_SDI0_M1/SAI4_SDO_M0/SAI1_SDI0_M0/GPIO4_B3_d	1A6	I/O	3.3V	DOWN	PCIE0_RST*	PCIE0_RST*
183	MIPI_TE_M0/SPI4_MISO_M2/FLEXBUS1_D15_M1/PDM1_SDI1_M1/SAI1_SDI1_M0/SAI1_SDO3_M0/GPIO4_B2_d	B6	I/O	3.3V	DOWN	PCIE1_RST*	PCIE1_RST*
185	PWM2_CH6_M1/UART6_TX_M3/SPI4_CSN0_M0/I2C3_SCL_M3/DP_HPDIN_M0/SAI4_LRCK_M2/ISP_PRELIGHT_TRIG_M1/GPIO4_C4_d	AL3	I/O	3.3V	DOWN	I2C3_SCL	I2C3_SCL (Core board pull up resistance 2.2K)
187	PWM2_CH5_M1/UART6_RX_M3/SPI4_MOSI_M0/I2C3_SDA_M3/SATA1_ACTLED_M1/PCIE0_WAKEN_M3/VP0_SYNC_OUT/SAI4_SDO_M2/ISP_FLASH_TRIGOUT_M1/GPIO4_C5_d	AK1	I/O	3.3V	DOWN	I2C3_SDA	I2C3_SDA (Core board pull up resistance 2.2K)
189	I2C0_SCL_M0/SPI2_CSN0_M0/AUPLL_CLK_IN_M1/GPIO0_B0_z	1U22	I/O	3.3V		I2C0_SCL	I2C0_SCL
191	I2C0_SDA_M0/SPI2_MISO_M0/GPIO0_B1_z	1P23	I/O	3.3V		I2C0_SDA	I2C0_SDA
193	I3C0_SDA_PU_M0/UART10_RX_M2/DP_HPDIN_M1/SAI0_SDO0_M1/GPIO0_C5_d	1AA22	I/O	1.8V	DOWN	SAI0_SDO0_M1	I2S0_DOUT
195	SPI0_MOSI_M0/PDM0_SDI0_M0/SAI0_SDI0_M1/GPIO0_D0_d	1W23	I/O	1.8V	DOWN	SAI0_SDI0_M1	I2S0_DIN
197	SPI0_CLK_M0/I2C3_SDA_M1/SAI0_LRCK_M1/GPIO0_C7_d	1Y23	I/O	1.8V	DOWN	SAI0_LRCK_M1	I2S0_FS
199	SPI0_CSN0_M0/I2C3_SCL_M1/SAI0_SCLK_M1/GPIO0_C6_d	1Y21	I/O	1.8V	DOWN	SAI0_SCLK_M1	I2S0_SCLK

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201	GND		G			GND	GND
203	SPI1_MOSI_M2/UART8_TX_M0/FLEXBUS1_D5/DSMC_DATA3/SAI1_LRCK_M1/VO_EBC_SDDO5/VO_LCDC_D5/GPIO3_C6_d	1D7	I/O	1.8V	DOWN	UART8_TX_M0	UART8_TX
205	PWM2_CH2_M3/SPI1_MISO_M2/UART8_RX_M0/FLEXBUS1_D6/DSMC_DATA4/SAI1_SDO0_M1/VO_EBC_SDDO6/VO_LCDC_D6/GPIO3_C5_d	1B9	I/O	1.8V	DOWN	UART8_RX_M0	UART8_RX
207	SPI1_CLK_M2/UART8_RTSN_M0/FLEXBUS1_D4/DSMC_DATA2/SAI1_SCLK_M1/VO_EBC_SDDO4/VO_LCDC_D4/GPIO3_C7_d	1C7	I/O	1.8V	DOWN	CAM1_RESET	CAM1_RESET OUTPUT ,Active L
209	PWM2_CH3_M3/SPI1_CSN0_M2/UART8_CTSN_M0/FLEXBUS1_D3/DSMC_DATA1/SAI1_MCLK_M1/VO_EBC_SDDO3/VO_LCDC_D3/GPIO3_D0_d	1C12	I/O	1.8V	DOWN	CAM2_RESET	CAM2_RESET OUTPUT ,Active L
211	PWM0_CH0_M0/UART10_TX_M2/PDM0_CLK0_M0/SAI0_MCLK_M1/GPIO0_C4_d	1W22	I/O	1.8V	DOWN	SAI0_MCLK_M1	I2S0_MCLK
213	PWM2_CH2_M1/CAN1_TX_M1/SPI4_MISO_M0/I2C6_SCL_M3/SATA0_ACTLED_M1/PCIE0_CLKREQN_M3/VP1_SYNC_OUT/SAI4_SDI_M2/GPIO4_C6_d	1AE1	I/O	3.3V	DOWN	I2C6_SCL_M3	I2C6_SCL (Core board pull up resistance 2.2K)
215	PWM2_CH3_M1/CAN1_RX_M1/SPI4_CLK_M0/I2C6_SDA_M3/VP2_SYNC_OUT/SAI4_SCLK_M2/GPIO4_C7_d	AJ1	I/O	3.3V	DOWN	I2C6_SDA_M3	I2C6_SDA (Core board pull up resistance 2.2K)
217	PWM0_CH0_M3/SPI2_MOSI_M2/UART10_RX_M0/FLEXBUS0_D8/DSMC_CSN1/SAI4_MCLK_M1/ETH0_MCLK_M0/VO_EBC_SDCE3/VO_LCDC_D19/GPIO3_B0_d	B14	I/O	1.8V	DOWN	NC	MODULE_ID (Core board pull down resistance 1K)
219	PWM2_CH2_M0/CAN0_RX_M0/SPI0_MOSI_M1/I2C8_SCL_M0/UART7_RX_M2/UART0_RX_M1/DSM_AUD_LP_M0/FSPI1_D0_M0/SDMMC0_D0/GPIO2_A0_d	B24	I/O	1.8V/3.3V	DOWN	SDMMC_DAT0	SDMMC_DAT0
221	PWM2_CH3_M0/CAN0_TX_M0/SPI0_MISO_M1/I2C8_SDA_M0/UART7_TX_M2/UART0_TX_M1/SAI3_MCLK_M3/DSM_AUD_LN_M0/FSPI1_D1_M0/SDMMC0_D1/GPIO2_A1_d	B25	I/O	1.8V/3.3V	DOWN	SDMMC_DAT1	SDMMC_DAT1
223	I3C1_SCL_M1/CAN1_RX_M0/SPI0_CSN1_M1/UART5_RTSN_M2/JTAG_TCK_M0/SAI3_LRCK_M3/DSM_AUD_RP_M0/FSPI1_D2_M0/SDMMC0_D2/GPIO2_A2_d	A23	I/O	1.8V/3.3V	DOWN	SDMMC_DAT2	SDMMC_DAT2
225	I3C1_SDA_M1/CAN1_TX_M0/UART5_CTSN_M2/JTAG_TMS_M0/SAI3_SDI_M3/DSM_AUD_RN_M0/FSPI1_D3_M0/SDMMC0_D3/GPIO2_A3_d	B23	I/O	1.8V/3.3V	DOWN	SDMMC_DAT3	SDMMC_DAT3
227	PWM2_CH4_M0/SPI0_CSN0_M1/I2C5_SDA_M0/UART5_RX_M2/SAI3_SDO_M3/FSPI1_CSN0_M0/SDMMC0_CMD/GPIO2_A4_d	1A21	I/O	1.8V/3.3V	DOWN	SDMMC_CMD	SDMMC_CMD
229	I3C1_SDA_PU_M1/SPI0_CLK_M1/I2C5_SCL_M0/UART5_TX_M2/TEST_CLK_OUT/SAI3_SCLK_M3/FSPI1_CLK_M0/SDMMC0_CLK/GPIO2_A5_d	1B21	I/O	1.8V/3.3V	DOWN	SDMMC_CLK	SDMMC_CLK (Core board series resistance 33R)
231	GND		G			GND	GND

Interface definition

233	PMIC_EXT_EN_OUT		O	5.0V		PMIC_EXT_EN_OUT	PMIC_EXT_EN_OUT
235	RTC_BAT		P	3.0V/5.0V		RTC_BAT	RTC_BAT INPUT
237	POWER_EN Input (Default:NC)		I	5.0V		NC	POWER_EN INPUT, Active H
239	NPOR (System Reset Input, Active L)	W28	I	1.8V		SYS_RESET*	SYS_RESET INPUT, Active L (Core board pull up resistance 10K)
241	GND		G	GND		GND	GND
243	GND		G	GND			
245	GND		G	GND			
247	GND		G	GND			
249	GND		G	GND			
251	VDD5V0_IN		P	5.0V		CORE_5V Normal: 5V/220mA Max.:5V/1400mA Min.:5V/5mA	Input Voltage 5.0V +/-5%
253	VDD5V0_IN		P	5.0V			
255	VDD5V0_IN		P	5.0V			
257	VDD5V0_IN		P	5.0V			
259	VDD5V0_IN		P	5.0V			
PIN	**CORE-3576JD4 pin definition**	**RK3576 Pin NO.**	**Pad type**	**IO Power domain**	**IO Pull**	**Function for Main BOARD (MB-JD4-BM1688)**	**Defual function description**
2	GND		G			GND	GND
4	MIPI_DPHY_CSI1_RX_D0N	AE28	I/O	-		CSI0_D0_N	CSI0_D0_N
6	MIPI_DPHY_CSI1_RX_D0P	AE29	I/O	-		CSI0_D0_P	CSI0_D0_P

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8	GND		G			GND	GND
10	MIPI_DPHY_CSI1_RX_CLKN	1AC23	I/O	-		CSI0_CLK_N	CSI0_CLK_N
12	MIPI_DPHY_CSI1_RX_CLKP	1AC22	I/O	-		CSI0_CLK_P	CSI0_CLK_P
14	GND		G			GND	GND
16	MIPI_DPHY_CSI1_RX_D1N	AF28	I/O	-		CSI0_D1_N	CSI0_D1_N
18	MIPI_DPHY_CSI1_RX_D1P	AF29	I/O	-		CSI0_D1_P	CSI0_D1_P
20	GND		G			GND	GND
22	MIPI_DPHY_CSI3_RX_D0N	H29	I/O	-		CSI2_D0_N	CSI2_D0_N
24	MIPI_DPHY_CSI3_RX_D0P	H28	I/O	-		CSI2_D0_P	CSI2_D0_P
26	GND		G			GND	GND
28	MIPI_DPHY_CSI3_RX_CLKN	1H23	I/O	-		CSI2_CLK_N	CSI2_CLK_N
30	MIPI_DPHY_CSI3_RX_CLKP	1H22	I/O	-		CSI2_CLK_P	CSI2_CLK_P
32	GND		G			GND	GND
34	MIPI_DPHY_CSI3_RX_D1N	J29	I/O	-		CSI2_D1_N	CSI2_D1_N
36	MIPI_DPHY_CSI3_RX_D1P	J28	I/O	-		CSI2_D1_P	CSI2_D1_P
38	GND		G			GND	GND
40	MIPI_DPHY_CSI0_RX_D0N/MIPI_CPHY_CSI_RX_TRIO0_A	AL20	I/O	-		CSI4_D0_N	CSI4_D0_N
42	MIPI_DPHY_CSI0_RX_D0P/MIPI_CPHY_CSI_RX_TRIO0_B	AK20	I/O	-		CSI4_D0_P	CSI4_D0_P

Interface definition

44	GND		G			GND	GND
46	MIPI_DPHY_CSI0_RX_CLKN/MIPI_CPHY_CSI_RX_TRIO1_B	AL22	I/O	-		CSI4_CLK_N	CSI4_CLK_N
48	MIPI_DPHY_CSI0_RX_CLKP/MIPI_CPHY_CSI_RX_TRIO1_C	AK22	I/O	-		CSI4_CLK_P	CSI4_CLK_P
50	GND		G			GND	GND
52	MIPI_DPHY_CSI0_RX_D1N/MIPI_CPHY_CSI_RX_TRIO0_C	AL21	I/O	-		CSI4_D1_N	CSI4_D1_N
54	MIPI_DPHY_CSI0_RX_D1P/MIPI_CPHY_CSI_RX_TRIO1_A	AK21	I/O	-		CSI4_D1_P	CSI4_D1_P
56	GND		G			GND	GND
58	MIPI_DPHY_CSI0_RX_D3N/MIPI_CPHY_CSI_RX_TRIO2_C	AL24	I/O	-		CSI4_D3_N	CSI4_D3_N
60	MIPI_DPHY_CSI0_RX_D3P/NO_USE	AK24	I/O	-		CSI4_D3_P	CSI4_D3_P
62	GND		G			GND	GND
64	NC					NC	NC
66	NC					NC	NC
68	GND		G			GND	GND
70	GBE1_MDI0_P		I/O	-		GBE1_MDI0_P	GBE1_MDI0_P (To Ethernet Transformer)
72	GBE1_MDI0_N		I/O	-		GBE1_MDI0_N	GBE1_MDI0_N (To Ethernet Transformer)
74	GBE1_MDI1_P		I/O	-		GBE1_MDI1_P	GBE1_MDI1_P (To Ethernet Transformer)
76	GBE1_MDI1_N		I/O	-		GBE1_MDI1_N	GBE1_MDI1_N (To Ethernet Transformer)
78	GND		G			GND	GND

Interface definition

80	GBE1_MDI2_P		I/O	-		GBE1_MDI2_P	GBE1_MDI2_P (To Ethernet Transformer)
82	GBE1_MDI2_N		I/O	-		GBE1_MDI2_N	GBE1_MDI2_N (To Ethernet Transformer)
84	GBE1_MDI3_P		I/O	-		GBE1_MDI3_P	GBE1_MDI3_P (To Ethernet Transformer)
86	GBE1_MDI3_N		I/O	-		GBE1_MDI3_N	GBE1_MDI3_N (To Ethernet Transformer)
88	PWM2_CH5_M0/AUPLL_CLK_IN_M2/SAI4_MCLK_M0/SAI1_MCLK_M0/GPIO4_A2_d	1D6	I/O	3.3V	DOWN	HDMI0_TX_ON_H	HDMI0_TX_ON_H (H:HDMI2.0 ; L:HDMI2.1)
90	GBE1_LED_LINK		I/O	3.3V		PHY1_CFG_EXT	PHY1_CFG_EXT
92	GBE1_LED2_ACT		I/O	3.3V		PHY1_CFG_LDO1	PHY1_CFG_LDO1
94	PWM1_CH5_M1/UART11_TX_M2/SPI4_CSN1_M0/I2C7_SCL_M3/PCIE1_WAKEN_M3/HDMI_TX_CEC_M0/SAI4_MCLK_M2/DSM_AUD_LP_M1/GPIO4_C0_d	AK3	I/O	3.3V	DOWN	HDMITX0_CEC	HDMITX0_CEC
96	PWM0_CH1_M1/UART11_RX_M2/EDP_TX_HPDIN_M0/I2C7_SDA_M3/PCIE1_CLKREQN_M3/HDMI_TX_HPDIN_M0/DSM_AUD_LN_M1/GPIO4_C1_d	AK2	I	5.0V	DOWN	HDMITX0_HPDIN	HDMI_HPD INPUT, Active H
98	PWM2_CH1_M1/UART9_RX_M2/CAN0_RX_M1/I2C2_SDA_M3/HDMI_TX_SDA/DSM_AUD_RN_M1/GPIO4_C3_d	1AE2	I/O	3.3V	DOWN	HDMI_DDC_SDA	HDMI_DDC_SDA
100	PWM2_CH0_M1/UART9_TX_M2/CAN0_TX_M1/I2C2_SCL_M3/HDMI_TX_SCL/DSM_AUD_RP_M1/GPIO4_C2_d	AL2	I/O	3.3V	DOWN	HDMI_DDC_SCL	HDMI_DDC_SCL
102	GND		G			GND	GND
104	PWM1_CH1_M1/PCIE1_WAKEN_M1/SPI1_MOSI_M0/I2C9_SCL_M1/SAI3_LRCK_M1/SDMMC1_D1_M0/ETH1_RXD3_M1/GPIO1_B5_d	B27	I/O	1.8V	DOWN	NC	SPI1_MOSI
106	PWM1_CH0_M1/PCIE1_CLKREQN_M1/SPI1_CLK_M0/I2C9_SDA_M1/SAI3_SCLK_M1/SDMMC1_D0_M0/ETH1_RXD2_M1/GPIO1_B4_d	A28	I/O	1.8V	DOWN	NC	SPI1_SCK
108	PCIE0_CLKREQN_M1/SPI1_MISO_M0/UART3_CTSN_M2/SAI3_SDO_M1/SDMMC1_D2_M0/ETH1_RXCLK_M1/GPIO1_B6_d	1A23	I/O	1.8V	DOWN	NC	SPI1_MISO
110	PCIE0_WAKEN_M1/SPI1_CSN0_M0/UART3_RTSN_M2/SAI3_SDI_M1/SDMMC1_D3_M0/ETH1_TXD2_M1/GPIO1_B7_d	A27	I/O	1.8V	DOWN	NC	SPI1_CS0
112	PWM0_CH0_M1/PI1_CSN1_M0/UART3_TX_M2/PDM0_SDI2_M2/SDMMC1_CMD_M0/ETH1_TXD3_M1/GPIO1_C0_d	B26	I/O	1.8V	DOWN	CAM4_PWDN	SPI1_CS1
114	I2C3_SDA_M2/SPI3_MISO_M1/UART5_TX_M0/FLEXBUS1_D0/DSMC_CLKP/SAI1_SDI2_M1/VO_EBC_GDCLK/VO_LCDC_HSYNC/GPIO3_D5_d	1D10	I/O	1.8V	DOWN	CAM0_PWDN	CAM0_PWDN

Interface definition

116	PWM2_CH7_M3/SPI3_CSN1_M1/UART5_RTSN_M0/FLEXBUS1_CSN_M1/FLEXBUS1_D12_M0/FLEXBUS0_D15_M0/DSMC_RESETN/SAI4_SCLK_M1/CAM_CLK0_OUT_M0/VO_EBC_SDOE/VO_LCDC_CLK/GPIO3_D7_d	1E7	I/O	1.8V	DOWN	CAM0_MCLK	CAM0_MCLK
118	I2C7_SDA_M2/UART3_RX_M1/FLEXBUS0_CSN_M1/FLEXBUS1_D13_M0/FLEXBUS0_D14_M0/DSMC_INT2/SAI4_SDO_M1/CAM_CLK2_OUT_M0/SPDIF_TX0_M1/VO_POST_EMPTY/GPIO4_A1_d	1B7	I/O	1.8V	DOWN	CAM2_MCLK	CAM2_MCLK
120	PWM2_CH6_M3/SPI3_MOSI_M1/UART5_CTSN_M0/FLEXBUS1_CLK/DSMC_CLKN/SAI1_SDI3_M1/VO_EBC_SDCLK/VO_LCDC_VSYNC/GPIO3_D6_d	1C10	I/O	1.8V	DOWN	CAM1_PWDN	CAM1_PWDN
122	MIPI_TE_M2/I2C7_SCL_M2/SPI1_CSN1_M2/UART3_TX_M1/FLEXBUS1_CSN_M3/FLEXBUS1_D14_M0/FLEXBUS0_D13_M0/DSMC_INT0/SAI4_LRCK_M1/CAM_CLK1_OUT_M0/SPDIF_RX0_M1/GPIO4_A0_d	1B12	I/O	1.8V	DOWN	CAM1_MCLK	CAM1_MCLK
124	SPI2_CSN0_M1/I2C6_SDA_M1/UART4_CTSN_M1/FSPI1_CSN0_M1/SDMMC1_DETN_M0/ETH1_PPSTRIG_M1/GPIO1_C3_u	1C23	I/O	1.8V	UP	BT_WAKE_AP	BT_WAKE_AP
126	PCIE0_BUTTONRSTN/SPI2_MOSI_M1/UART2_RTSN_M0/UART4_TX_M1/FSPI1_D0_M1/ETH1_TXD0_M1/GPIO1_C4_d	1B23	I/O	1.8V	DOWN	BT_DISABLE, Active H	BT_DISABLE, Active H
128	PCIE1_BUTTONRSTN/SPI2_MISO_M1/UART2_CTSN_M0/UART4_RX_M1/FSPI1_D1_M1/ETH1_TXD1_M1/GPIO1_C5_d	B28	I/O	1.8V	DOWN	WIFI_DISABLE, Active H	WIFI_DISABLE, Active H
130	I2C5_SCL_M1/UART10_RTSN_M1/SPDIF_RX1_M2/PDM0_SDI3_M2/SAI2_MCLK_M0/FSPI1_DQS_M1 |ETH1_MCLK_M1/GPIO1_D4_d	1E21	I/O	1.8V	DOWN	AP_WAKE_BT	AP_WAKE_BT
132	GND		G			GND	GND
134	PCIE0_TXN/SATA0_TXN	P28	O	-		PCIE0_TXN/SATA0_TXN	PCIE1_TX0_N
136	PCIE0_TXP/SATA0_TXP	P29	O	-		PCIE0_TXP/SATA0_TXP	PCIE1_TX0_P
138	GND		G			GND	GND
140	NC					NC	NC
142	NC					NC	NC
144	GND		G			GND	GND
146	SARADC_VIN3_HP_HOOK	1C19	I	1.8V		ADC3	ADC3 Input (HW version)

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148	I2C4_SCL_M3/UART3_RTSN_M1/UART2_TX_M2/FLEXBUS1_D9/DSMC_DATA7/SAI1_SDO3_M1/ETH0_PPSCLK_M0/VO_EBC_SDDO11/VO_LCDC_D11/GPIO3_C0_d	1E9	I/O	1.8V	DOWN	PCIE_PWR_EN	PCIE_PWR_EN
150	CAN0_RX_M3/I2C5_SDA_M3/SPI2_MISO_M2/UART11_RX_M0/FLEXBUS1_D8/DSMC_DATA6/SAI1_SDO2_M1/ETH0_PTP_REFCLK_M0/VO_EBC_SDDO10/VO_LCDC_D10/GPIO3_C1_d	1B10	I/O	1.8V	DOWN	WIFI_PWR_EN	WIFI_PWR_EN
152	GND		G			GND	GND
154	NC					NC	NC
156	NC					NC	NC
158	GND		G			GND	GND
160	PCIE0_REFCLKN	1N23	O	-		PCIE0_REFCLKN (default)	PCIE0_REFCLKN
162	PCIE0_REFCLKP	1N22	O	-		PCIE0_REFCLKP (default)	PCIE0_REFCLKP
164	GND		G			GND	GND
166	PCIE1_TXN/SATA1_TXN/USB3_HOST1_SSTXN (Default) (GPIO0_D1_d: H)	N29	O	-		USB3_HOST1_SSTXN	USBSS0_TX_N
168	PCIE1_TXP/SATA1_TXP/USB3_HOST1_SSTXP (Default) (GPIO0_D1_d: H)	N28	O	-		USB3_HOST1_SSTXP	USBSS0_TX_P
170	GND		G			GND	GND
172	PCIE1_TXN/SATA1_TXN/USB3_HOST1_SSTXN (optional) (GPIO0_D1_d: L) Default-NC	N29	O	-		PCIE1_TXN/SATA1_TXN (optional)	PCIE1_TXN/SATA1_TXN (optional)
174	PCIE1_TXP/SATA1_TXP/USB3_HOST1_SSTXP (optional) (GPIO0_D1_d: L) Default-NC	N28	O	-		PCIE1_TXP/SATA1_TXP (optional)	PCIE1_TXP/SATA1_TXP (optional)
176	GND		G			GND	GND
178	I3C0_SCL_M0/UART8_TX_M2/I2C0_SCL_M1/GPIO0_C1_d	AB28	I	1.8V	DOWN	MOD_SLEEP*	MOD_SLEEP* INPUT, Active L
180	CAN1_TX_M2/PCIE0_CLKREQN_M2/I2C3_SCL_M0/UART2_TX_M1 |FLEXBUS0_D15_M1 |SPDIF_TX0_M0/GPIO4_B5_d	1A4	I/O	3.3V	DOWN	PCIE0_CLKREQN	PCIE0_CLKREQN

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182	PCIE1_CLKREQN_M2/I2C2_SDA_M2/UART5_CTSN_M1/SPI4_CSN1_M2/FLEXBUS1_D12_M1 SAI1_LRCK_M0/GPIO4_A5_d	1B6	I/O	3.3V	DOWN	PCIE1_CLKREQN	PCIE1_CLKREQN
184	PHY0_MDI0-		I/O	-		GBE0_MDI0_N	GBE0_MDI0_N (To Ethernet Transformer)
186	PHY0_MDI0+		I/O	-		GBE0_MDI0_P	GBE0_MDI0_P (To Ethernet Transformer)
188	PHY0_LED_LINK		I/O	3.3V		PHY0_CFG_EXT	PHY0_CFG_EXT
190	PHY0_MDI1-		I/O	-		GBE0_MDI1_N	GBE0_MDI1_N (To Ethernet Transformer)
192	PHY0_MDI1+		I/O	-		GBE0_MDI1_P	GBE0_MDI1_P (To Ethernet Transformer)
194	PHY0_LED2/CFG_LDO1		I/O	3.3V		PHY0_CFG_LDO1	PHY0_CFG_LDO1
196	PHY0_MDI2-		I/O	-		GBE0_MDI2_N	GBE0_MDI2_N (To Ethernet Transformer)
198	PHY0_MDI2+		I/O	-		GBE0_MDI2_P	GBE0_MDI2_P (To Ethernet Transformer)
200	GND		G			GND	GND
202	PHY0_MDI3-		I/O	-		GBE0_MDI3_N	GBE0_MDI3_N (To Ethernet Transformer)
204	PHY0_MDI3+		I/O	-		GBE0_MDI3_P	GBE0_MDI3_P (To Ethernet Transformer)
206	SPI2_CSN1_M0/SDMMC0_DETN/GPIO0_A7_u	1U21	I/O	1.8V	UP	SDMMC0_DET_L	SDMMC0_DET_L
208	SAI0_SDI2_M1/SAI0_SDO2_M1/PDM0_SDI2_M0/I2C4_SCL_M0/CPUBIG_AVS/PWM1_CH5_M0/UART1_CTSN_M0/GPIO0_D2_d	1Y22	I/O	1.8V	DOWN	DIY_LED	DIY_LED EN, Active H
210	ETH_CLK1_25M_OUT_M1/FSPI1_CLK_M1/PDM0_CLK1_M2/SPDIF_TX1_M2/UART10_CTSN_M1/I2C5_SDA_M1/SPI2_CLK_M1/SATA_MPSWIT/CLK1_32K_OUT/GPIO1_D5_d	1E22	I/O	1.8V	DOWN	32KOUT_WIFI	32.768KHz OUTPUT TO WIFI
212	VI_CIF_D14/SDMMC1_D1_M1/ETH0_TXCTL_M1/SAI0_SDO1_M0/UART8_RX_M1/I2C4_SDA_M2/GPIO2_A7_d	B20	I/O	1.8V	DOWN	HP_DET_L	HP_DET INPUT, Active H
214	SARADC_VIN0_BOOT	A25	I	1.8V		FORCE_RECOVERY (ADC0)	FORCE_RECOVERY*
216	VI_CIF_D13/SDMMC1_D2_M1/ETH0_TXD1_M1/SAI0_SDI0_M0/PDM0_SDI3_M3/UART1_TX_M1/GPIO2_B0_d	B19	I/O	1.8V	DOWN	CAM2_PWDN	CAM2_PWDN

Interface definition

218	SDMMC0_PWREN/SDMMC1_DETN_M2/HDMI_TX_HPDIN_M1/EDP_TX_HPDIN_M1/PWM1_CH2_M0/GPIO0_B6_d	1Y24	I/O	1.8V	DOWN	SD0_PWR_EN	SD0_PWR_EN
220	ETH1_RXD1_M1/SAI2_SDO_M0/UART10_TX_M1/GPIO1_D0_d	C29	I/O	1.8V	DOWN	4G_PWR_EN	4G_PWR_EN
222	ETH1_MDIO_M1/SAI2_SDI_M0/I3C0_SDA_M1/PWM1_CH4_M1/GPIO1_D3_d	C28	I/O	1.8V	DOWN	HUB20_PWR_EN	HUB20_PWR_EN
224	ETH1_MDC_M1/SAI2_LRCK_M0/I3C0_SCL_M1/PWM1_CH3_M1/GPIO1_D2_d	1A24	I/O	1.8V	DOWN	SATA_DEVSLP	SATA_DEVSLP
226	ETH1_RXCTL_M1/SAI2_SCLK_M0/UART10_RX_M1/I3C0_SDA_PU_M1/GPIO1_D1_d	1D22	I/O	1.8V	DOWN	USB30_VCC5V0_EN	USB30_VCC5V0_EN
228	SAI0_SDI3_M1/SAI0_SDO1_M1/PDM0_SDI3_M0/I2C4_SDA_M0/GPU_AVS/PWM2_CH0_M0/UART1_RTSN_M0/GPIO0_D3_d	1AA23	I/O	1.8V	DOWN	PHONE_CTL	PHONE_CTL
230	ETH1_PPSCLK_M1/SDMMC1_PWREN_M0/FSPI1_RSTN_M1/FSPI1_CSN1_M1/UART4_RTSN_M1/I2C6_SCL_M1/SPI2_CSN1_M1/PWM1_CH2_M1/GPIO1_C2_u	B29	I/O	1.8V	UP	PWM1_CH2_M1	FAN_PWM
232	VI_CIF_D7/ETH1_PTP_REFCLK_M1/ETH0_RXD3_M1/SAI0_SCLK_M0/UART7_TX_M0/UART8_RTSN_M1/I2C8_SCL_M2/GPIO2_B6_d	A21	I/O	1.8V	DOWN	I2C8_SCL_M2	I2C8_SCL (Core board pull up resistance 2.2K)
234	VI_CIF_D6/ETH0_RXD2_M1/SAI0_LRCK_M0/UART7_RX_M0/UART8_CTSN_M1/I2C8_SDA_M2/GPIO2_B7_d	B21	I/O	1.8V	DOWN	I2C8_SDA_M2	I2C8_SDA (Core board pull up resistance 2.2K)
236	UART0_TX_M0/JTAG_TCK_M1/GPIO0_D4_u	1U24	I/O	1.8V	UP	UART0_TX_M0_DEBUG	Debug_UART0_TXD
238	UART0_RX_M0/JTAG_TMS_M1/GPIO0_D5_u	AA28	I/O	1.8V	UP	UART0_RX_M0_DEBUG	Debug_UART0_RXD
240	PWRON_L		I	5.0V		PWRON_L	PWERON KEY INPUT,Active L
242	GND		G	GND		GND	GND
244	GND		G	GND			
246	GND		G	GND			
248	GND		G	GND			
250	GND		G	GND			

Interface definition

252	VDD5V0_IN		P	5.0V		CORE_5V Normal: 5V/220mA Max.:5V/1400mA Min.:5V/5mA	Input Voltage 5.0V +/-5%
254	VDD5V0_IN		P	5.0V			
256	VDD5V0_IN		P	5.0V			
258	VDD5V0_IN		P	5.0V			
260	VDD5V0_IN		P	5.0V			

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