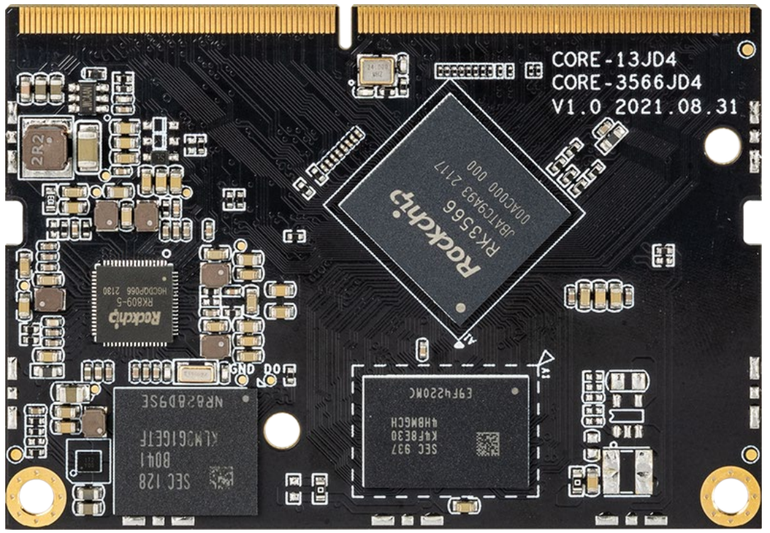
Core-3566JD4

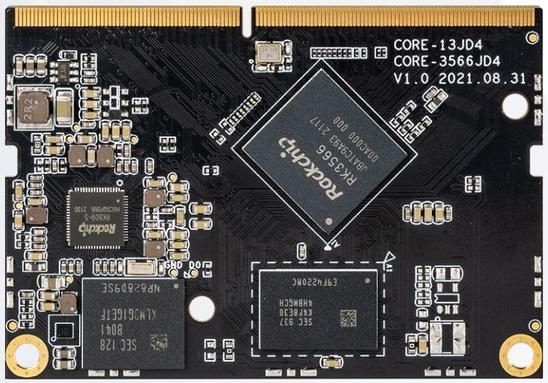
Quad-core 64-Bit AI Core Board



V1.0 2024-9-30

T-CHIP INTELLIGENCE TECHNOLOGY

Product features



RK3566 quad-core 64-bit processor

RK3566 quad-core 64-bit Cortex-A55 processor has frequency up to 1.8GHz. With 22nm lithography process, it features low power consumption and high performance.

8GB large RAM

It supports up to 8GB RAM, making data safer and more reliable, and meeting the requirements of run ning large-memory products.

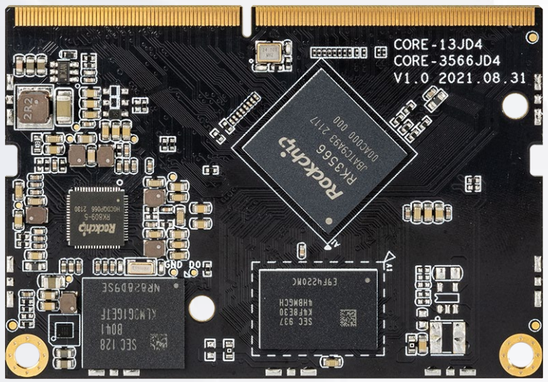
Integrated co-processors

It is integrated with dual-core ARM G52 GPU, high-performance VPU and high-efficiency NPU. The GPU supports OpenGL ES3.2/2.0/1.1, Vulkan1.1. The VPU can achieve 4K@60fps H.265/H.264/VP9 video decoding and 1080P@100fps H.265/ H.264 video encoding. The NPU with computing power of 1Tops supports one-click switching of mainstream frameworks like Caffe/TensorFlow.

To form high-performance mainboard

The core board with SODIMM 260P interface can be combined with a backplane to form a complete high-performance industrial mainboard delivering more powerful performance, which can be directly applied to various smart products to accelerate the product development process.

Product features



PCIe2.1 and SATA3.0

PCIe2.1 can be connected with NVMe SSD, and SATA3.0 can be connected with SSD/HDD, owning the superiority of high-speed reading and writing and large storage.

Supports various operating systems

It supports Android, Ubuntu, Buildroot+QT, OpenWRT, Debian and other operating systems, stable and reliable.

Abundant expansion interfaces

The core board has interfaces like UART, I2C, ADC, PWM, GPIO, PCIE2.1, SATA3.0, USB2.0, USB3.0, HDMI2.0, eDP1.3, MIPI-CSI, MIPI-DSI, I2S, SPEAK and HPOUT.

A wide range of applications

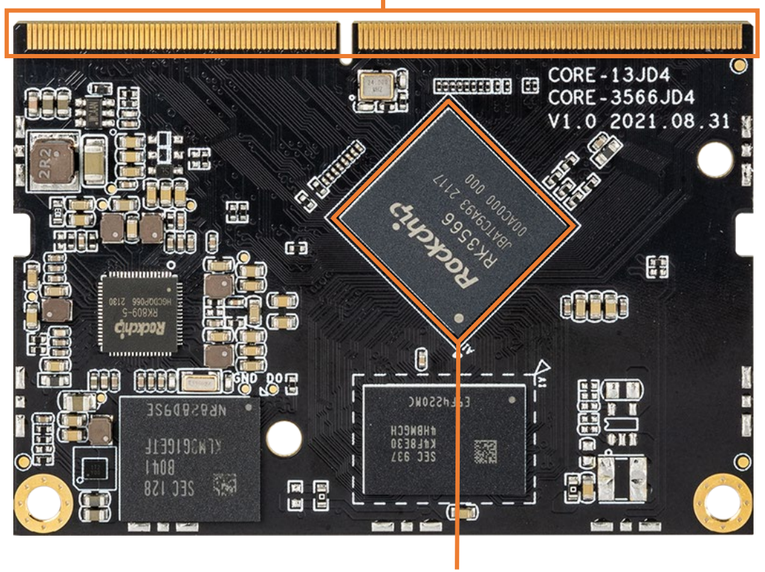
The product is suitable for scenarios such as entertainment PC, smart NVR, cloud terminal, IoT gateway, industrial control, edge computing, face recognition gate, NAS, and vehicle center console.

Specifications

Specifications		
Basic Specifications	SOC	Rockchip RK3566
	CPU	Quad-core 64-bit Cortex-A55, 22nm lithography process, up to 1.8GHz
	GPU	ARM G52 2EE GPU, supports OpenGL ES 1.1/2.0/3.2, OpenCL 2.0, Vulkan 1.1, Embedded high-performance 2D acceleration hardware
	NPU	Integrated RKNN NPU AI accelerator, 1Tops@INT8 Supports one-click switching of Caffe/TensorFlow/TFLite/ONNX/PyTorch/Keras/Darknet
	VPU	Video decoding: 4K@60fps H.265/H.264/VP9, 1080P@60fps VC-1, VP8, MPEG-1/2/4 Video encoding: 1080P@100fps H.265, 1080P@60fps H.264
	ISP	Support 8M ISP
	RAM	1GB/2GB/4GB/8GB LPDDR4/LPDDR4X, 32Bit
	Storage	8GB/32GB/64GB eMMC (16GB/128GB optional)
	Storage Expansion	1 × TF Card, 1 × M.2 PCIe 2.1 (Support 2242 NVMe SSD)
	OS	Android, Ubuntu, Buildroot+QT, OpenWRT, Debian
	Power	DC 5V (voltage tolerance ± 5%)
	Power consumption	Normal: 2.8W(5V/560mA), Min: 0.075W(5V/15mA), Max: 4.6W(5V/920mA)
	Interface	Goldfinger (260 PIN, SODIMM, 0.5mm pitch)
	Size	69.6mm × 48mm × 4.3mm
	Environment	Operating Temperature: -20℃ ~ 60℃, Storage Temperature: -20℃ ~ 70℃, Storage Humidity: 10% ~ 90%RH(non-condensing)
Interface Specifications	Ethernet	1 × GMAC, providing RGMII/RMII interface, supporting 10/100/1000Mbps data transfer rate
	WiFi	Extend to WiFi+Bluetooth combo module through SDIO: Supports 2.4GHz/5GHz dual-band WiFi, 802.11a/b/g/n/ac protocol and Bluetooth5.0 Supports extended 4G LTE/3G wireless network
	Video input	MIPI-CSI (4-channel), supports 2-channel input, built-in 8M ISP, supports single-lens 8M camera/dual-lens 2M camera
	Video output	1 × HDMI2.0: supports up to 4K@60fps output 2 × MIPI-DSI: supports single/dual channel mode, supports up to 2560×1440@60fps output 1 × eDP1.3: supports up to 2560×1600@60fps output
	Audio	1 × I2S (8-channel input and output), 2 x I2S (2-channel input and output), 1 x I2S (HDMI)
	SATA	2 × SATA3.0
	PCIe	1 × PCIe2.1(1lane, multiplexed with SATA3.0)
	USB	1 × USB3.0(multiplexed with SATA3.0), 2 × USB2.0 HOST, 1 × USB2.0 OTG
	Other interfaces	10 × UART, 6 × I2C, 4 × I2S, 4 × ADC, 15 × PWM, 118 × GPIO, 1 × SPEAK, 1 × HPOUT

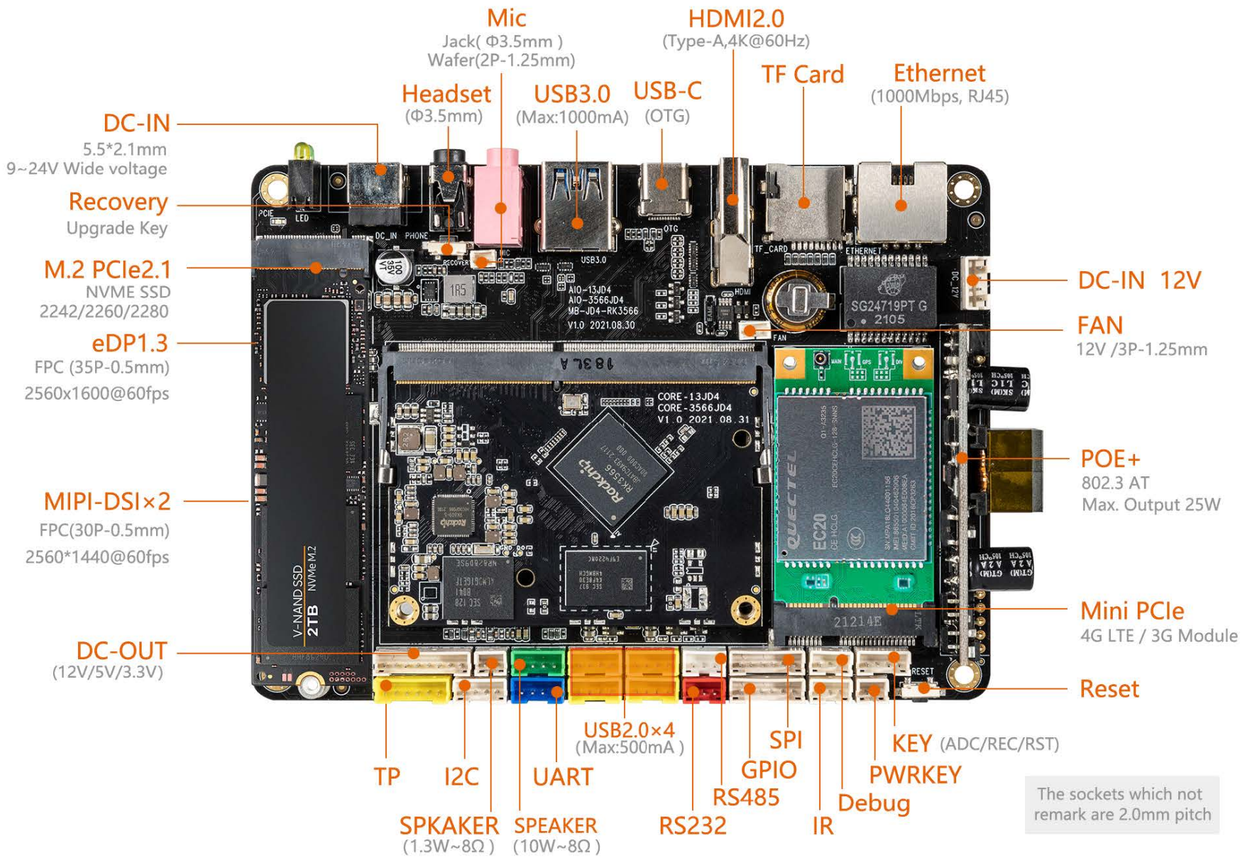
Core board Interface description

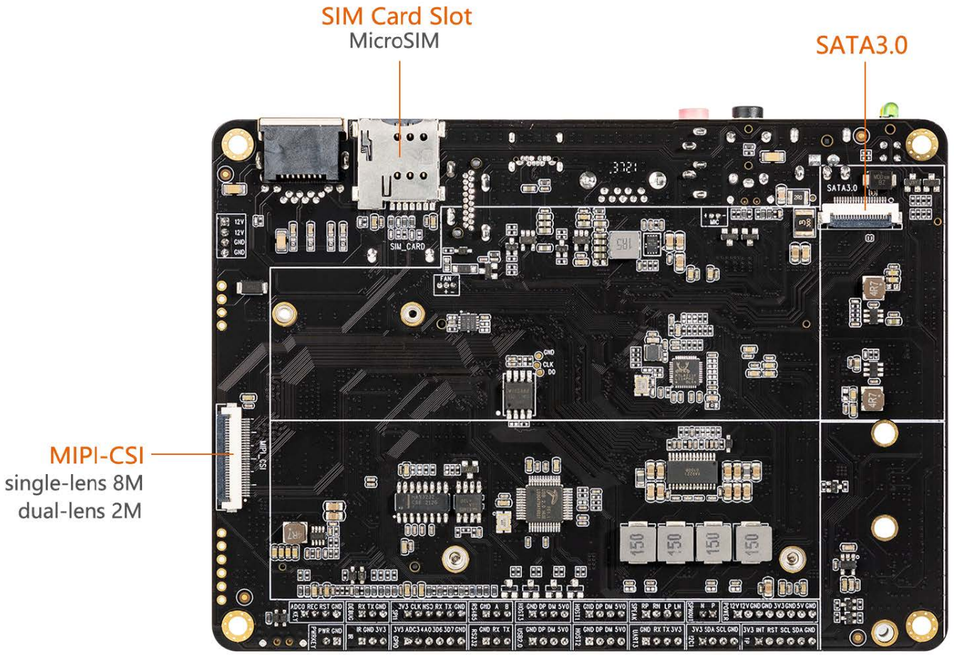
Goldfinger
(260 PIN, SODIMM, 0.5mm Pitch)



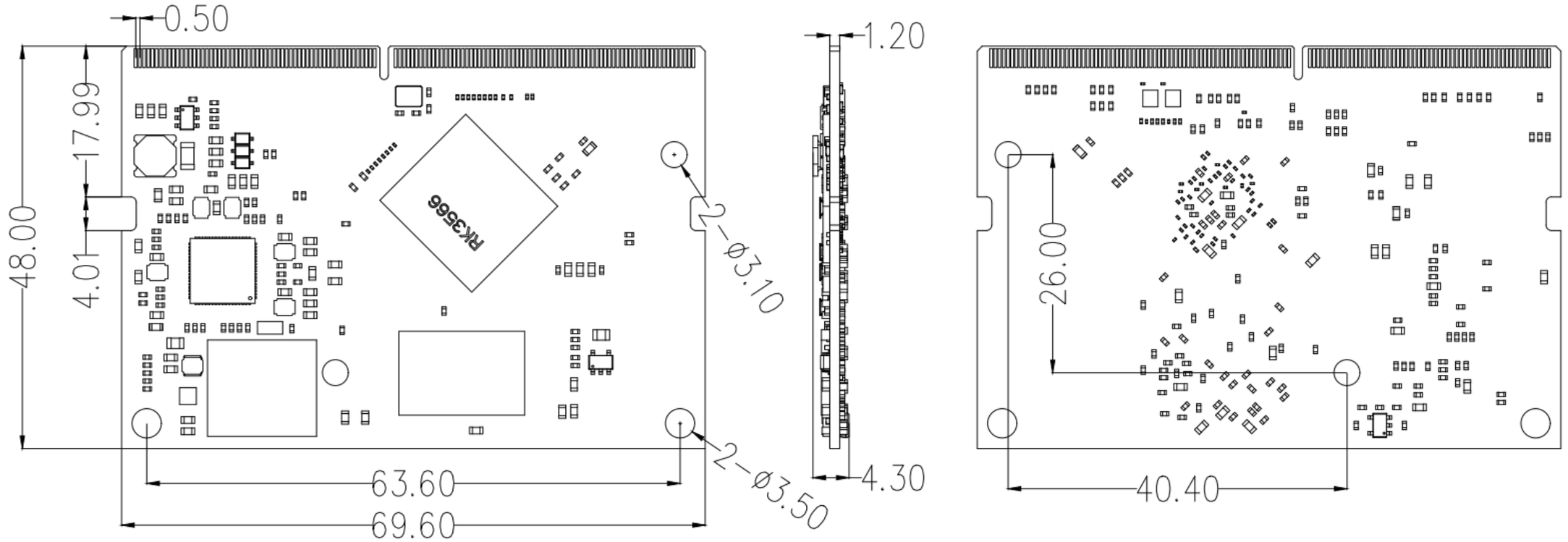
Rockchip RK3566
(Quad-core ARM Cortex-A55 @ 1.8 GHz)

Mainboard Interface description

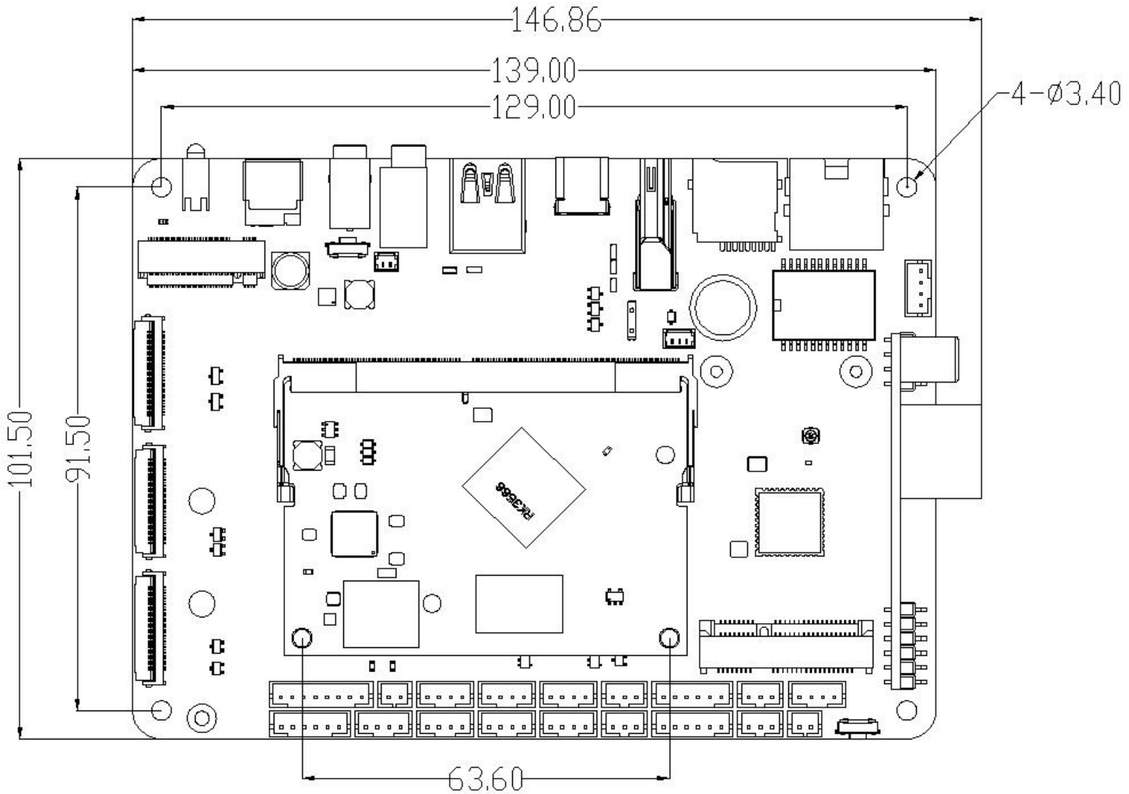


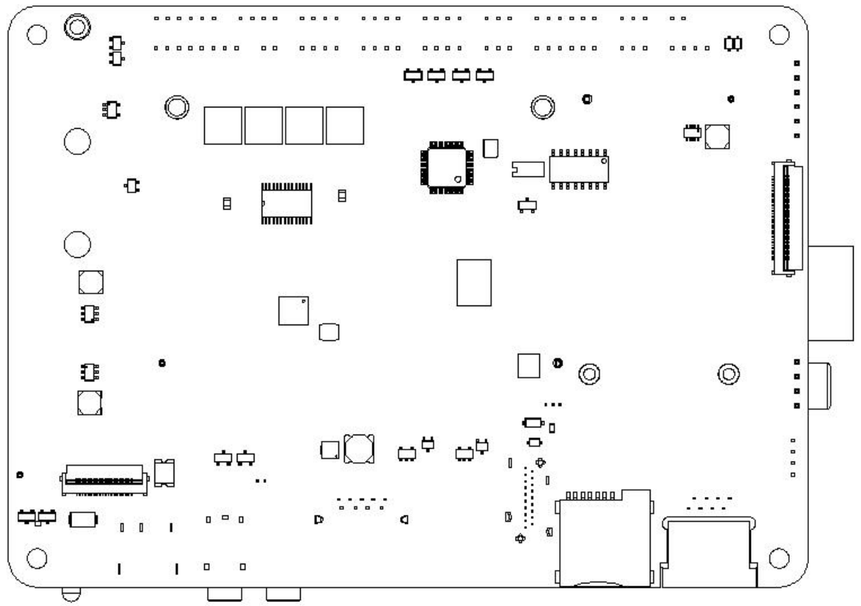


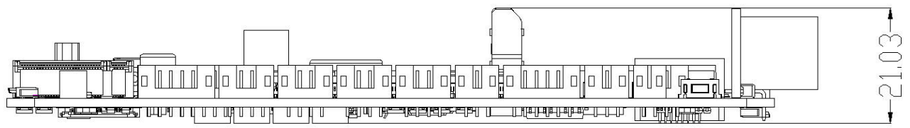
Core board Dimension



Mainboard Dimension







Interface definition

"Notes1:
Pad types: I = input, O = output, I/O = input/output (bidirectional) , G= Ground , P = power supply , DOWN = Internal pull DOWN , UP = Internal pull UP L = Lowe Level H = High level, AI=Analog Input, AO=Analog Output, AI/O=Analog Input/Output"

Part A	PIN	CORE_3566JD4 pin definition	Pad type	IO Pull	IO Power domain	RK3566 Pin Number	Function for (MB-RK3566-JD4)	Defual function description
	1	CAM_CLKOUT0/EBC_SDCE1/GMAC1_RXD0_M1/SPI3_CS1_M0/I2S1_LRCK_RX_M1/GPIO4_A7_d	I/O	DOWN	1.8V	1N1	MIPI_MCLK0	MIPI_MCLK0 OUT
	3	CIF_HREF/EBC_SDLE/GMAC1_MDC_M1/UART1_RTSn_M1/I2S2_MCLK_M1/GPIO4_B6_d	I/O	DOWN	1.8V	1L1	WORK_LED	WORK_LED EN, active H
	5	CAM_CLKOUT1/EBC_SDCE2/GMAC1_RXD1_M1/SPI3_MISO_M0/I2S1_SDO1_M1/GPIO4_B0_d	I/O	DOWN	1.8V	1L2	DIY_LED	DIY_LED EN, active H
	7	GND	G				GND	GND
	9	USB_HOST2_DM	AI/O		-	V1	USB_HOST2_DM	USB_HOST2_DM
	11	USB_HOST2_DP	AI/O		-	V2	USB_HOST2_DP	USB_HOST2_DP
	13	GND	G				GND	GND
	15	USB_HOST3_DM	AI/O		-	Y1	USB_HOST3_DM	USB_HOST3_DM
	17	USB_HOST3_DP	AI/O		-	Y2	USB_HOST3_DP	USB_HOST3_DP
	19	GND	G				GND	GND
	21	CIF_CLKOUT/EBC_GDCLK/PWM11_IR_M1/GPIO4_C0_d	I/O	DOWN	1.8V	AB2	MIPI_MCLK1	MIPI_MCLK1 OUTPUT
	23	CIF_VSYNC/EBC_SDOE/GMAC1_MDIO_M1/I2S2_SCLK_TX_M1/GPIO4_B7_d	I/O	DOWN	1.8V	AC2	GSENSOR_INT_L	GSENSOR interrupt INPUT
	25	I2C4_SCL_M0/EBC_GDOE/ETH1_REFCLKO_25M_M1/SPI3_CLK_M0/I2S2_SDO_M1/GPIO4_B3_d	I/O	DOWN	1.8V	AE2	I2C4_SCL_M0	I2C4_SCL_M0 Core board Pull up resistance 2.2K
	27	I2C4_SDA_M0/EBC_VCOM/GMAC1_RXER_M1/SPI3_MOSI_M0/I2S2_SDI_M1/GPIO4_B2_d Core board Pull up resistance 2.2K to 1.8V	I/O	DOWN	1.8V	AF2	I2C4_SDA_M0	I2C4_SDA_M0 Core board Pull up resistance 2.2K
	29	ISP_PRELIGHT_TRIG/EBC_SDCE3/GMAC1_RXDV_CRS_M1/I2S1_SDO2_M1/GPIO4_B1_d	I/O	DOWN	1.8V	AG2	MIPI_PDN1_CAM	MIPI CSI1 power down
	31	CIF_D14/EBC_SDDO14/GMAC1_TXD0_M1/UART9_TX_M2/I2S2_LRCK_TX_M1/GPIO4_A4_d	I/O	DOWN	1.8V	1N2	MIPI_RESET1_CAM	MIPI CSI1 reset

Interface definition

	33	CIF_D15/EBC_SDDO15/GMAC1_TXD1_M1/UART9_RX_M2/I2S2_LRCK_RX_M1/GPIO4_A5_d	I/O	DOWN	1.8V	AJ1	VCC5V0_USB30_EN	USB 3.0 power EN,Active H
	35	VOP_BT1120_D6/ETH1_REFCLKO_25M_M0/SDMMC2_PWREN_M1/GPIO3_B0_d	I/O	DOWN	3.3V	AR7	HUB_RST	Hub reset,Active H Core board series resistance 22R
	37	HDMITX_SDA/I2C5_SDA_M1/GPIO4_D0_u	I/O	UP	3.3V	AP11	HDMITX_SDA	I2C SDA for HDMI
	39	HDMITX_SCL/I2C5_SCL_M1/GPIO4_C7_u	I/O	UP	3.3V	AR12	HDMITX_SCL	I2C SCL for HDMI
	41	HDMITX_CEC_M0/SPI3_CS1_M1/GPIO4_D1_u	I/O	UP	3.3V	1V5	HDMITX_CEC_M0	HDMITX_CEC_M0
	43	GND	G				GND	
	45	MIPI_CSI_RX_D3N	AI		-	AP14	MIPI_CSI_RX_D3N	MIPI_CSI_RX_D3N Input
	47	MIPI_CSI_RX_D3P	AI		-	AR14	MIPI_CSI_RX_D3P	MIPI_CSI_RX_D3P Input
	49	MIPI_CSI_RX_D2N	AI		-	AR15	MIPI_CSI_RX_D2N	MIPI_CSI_RX_D2N Input
	51	MIPI_CSI_RX_D2P	AI		-	AP15	MIPI_CSI_RX_D2P	MIPI_CSI_RX_D2P Input
	53	MIPI_CSI_RX_D1N	AI		-	AP17	MIPI_CSI_RX_D1N	MIPI_CSI_RX_D1N Input
	55	MIPI_CSI_RX_D1P	AI		-	AR17	MIPI_CSI_RX_D1P	MIPI_CSI_RX_D1P Input
	57	MIPI_CSI_RX_D0N	AI		-	AR18	MIPI_CSI_RX_D0N	MIPI_CSI_RX_D0N Input
	59	MIPI_CSI_RX_D0P	AI		-	AP18	MIPI_CSI_RX_D0P	MIPI_CSI_RX_D0P Input
	61	GND	G				GND	
	63	MIPI_DSI_TX1_D3N	AO		-	AP20	MIPI_DSI_TX1_D3N	MIPI_DSI_TX1_D3N Output
	65	MIPI_DSI_TX1_D3P	AO		-	AR20	MIPI_DSI_TX1_D3P	MIPI_DSI_TX1_D3P Output
	67	MIPI_DSI_TX1_D2N	AO		-	AR21	MIPI_DSI_TX1_D2N	MIPI_DSI_TX1_D2N Output
	69	MIPI_DSI_TX1_D2P	AO		-	AP21	MIPI_DSI_TX1_D2P	MIPI_DSI_TX1_D2P Output
	71	MIPI_DSI_TX1_D1N	AO		-	AP23	MIPI_DSI_TX1_D1N	MIPI_DSI_TX1_D1N Output

Interface definition

	73	MIPI_DSI_TX1_D1P	AO		-	AR23	MIPI_DSI_TX1_D1P	MIPI_DSI_TX1_D1P Output
	75	MIPI_DSI_TX1_D0N	AO		-	AR24	MIPI_DSI_TX1_D0N	MIPI_DSI_TX1_D0N Output
	77	MIPI_DSI_TX1_D0P	AO			AP24	MIPI_DSI_TX1_D0P	MIPI_DSI_TX1_D0P Output
	79	GND	G				GND	
	81	MIPI_DSI_TX0_D3N/LVDS_TX0_D3N	AO		-	AP26	MIPI_DSI_TX0_D3N/LVDS_TX0_D3N	MIPI_DSI_TX0_D3N/LVDS_TX0_D3N
	83	MIPI_DSI_TX0_D3P/LVDS_TX0_D3P	AO		-	AR26	MIPI_DSI_TX0_D3P/LVDS_TX0_D3P	MIPI_DSI_TX0_D3P/LVDS_TX0_D3P
	85	MIPI_DSI_TX0_D2N/LVDS_TX0_D2N	AO		-	AR27	MIPI_DSI_TX0_D2N/LVDS_TX0_D2N	MIPI_DSI_TX0_D2N/LVDS_TX0_D2N
	87	MIPI_DSI_TX0_D2P/LVDS_TX0_D2P	AO		-	AP27	MIPI_DSI_TX0_D2P/LVDS_TX0_D2P	MIPI_DSI_TX0_D2P/LVDS_TX0_D2P
	89	MIPI_DSI_TX0_D1N/LVDS_TX0_D1N	AO		-	AP29	MIPI_DSI_TX0_D1N/LVDS_TX0_D1N	MIPI_DSI_TX0_D1N/LVDS_TX0_D1N
	91	MIPI_DSI_TX0_D1P/LVDS_TX0_D1P	AO		-	AR29	MIPI_DSI_TX0_D1P/LVDS_TX0_D1P	MIPI_DSI_TX0_D1P/LVDS_TX0_D1P
	93	MIPI_DSI_TX0_D0N/LVDS_TX0_D0N	AO		-	AR30	MIPI_DSI_TX0_D0N/LVDS_TX0_D0N	MIPI_DSI_TX0_D0N/LVDS_TX0_D0N
	95	MIPI_DSI_TX0_D0P/LVDS_TX0_D0P	AO		-	AP30	MIPI_DSI_TX0_D0P/LVDS_TX0_D0P	MIPI_DSI_TX0_D0P/LVDS_TX0_D0P
	97	GND	G				GND	
	99	HDMI_TXCLKN_PORT	AO		-	AP32	HDMI_TXCLKN_PORT	HDMI_TXCLKN Core board series resistance 2.2R
	101	HDMI_TXCLKP_PORT	AO		-	AR32	HDMI_TXCLKP_PORT	HDMI_TXCLKP Core board series resistance 2.2R
	103	HDMI_TX0N_PORT	AO		-	AR33	HDMI_TX0N_PORT	HDMI_TX0N Core board series resistance 2.2R
	105	HDMI_TX0P_PORT	AO		-	AP33	HDMI_TX0P_PORT	HDMI_TX0P Core board series resistance 2.2R
	107	HDMI_TX1N_PORT	AO		-	AP35	HDMI_TX1N_PORT	HDMI_TX1N Core board series resistance 2.2R
	109	HDMI_TX1P_PORT	AO		-	AR35	HDMI_TX1P_PORT	HDMI_TX1P Core board series resistance 2.2R
	111	HDMI_TX2N_PORT	AO		-	AR36	HDMI_TX2N_PORT	HDMI_TX2N Core board series resistance 2.2R

Interface definition

	113	HDMI_TX2P_PORT	AO		-	AP36	HDMI_TX2P_PORT	HDMI_TX2P Core board series resistance 2.2R
	115	GND	G				GND	
	117	PWM5/SPI0_CS1_M0/UART0_RTSn/GPIO0_C4_d	I/O	DOWN	3.3V	AN37	EDP_BL_PWM5	EDP Backlight EN, Active H
	119	PWM4/VOP_PWM_M0/MCU_JTAG_TRSTn/GPIO0_C3_d	I/O	DOWN	3.3V	AN38	LCD0_BL_PWM4	LCD Backlight EN for MIPI DSI0, Active H
	121	I2C1_SDA/PCIE20_BUTTONRSTn/MCU_JTAG_TCK/GPIO0_B4_u	I/O	UP	3.3V	AM38	I2C1_SDA	I2C1_SDA Core board Pull up resistance 2.2K
	123	I2C1_SCL/MCU_JTAG_TDO/GPIO0_B3_u	I/O	UP	3.3V	AK37	I2C1_SCL	I2C1_SCL Core board Pull up resistance 2.2K
	125	GPU_PWREN/SATA_CP_POD/GPIO0_A6_d	I/O	DOWN	3.3V	AJ38	LCD_PWR_EN_GPIO0_A6_D	MIPI DS1 1 power en,Active L
	127	SDMMC0_PWREN/SATA_MP_SWITCH/PCIE20_CLKREQn_M0/GPIO0_A5_d	I/O	DOWN	3.3V	AG37	TP_INT_GPIO0_A5_D	MIPI DS1 1 TP interrupt input ,Active L
	129	SDMMC0_DET/SATA_CP_DET/GPIO0_A4_u	I/O	UP	3.3V	AF37	SDMMC0_DET	SDMMC0_DET,Active L
	131	GND	G					
	133	USB_OTG0_DP	AI/O		-	R37	USB_OTG0_DP	USB_OTG0_DP
	135	USB_OTG0_DM	AI/O		-	R38	USB_OTG0_DM	USB_OTG0_DM
	137	GND	G					
	139	PCIE20_RXP/SATA2_RXP	AI/O		-	AB37	PCIE20_RXP/SATA2_RXP	PCIE20_RXP/SATA2_RXP
	141	PCIE20_RXN/SATA2_RXN	AI/O		-	AB38	PCIE20_RXN/SATA2_RXN	PCIE20_RXN/SATA2_RXN
	143	GPIO0_D4_D	I/O	DOWN	1.8V	1M19	RTCIC_INT_L_GPIO0_D4_D	RTC IC iINT Ipnut, Active L
	145	USB_OTG0_VBUSDET	I		3.3V	T38	USB_OTG0_VBUSDET	OTG0_VBUSDET Input ,Active H
	147	GND	G					
	149	PCIE20_TXN/SATA2_TXN	AI/O		-	AA38	PCIE20_TXN/SATA2_TXN	PCIE20_TXN/SATA2_TXN
	151	PCIE20_TXP/SATA2_TXP	AI/O		-	AA37	PCIE20_TXP/SATA2_TXP	PCIE20_TXP/SATA2_TXP

Interface definition

	153	GND	G					
	155	USB3_HOST1_SSTXP/SATA1_TXP	AI/O		-	W37	USB3_HOST1_SSTXP	USB3_HOST1_SSTXP
	157	USB3_HOST1_SSTXN/SATA1_TXN	AI/O		-	W38	USB3_HOST1_SSTXN	USB3_HOST1_SSTXN
	159	USB3_HOST1_SSRXP/SATA1_RXP	AI/O		-	V38	USB3_HOST1_SSRXP	USB3_HOST1_SSRXP
	161	USB3_HOST1_SSRXN/SATA1_RXN	AI/O		-	V37	USB3_HOST1_SSRXN	USB3_HOST1_SSRXN
	163	GND	G					
	165	EDP_TX_D3P	AO		-	N37	EDP_TX_D3P	EDP_TX_D3P core board series capacitance 0.1uF
	167	EDP_TX_D3N	AO		-	N38	EDP_TX_D3N	EDP_TX_D3N core board series capacitance 0.1uF
	169	EDP_TX_D2N	AO		-	M38	EDP_TX_D2N	EDP_TX_D2N core board series capacitance 0.1uF
	171	EDP_TX_D2P	AO		-	M37	EDP_TX_D2P	EDP_TX_D2P core board series capacitance 0.1uF
	173	EDP_TX_D1N	AO		-	K37	EDP_TX_D1N	EDP_TX_D1N core board series capacitance 0.1uF
	175	EDP_TX_D1P	AO		-	K38	EDP_TX_D1P	EDP_TX_D1P core board series capacitance 0.1uF
	177	EDP_TX_D0N	AO		-	J38	EDP_TX_D0N	EDP_TX_D0N core board series capacitance 0.1uF
	179	EDP_TX_D0P	AO		-	J37	EDP_TX_D0P	EDP_TX_D0P core board series capacitance 0.1uF
	181	EDP_TX_AUXP	AO		-	1H19	EDP_TX_AUXP	EDP_TX_AUXP
	183	EDP_TX_AUXN	AO		-	1H20	EDP_TX_AUXN	EDP_TX_AUXN
	185	GND	G				GND	GND
	187	UART1_CTSn_M0/SPI1_MISO_M0/GPIO2_B6_u	I/O	UP	1.8V	G37	UART1_CTSn_M0	UART1_CTSn_M0
	189	UART1_RX_M0/GPIO2_B3_u	I/O	UP	1.8V	1C20	UART1_RX_M0	UART1_RX_M0
	191	UART1_TX_M0/GPIO2_B4_u	I/O	UP	1.8V	F38	UART1_TX_M0	UART1_TX_M0

Interface definition

	193	UART1_RTSn_M0/SPI1_CLK_M0/GPIO2_B5_u	I/O	UP	1.8V	F37	UART1_RTSn_M0	UART1_RTSn_M0
	195	I2S2_SCLK_RX_M0/UART6_RTSn_M0/SPI1_MOSI_M0/GPIO2_B7_d	I/O	DOWN	1.8V	D38	BT_REG_ON_H	BT_REG_ON,Active H
	197	CLK32K_OUT1/UART8_RX_M0/SPI1_CS1_M0/GPIO2_C6_d	I/O	DOWN	1.8V	B38	CLK32K_OUT1_WIFI	32KHz clodk out for WIFI
	199	I2S2_LRCK_RX_M0/UART6_CTSn_M0/SPI1_CS0_M0/GPIO2_C0_d	I/O	DOWN	1.8V	A37	BT_WAKE_HOST_H	BT_WAKE_HOST,Active H
	201	I2S2_MCLK_M0/ETH0_REFCLKO_25M/UART7_RTSn_M0/SPI2_CLK_M0/GPIO2_C1_d	I/O	DOWN	1.8V	B36	HOST_WAKE_BT_H	HOST_WAKE_BT,Active H
	203	SDMMC1_PWREN/I2C4_SDA_M1/UART8_RTSn_M0/GPIO2_B1_d	I/O	DOWN	1.8V	A35	WIFI_REG_ON_H	WIFI_REG_ON,Active H
	205	SDMMC1_DET/I2C4_SCL_M1/UART8_CTSn_M0/GPIO2_B2_u	I/O	UP	1.8V	B34	WIFI_WAKE_HOST_H	WIFI_WAKE_HOST,Active H
	207	I2S1_SDO2_M0/I2S1_SDI2_M0/PDM_SDI2_M0/PCIE20_WAKEn_M2/GPIO1_B1_d	I/O	DOWN	3.3V	A26	PCIE_WAKE	PCIE_WAKE
	209	I2S1_SDO3_M0/I2S1_SDI1_M0/PDM_SDI1_M0/PCIE20_PERSTn_M2/GPIO1_B2_d	I/O	DOWN	3.3V	B26	PCIE_RST	PCIE RESET
	211	I2S1_SDO1_M0/I2S1_SDI3_M0/PDM_SDI3_M0/PCIE20_CLKREQn_M2/GPIO1_B0_d	I/O	DOWN	3.3V	1B13	PCIE_CLKREQ	PCIE_CLK REQ
	213	I2S1_SCLK_RX_M0/UART4_RX_M0/PDM_CLK1_M0/SPDIF_TX_M0/GPIO1_A4_d	I/O	DOWN	3.3V	1A13	SPK_CTL_H_GPIO1_A4	SPEAK CONTROL,Active H
	215	I2C3_SDA_M0/UART3_RX_M0/AUDIOPWM_LOUT_P/GPIO1_A0_u	I/O	UP	3.3V	A22	I2C3_SDA_M0/UART3_RX_M0	I2C3_SDA_M0/UART3_RX_M0 Core board Pull up resistance 2.2K
	217	I2C3_SCL_M0/UART3_TX_M0/AUDIOPWM_LOUT_N/GPIO1_A1_u	I/O	UP	3.3V	B22	I2C3_SCL_M0/UART3_TX_M0	I2C3_SCL_M0/UART3_TX_M0 Core board Pull up resistance 2.2K
	219	GND	G					
	221	PMIC_PWRON					PMIC_PWRON	PMIC POWER ON Input, Active L
	223	PMIC_VDC			5.0V		VCC_5V_S	PMIC_VDC Input,Active H
	225	VCCIO_WL	P		1.8V		VCCIO_WL	1.8V Output for WIFI VCCIO (Pin224/225 Total Max 300mA)
	227	VCC3V3_SD	P		3.3V		VCC3V3_SD	3.3V Output For TF Card Power (Pin226/227 Total Max:300mA)
	229	VCC_1V8	P		1.8V		VCC_1V8	1.8V Output,(Pin228/229 Total Max:500mA)
	231	VCCIO_ACODEC	P		3.3V		VCCIO_ACODEC	3.3V Output For codec, (Pin230/231 Total Max:300mA)

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	233	VCC_5V_S	P		5.0V		VCC_5V_S	5.0V input to EXT_EN (Default NC)
	235	VCC3V3_SYS	P		3.3V		VCC3V3_SYS	3.3V Output , (Pin234/235 Total Max:500mA)
	237	HP_SNS	G				HP_SNS	HeadPhone_OUT GND
	239	MIC2_IN			3.3V		MIC2_IN	MIC1_INPUT- core board series capacitance 0.1uF
	241	MIC1_IN			3.3V		MIC1_IN	MIC1_INPUT+ core board series capacitance 0.1uF
	243	GND	G				GND	GND
	245	GND	G				GND	
	247	GND	G				GND	
	249	GND	G				GND	
	251	VCC5V0_SYS	P		5V		VCC5V0_SYS	Input Voltage 5.0V +/-5% CORE BOARD 5.0V Supply current: Normal: 600mA Max: 1.0A Recommend:5.0V@1.5A
	253	VCC5V0_SYS	P				VCC5V0_SYS	
	255	VCC5V0_SYS	P				VCC5V0_SYS	
	257	VCC5V0_SYS	P				VCC5V0_SYS	
	259	VCC5V0_SYS	P				VCC5V0_SYS	
Part B	**PIN**	**CORE_3566JD4 pin definition**	**Pad type**	**IO Pull**	**IO Power domain**	**RK3566 Pin Number**	**Function for (MB-RK3566-JD4)**	**Defual function description**
	2	GND	G				GND	
	4	CIF_CLKIN/EBC_SDCLK/GMAC1_MCLKINOUT_M1/UART1_CTSn_M1/I2S2_SCLK_RX_M1/GPIO4_C1_d	I/O	DOWN	1.8V	AB1	EDP_LCD_EN	EDP LCD EN
	6	CIF_D11/EBC_SDDO11/GMAC1_RXD2_M1/PDM_SDI1_M1/GPIO4_A1_d	I/O	DOWN	1.8V	1P2	FAN_CTL	FAN control
	8	CIF_D12/EBC_SDDO12/GMAC1_RXD3_M1/UART7_TX_M2/PDM_SDI2_M1/GPIO4_A2_d	I/O	DOWN	1.8V	1R1	EDP_TP_INT	EDP TP interrupt

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	10	CIF_D13/EBC_SDDO13/GMAC1_RXCLK_M1/UART7_RX_M2/PDM_SDI3_M1/GPIO4_A3_d	I/O	DOWN	1.8V	AJ2	EDP_TP_RESET	EDP TP Reset
	12	CIF_D10/EBC_SDDO10/GMAC1_TXCLK_M1/PDM_CLK1_M1/GPIO4_A0_d	I/O	DOWN	1.8V	AK2	GPIO4_A0_D	GPIO4_A0_D
	14	CIF_D8/EBC_SDDO8/GMAC1_TXD2_M1/UART1_TX_M1/PDM_CLK0_M1/GPIO3_D6_d	I/O	DOWN	1.8V	AL2	GPIO3_D6_D	GPIO3_D6_D
	16	CIF_D9/EBC_SDDO9/GMAC1_TXD3_M1/UART1_RX_M1/PDM_SDI0_M1/GPIO3_D7_d	I/O	DOWN	1.8V	AL1	GPIO3_D7_D	GPIO3_D7_D
	18	CIF_D6/EBC_SDDO6/SDMMC2_DET_M0/I2S1_SDI2_M1/VOP_BT656_D6_M1/GPIO3_D4_d	I/O	DOWN	1.8V	1R2	MIPI_PDN0_CAM	MIPI CSI powerdown
	20	CIF_D7/EBC_SDDO7/SDMMC2_PWREN_M0/I2S1_SDI3_M1/VOP_BT656_D7_M1/GPIO3_D5_d	I/O	DOWN	1.8V	1T1	MIPI_RESET0_CAM	MIPI CSI reset
	22	I2C2_SDA_M1/EBC_GDSP/ISP_FLASH_TRIGIN/VOP_BT656_CLK_M1/GPIO4_B4_d	I/O	DOWN	1.8V	AF1	NC	NC
	24	I2C2_SCL_M1/EBC_SDSHR/I2S1_SDO3_M1/GPIO4_B5_d	I/O	DOWN	1.8V	AD1	LCD0_PWR_EN	MIPI DSI0 LCD Power EN
	26	ISP_FLASHTRIGOUT/EBC_SDCE0/GMAC1_TXEN_M1/SPI3_CS0_M0/I2S1_SCLK_RX_M1/GPIO4_A6_d	I/O	DOWN	1.8V	AH2	TP0_RST_L_GPIO4_A6	MIPI DSI0 TP Reset
	28	CIF_D0/EBC_SDDO0/SDMMC2_D0_M0/I2S1_MCLK_M1/VOP_BT656_D0_M1/GPIO3_C6_d	I/O	DOWN	1.8V	1T2	LCD0_RST_L_GPIO3_C6	MIPI DSI0 LCD Reset
	30	CIF_D1/EBC_SDDO1/SDMMC2_D1_M0/I2S1_SCLK_TX_M1/VOP_BT656_D1_M1/GPIO3_C7_d	I/O	DOWN	1.8V	1U1	TP1_RST_L_GPIO3_C7	MIPI DSI1 TP Reset
	32	CIF_D5/EBC_SDDO5/SDMMC2_CLK_M0/I2S1_SDI1_M1/VOP_BT656_D5_M1/GPIO3_D3_d	I/O	DOWN	1.8V	AM1	LCD1_RST_L_GPIO3_D3	MIPI DSI1 LCD Reset
	34	CIF_D4/EBC_SDDO4/SDMMC2_CMD_M0/I2S1_SDI0_M1/VOP_BT656_D4_M1/GPIO3_D2_d	I/O	DOWN	1.8V	AM2	VCC5V0_HOST_EN	HOST Power EN
	36	CIF_D3/EBC_SDDO3/SDMMC2_D3_M0/I2S1_SDO0_M1/VOP_BT656_D3_M1/GPIO3_D1_d	I/O	DOWN	1.8V	AN2	VCC5V0_OTG_EN	OTG Power EN
	38	CIF_D2/EBC_SDDO2/SDMMC2_D2_M0/I2S1_LRCK_TX_M1/VOP_BT656_D2_M1/GPIO3_D0_d	I/O	DOWN	1.8V	AP1	EAR_CTL	Headphone output control, Active H
	40	VOP_BT1120_D15/SPI1_CLK_M1/UART5_RX_M1/I2S1_SCLK_RX_M2/GPIO3_C3_d	I/O	DOWN	3.3V	AR2	SPI1_CLK_M1/UART5_RX	SPI1_CLK_M1/UART5_RX
	42	VOP_BT1120_D14/SPI1_MISO_M1/UART5_TX_M1/I2S1_SDO3_M2/GPIO3_C2_d	I/O	DOWN	3.3V	AP3	SPI1_MISO_M1/UART5_TX	SPI1_MISO_M1/UART5_TX
	44	VOP_BT1120_D13/SPI1_MOSI_M1/PCIE20_PERSTn_M1/I2S1_SDO2_M2/GPIO3_C1_d	I/O	DOWN	3.3V	AP4	SPI1_MOSI_M1	SPI1_MOSI_M1
	46	VOP_BT1120_D0/SPI1_CS0_M1/SDMMC2_D0_M1/GPIO3_A1_d	I/O	DOWN	3.3V	1U5	SPI1_CS0_M1	SPI1_CS0_M1

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	48	EDP_HPDIN_M0/SPDIF_TX_M2/SATA2_ACT_LED/I2S3_LRCK_M1/GPIO4_C4_d	I/O	DOWN	3.3V	1V6	SATA2_ACT_LED	SATA2_ACT_LED EN,Active H
	50	PWM15_IR_M1/SPI3_MOSI_M1/I2S3_SCLK_M1/GPIO4_C3_d	I/O	DOWN	3.3V	AP12	TP_INT_L_GPIO4_C3	MIPI DSI0 TP interrupt
	52	PWM13_M1/SPI3_CS0_M1/SATA0_ACT_LED/UART9_RX_M1/I2S3_SDI_M1/GPIO4_C6_d	I/O	DOWN	3.3V	1T6	UART9_RX_M1	UART9_RX_M1
	54	PWM12_M1/SPI3_MISO_M1/SATA1_ACT_LED/UART9_TX_M1/I2S3_SDO_M1/GPIO4_C5_d	I/O	DOWN	3.3V	1U6	UART9_TX_M1	UART9_TX_M1
	56	PWM14_M1/SPI3_CLK_M1/I2S3_MCLK_M1/GPIO4_C2_d	I/O	DOWN	3.3V	1U7	EDP_HPD	EDP_HPD det ,Active H
	58	GND	G				GND	
	60	MIPI_CSI_RX_CLK1N	AI		-	1U8	MIPI_CSI_RX_CLK1N	MIPI_CSI_RX_CLK1N
	62	MIPI_CSI_RX_CLK1P	AI		-	1V8	MIPI_CSI_RX_CLK1P	MIPI_CSI_RX_CLK1P
	64	GND	G				GND	
	66	MIPI_CSI_RX_CLK0N	AI		-	1U9	MIPI_CSI_RX_CLK0N	MIPI_CSI_RX_CLK0N
	68	MIPI_CSI_RX_CLK0P	AI		-	1V9	MIPI_CSI_RX_CLK0P	MIPI_CSI_RX_CLK0P
	70	GND	G				GND	
	72	MIPI_DSI_TX1_CLKN	AO		-	1U11	MIPI_DSI_TX1_CLKN	MIPI_DSI_TX1_CLKN
	74	MIPI_DSI_TX1_CLKP	AO		-	1V11	MIPI_DSI_TX1_CLKP	MIPI_DSI_TX1_CLKP
	76	GND	G				GND	
	78	EMMC_RSTn/FSPI_D2/FLASH_WPn/GPIO1_C7_d	I/O	DOWN	3.3V	1B16	FSPI_D2	FSPI_D2
	80	FSPI_D3/FLASH_CS1n/GPIO1_D4_u	I/O	UP	3.3V	1C15	FSPI_D3	FSPI_D3
	82	FSPI_CLK/FLASH_ALE/GPIO1_D0_d	I/O	DOWN	3.3V	1A15	FSPI_CLK	FSPI_CLK
	84	FSPI_CS0n/FLASH_CS0n/GPIO1_D3_u	I/O	UP	3.3V	1B17	FSPI_CS0n	FSPI_CS0n
	86	FSPI_D0/FLASH_RDY/GPIO1_D1_u	I/O	UP	3.3V	1A17	FSPI_D0	FSPI_D0

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	88	FSPI_D1/FLASH_RDn/GPIO1_D2_u	I/O	UP	3.3V	1A18	FSPI_D1	FSPI_D1
	90	MIPI_DSI_TX0_CLKN/LVDS_TX0_CLKN	AO		-	1V15	MIPI_DSI_TX0_CLKN/LVDS_TX0_CLKN	MIPI_DSI_TX0_CLKN/LVDS_TX0_CLKN
	92	MIPI_DSI_TX0_CLKP/LVDS_TX0_CLKP	AO		-	1V16	MIPI_DSI_TX0_CLKP/LVDS_TX0_CLKP	MIPI_DSI_TX0_CLKP/LVDS_TX0_CLKP
	94	GND	G				GND	
	96	HDMI_TX_HPDIN	I		5.0V	1V17	HDMI_TX_HPDIN	HDMI_TX_HPD INPUT,Active H
	98	PWM7_IR/SPI0_CS0_M0/GPIO0_C6_d	I/O	DOWN	3.3V	1T18	PWM7_IR	PWM7_IR
	100	UART2_RX_M0/GPIO0_D0_u	I/O	UP	3.3V	1V19	UART2_RX_M0_DEBUG	UART2_RX_M0 (DEBUG)
	102	UART2_TX_M0/GPIO0_D1_u	I/O	UP	3.3V	1U18	UART2_TX_M0_DEBUG	UART2_TX_M0 (DEBUG)
	104	HDMITX_CEC_M1/PWM0_M1/UART0_CTSn/GPIO0_C7_d	I/O	DOWN	3.3V	1V20	LCD0_BL_PWM0_M1	LCD0 Backlight EN, Active H
	106	PWM6/SPI0_MISO_M0/GPIO0_C5_d	I/O	DOWN	3.3V	1U19	EDP_BL_EN	EDP Backlight EN, Active H
	108	I2C2_SCL_M0/SPI0_CLK_M0/PCIE20_WAKEn_M0/PWM1_M1/GPIO0_B5_u	I/O	UP	3.3V	1U20	I2C2_SCL_M0	I2C2_SCL_M0 Core board Pull up resistance 2.2K
	110	I2C2_SDA_M0/SPI0_MOSI_M0/PCIE20_PERSTn_M0/PWM2_M1/GPIO0_B6_u	I/O	UP	3.3V	1R16	I2C2_SDA_M0	I2C2_SDA_M0 Core board Pull up resistance 2.2K
	112	PWM2_M0/NPUAVS/UART0_TX/MCU_JTAG_TDI/GPIO0_C1_d	I/O	DOWN	3.3V	1R18	UART0_TX	UART0_TX
	114	PWM1_M0/GPUAVS/UART0_RX/GPIO0_C0_d	I/O	DOWN	3.3V	1T19	UART0_RX	UART0_RX
	116	PWM3_IR/EDP_HPDIN_M1/MCU_JTAG_TMS/GPIO0_C2_d	I/O	DOWN	3.3V	AM37	BL_EN_GPIO0_C2	BL_EN
	118	GPIO0_D6_d	I/O	DOWN	1.8V	1P20	VCC_5V_EN_GPIO0_D6_D	VCC_5V power EN
	120	REFCLK_OUT/GPIO0_A0_d	I/O	DOWN	3.3V	1P18	BL_EN_GPIO0_A0_D	MIPI DSI1 Backlight EN, Active H
	122	GPIO0_D3_d	I/O	DOWN	1.8V	1N19	4G_PWR_EN_GPIO0_D3_D	4G Moudle Power EN
	124	GPIO0_D5_d	I/O	DOWN	1.8V	1N20	EDP_TP_PWREN	EDP TP Power EN
	126	PCIE20_REFCLKP	AI/O		-	1K19	PCIE20_REFCLKP	PCIE20_REFCLKP

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	128	PCIE20_REFCLKN	AI/O		-	1K20	PCIE20_REFCLKN	PCIE20_REFCLKN
	130	GND	G				GND	
	132	USB3_HOST1_DP	AI/O		-	1J19	USB3_HOST1_DP	USB3_HOST1_DP
	134	USB3_HOST1_DM	AI/O		-	1J20	USB3_HOST1_DM	USB3_HOST1_DM
	136	GND	G				GND	
	138	SDMMC0_D1/UART2_RX_M1/UART6_RX_M1/PWM9_M1/GPIO1_D6_u	I/O	UP	Auto: 1.8V/3.3V	1F19	SDMMC0_D1	SDMMC0_D1
	140	SDMMC0_D3/ARM_JTAG_TMS/UART5_RTSn_M0/GPIO2_A0_u	I/O	UP	Auto: 1.8V/3.3V	1F18	SDMMC0_D3	SDMMC0_D3
	142	SDMMC0_D0/UART2_TX_M1/UART6_TX_M1/PWM8_M1/GPIO1_D5_u	I/O	UP	Auto: 1.8V/3.3V	1E20	SDMMC0_D0	SDMMC0_D0
	144	SDMMC0_CMD/PWM10_M1/UART5_RX_M0/GPIO2_A1_u	I/O	UP	Auto: 1.8V/3.3V	1E19	SDMMC0_CMD	SDMMC0_CMD
	146	SDMMC0_D2/ARM_JTAG_TCK/UART5_CTSn_M0/GPIO1_D7_u	I/O	UP	Auto: 1.8V/3.3V	1D20	SDMMC0_D2	SDMMC0_D2
	148	SDMMC0_CLK/TEST_CLKOUT/UART5_TX_M0/GPIO2_A2_d	I/O	UP	Auto: 1.8V/3.3V	G38	SDMMC0_CLK	SDMMC0_CLK Core board series resistance 22R
	150	SARADC_VIN1	AI		1.8V	1C17	SARADC_VIN1	SARADC_VIN1 Core board Pull up resistance 10K
	152	SARADC_VIN0	AI		1.8V	1D17	RECOVERY	RECOVERY Core board Pull up resistance 10K
	154	SARADC_VIN2	AI		1.8V	1B18	SARADC_VIN2	SARADC_VIN2 Core board Pull up resistance 10K
	156	SARADC_VIN3	AI		1.8V	1A19	SARADC_VIN3	SARADC_VIN3 Core board Pull up resistance 10K
	158	GND	G				GND	
	160	SDMMC1_D2/UART7_RX_M0/GPIO2_A5_u	I/O	UP	1.8V	1D18	SDMMC1_D2	SDMMC1_D2
	162	SDMMC1_D3/UART7_TX_M0/GPIO2_A6_u	I/O	UP	1.8V	1A20	SDMMC1_D3	SDMMC1_D3
	164	SDMMC1_CMD/UART9_RX_M0/GPIO2_A7_u	I/O	UP	1.8V	B35	SDMMC1_CMD	SDMMC1_CMD
	166	SDMMC1_CLK/UART9_TX_M0/GPIO2_B0_d	I/O	UP	1.8V	B37	SDMMC1_CLK	SDMMC1_CLK Core board series resistance 22R

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	168	SDMMC1_D0/UART6_RX_M0/GPIO2_A3_u	I/O	UP	1.8V	1C19	SDMMC1_D0	SDMMC1_D0
	170	SDMMC1_D1/UART6_TX_M0/GPIO2_A4_u	I/O	UP	1.8V	1E18	SDMMC1_D1	SDMMC1_D1
	172	GND	G				GND	
	174	I2S2_SCLK_TX_M0/UART7_CTSn_M0/SPI2_MISO_M0/GPIO2_C2_d	I/O	DOWN	1.8V	C37	I2S2_SCLK_TX_M0	I2S2_SCLK_TX_M0
	176	I2S2_LRCK_TX_M0/UART9_RTSn_M0/SPI2_MOSI_M0/GPIO2_C3_d	I/O	DOWN	1.8V	1D19	I2S2_LRCK_TX_M0	I2S2_LRCK_TX_M0
	178	I2S2_SDO_M0/UART9_CTSn_M0/SPI2_CS0_M0/GPIO2_C4_d	I/O	DOWN	1.8V	1B20	I2S2_SDO_M0	I2S2_SDO_M0
	180	I2S2_SDI_M0/UART8_TX_M0/SPI2_CS1_M0/GPIO2_C5_d	I/O	DOWN	1.8V	D37	I2S2_SDI_M0	I2S2_SDI_M0
	182	PWM15_IR_M0/SPDIF_TX_M1/GMAC1_MDIO_M0/UART7_RX_M1/I2S1_LRCK_RX_M2/GPIO3_C5_d	I/O	DOWN	3.3V	1P4	MAC_MDIO	MAC_MDIO
	184	PWM14_M0/VOP_PWM_M1/GMAC1_MDC_M0/UART7_TX_M1/PDM_CLK1_M2/GPIO3_C4_d	I/O	DOWN	3.3V	1P3	MAC_MDC	MAC_MDC
	186	VOP_BT1120_CLK/GMAC1_TXCLK_M0/I2S3_SDI_M0/SDMMC2_CLK_M1/GPIO3_A6_d	I/O	DOWN	3.3V	1U4	PHY_TXCLK	PHY_TXCLK Core board series resistance 22R
	188	VOP_BT1120_D5/GMAC1_RXCLK_M0/SDMMC2_DET_M1/GPIO3_A7_d	I/O	DOWN	3.3V	1V3	MAC_RXCLK	MAC_RXCLK
	190	GND	G				GND	
	192	PWM13_M0/GMAC1_MCLKINOUT_M0/UART3_RX_M1/PDM_SDI3_M2/GPIO3_C0_d	I/O	DOWN	3.3V	AR4	MAC_CLK	MAC_CLK core board series resistance 22R
	194	VOP_BT1120_D8/GMAC1_RXD1_M0/UART4_TX_M1/PWM9_M0/GPIO3_B2_d	I/O	DOWN	3.3V	AR6	MAC_RXD1	MAC_RXD1
	196	VOP_BT1120_D4/GMAC1_RXD3_M0/I2S3_SDO_M0/SDMMC2_CMD_M1/GPIO3_A5_d	I/O	DOWN	3.3V	AP9	MAC_RXD3	MAC_RXD3
	198	VOP_BT1120_D7/GMAC1_RXD0_M0/UART4_RX_M1/PWM8_M0/GPIO3_B1_d	I/O	DOWN	3.3V	AP7	MAC_RXD0	MAC_RXD0
	200	VOP_BT1120_D3/GMAC1_RXD2_M0/I2S3_LRCK_M0/SDMMC2_D3_M1/GPIO3_A4_d	I/O	DOWN	3.3V	AR9	MAC_RXD2	MAC_RXD2
	202	VOP_BT1120_D9/GMAC1_RXDV_CRS_M0/I2C5_SCL_M0/PDM_SDI0_M2/GPIO3_B3_d	I/O	DOWN	3.3V	AP6	MAC_RXDV	MAC_RXDV
	204	VOP_BT1120_D11/GMAC1_TXD0_M0/I2C3_SCL_M1/PWM10_M0/GPIO3_B5_d	I/O	DOWN	3.3V	1T4	PHY_TXD0	PHY_TXD0 core board series resistance 22R
	206	VOP_BT1120_D1/GMAC1_TXD2_M0/I2S3_MCLK_M0/SDMMC2_D1_M1/GPIO3_A2_d	I/O	DOWN	3.3V	AR10	PHY_TXD2	PHY_TXD2 core board series resistance 22R

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	208	VOP_BT1120_D2/GMAC1_TXD3_M0/I2S3_SCLK_M0/SDMMC2_D2_M1/GPIO3_A3_d	I/O	DOWN	3.3V	AP10	PHY_TXD3	PHY_TXD3 core board series resistance 22R
	210	VOP_BT1120_D12/GMAC1_TXD1_M0/I2C3_SDA_M1/PWM11_IR_M0/GPIO3_B6_d	I/O	DOWN	3.3V	1V2	PHY_TXD1	PHY_TXD1 core board series resistance 22R
	212	PWM12_M0/GMAC1_TXEN_M0/UART3_TX_M1/PDM_SDI2_M2/GPIO3_B7_d	I/O	DOWN	3.3V	AP5	PHY_TXEN	PHY_TXEN core board series resistance 22R
	214	VOP_BT1120_D10/GMAC1_RXER_M0/I2C5_SDA_M0/PDM_SDI1_M2/GPIO3_B4_d	I/O	DOWN	3.3V	1U3	PHY_INT/PMEB	Phy interrupt Input, Active L
	216	PWM0_M0/CPUAVS/GPIO0_B7_d	I/O	DOWN	3.3V	1R17	PHY_RST	Phy reset Output, Active L
	218	RESET_KEY	I	UP	3.3V	AG38	SYS_RESET	System reset input Reset key , Active L core board Pull up resistance 10K ,
	220	PMIC_EXT_EN	O		5.0V		NC	EXT_EN Output, Active H (NC)
	222	GND	G				GND	
	224	VCCIO_WL	P		1.8V		VCCIO_WL	1.8V Output for WIFI VCCIO (Pin224/225 Total Max 300mA)
	226	VCC3V3_SD	P		3.3V		VCC3V3_SD	3.3V Output For TF Card Power (Pin226/227 Total Max:300mA)
	228	VCC_1V8	P		1.8V		VCC_1V8	1.8V Output ,(Pin228/229 Total Max:500mA)
	230	VCCIO_ACODEC	P		3.3V		VCCIO_ACODEC	3.3V Output For codec, (Pin230/231 Total Max:300mA)
	232	PMIC_32KOUT_WIFI			3.3V		PMIC_32KOUT_WIFI	32.768KHz for WIFI
	234	VCC3V3_SYS	P		3.3V		VCC3V3_SYS	3.3V Output , (Pin234/235 Total Max:500mA)
	236	SPKP_OUT	O		5.0V		SPKP_OUT	RK809 Speak out + MAX:1.3W ClassD @ 8Ω
	238	SPKN_OUT	O		5.0V		SPKN_OUT	RK809 Speak out - MAX:1.3W ClassD @ 8Ω
	240	HPL_OUT	O		+/2.7V		HPL_OUT	RK809 HeadPhone_OUT L MAX:0.5Vrms @ 32Ω/ 0.8Vrms @ 300Ω
	242	HPR_OUT	O		+/2.7V		HPR_OUT	RK809 HeadPhone_OUT R MAX:0.5Vrms @ 32Ω/ 0.8Vrms @ 300Ω

Interface definition

	244	GND	G				GND	GND
	246	GND	G				GND	
	248	GND	G				GND	
	250	GND	G				GND	
	252	VCC5V0_SYS	P		5.0V		VCC5V0_SYS	Input Voltage 5.0V +/-5% CORE BOARD 5.0V Supply current: Normal: 600mA Max: 1.0A Recommend:5.0V@1.5A
	254	VCC5V0_SYS	P				VCC5V0_SYS	
	256	VCC5V0_SYS	P				VCC5V0_SYS	
	258	VCC5V0_SYS	P				VCC5V0_SYS	
	260	VCC5V0_SYS	P				VCC5V0_SYS	

T-CHIP INTELLIGENCE TECHNOLOGY

Contact Us
(+86)18688117175

E-mail
global@t-firefly.com

Website
www.t-firefly.com

Address
Room 2101, Hongyu Building, #57 Zhongshan 4Rd, East District, Zhongshan, Guangdong, China.