

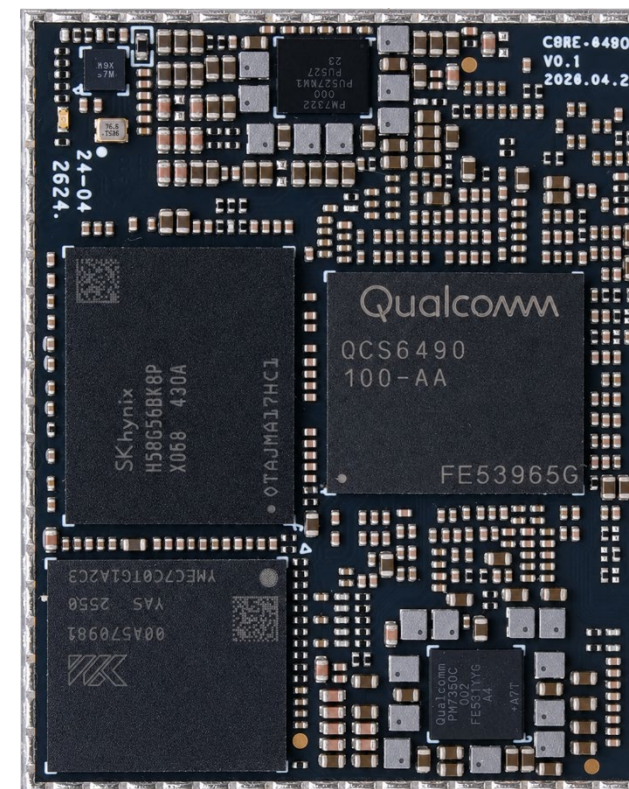


# CORE-6490L

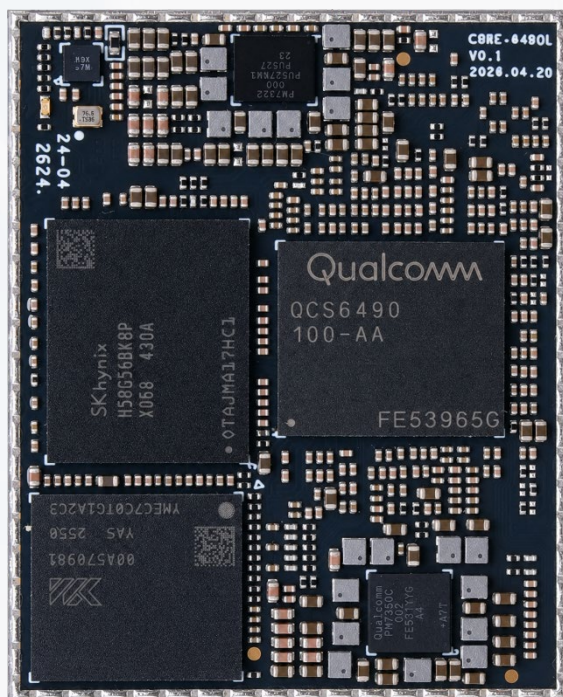
高通 QCS6490 核心板

V0.1 2026-9-21

天启集团



# 产品特点 Product features



## 八核异构处理器 QCS6490

采用高通 QCS6490 八核64位异构处理器，基于 ARM 架构，主频高达 2.7GHz，为高性能计算和多任务处理提供了强大支持



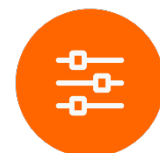
## 集成 Adreno 643 GPU

集成 Adreno 643 GPU，支持 OpenGL ES 3.2, Vulkan 1.x, OpenCL 2.0, DX FL12 图形接口，可高效承载图形渲染、画面处理等任务，满足边缘场景可视化显示需求



## 支持 5 路摄像头输入、双屏异显

板载 5 路 MIPI-CSI，支持 5 路摄像头同步采集；配备 MIPI-DSI、DP、eDP 显示接口，支持双屏异显/同显



## 丰富的扩展接口

拥有 PCIe3.0、USB3.0、MIPI-CSI、MIPI-DSI、DP、SDIO3.0、SPI、I2C、UART 等扩展接口，满足不同场景的外设扩展需求



## 4K 视频编解码

支持 4K@60fps H.265/H.264/VP9 视频解码、4K@30fps H.265/H.264 视频编码，支持 4K 解码与 1080P 编码并发处理



## 内置 12TOPS 算力 NPU

内置 12TOPS NPU，支持 INT4/INT8/INT16/FP16 混合运算，能进行智能的数据处理、语音识别、图像分析，满足绝大多数终端设备边缘计算AI应用需求



## 463P LGA 封装

核心板采用463Pin LGA封装，传输与稳定性更强，尺寸小巧，仅43x35mm，可便捷嵌入各类整机设备，快速完成产品集成



## 广泛的应用场景

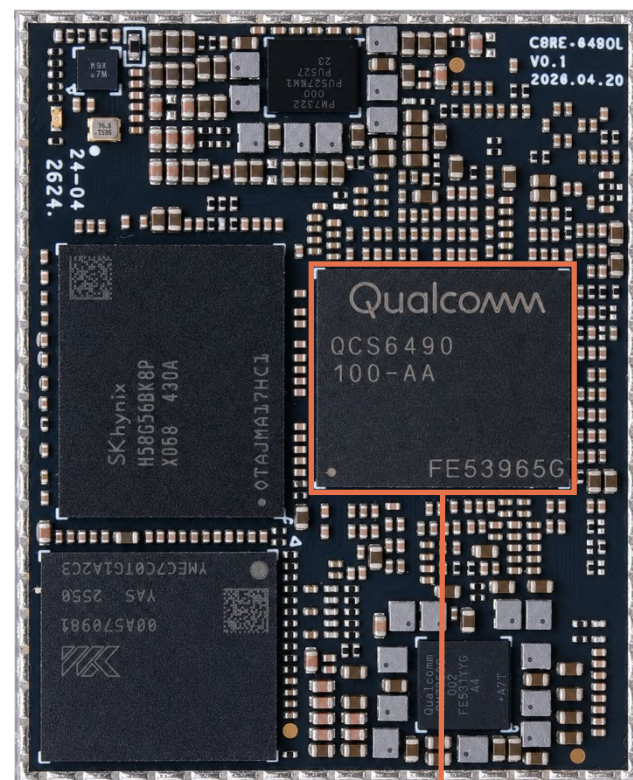
广泛适用于：边缘计算、端侧大模型私有化部署、机器人、工业视觉检测、智能安防、车载多目感知、智慧零售等行业领域

规格参数 Specifications

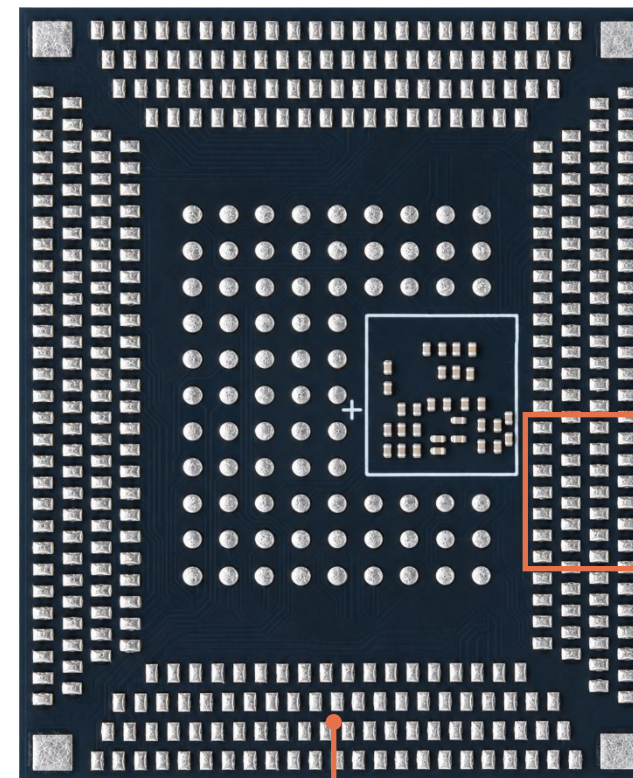


| 规格参数 |      |                                                                                                                                                                                                    |
|------|------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 基本参数 | SOC  | Qualcomm QCS6490                                                                                                                                                                                   |
|      | CPU  | 高通 Kryo CPU：八核64位处理器（1×Kryo Prime 2.7GHz + 3×Kryo Gold 2.4GHz + 4×Kryo Silver 1.9GHz），6nm先进工艺，主频高达2.7GHz                                                                                           |
|      | GPU  | Adreno 643 GPU，支持 OpenGL ES 3.2, Vulkan 1.x, OpenCL 2.0, DX FL12                                                                                                                                   |
|      | NPU  | Hexagon 770 NPU，12 稠密 TOPS                                                                                                                                                                         |
|      | ISP  | Spectra 570L ISP，零快门延时(ZSL)模式：64MP / 36MP+22MP / 3×22MP @30fps，非零快门延时(non-ZSL)模式：192MP                                                                                                             |
|      | 编解码  | 视频解码：1×4K@60fps/2×4K@30fps/4×1080P@60fps（H.265/HEVC、H.264/AVC、VP9）<br>视频编码：1×4K@30fps/4×1080P@60fps（H.265/HEVC、H.264/AVC）<br>视频并发能力：1080P@60fps 解码 + 1080P@60fps 编码 / 4K@30fps 解码 + 1080P@30fps 编码 |
|      | 内存   | 4GB/8GB LPDDR5                                                                                                                                                                                     |
|      | 存储   | 64GB eMMC                                                                                                                                                                                          |
|      | 电源   | DC 3.8V                                                                                                                                                                                            |
|      | 功耗   | 典型功耗：0.532W(3.8V/140mA)，最大功耗：6.08W(3.8V/1600mA)，休眠功耗：0.133W(3.8V/35mA)                                                                                                                             |
|      | 系统   | Qualcomm Linux、Ubuntu                                                                                                                                                                              |
|      | 软件支持 | 支持Transformer架构下超大规模参数模型的私有化部署，如Deepseek-R1系列、Gemma系列、Llama系列、Qwen系列、Phi系列等大语言模型<br>支持QNN AI推理框架，以及TensorFlow、TensorFlow Lite、PyTorch、MXNet、Keras、Caffe等多种深度学习框架                                   |
|      | 接口类型 | LGA封装，总引脚数 463Pin，14 层 PCB 板设计；焊盘尺寸：1mm × 0.6mm；模块平整度管控：0.15mm                                                                                                                                     |
|      | 尺寸   | 43.0mm × 35.0mm × 2.33mm                                                                                                                                                                           |
|      | 重量   | ≈7g                                                                                                                                                                                                |
|      | 环境   | 工作温度：-20℃ ~ 60℃，存储温度：-20℃ ~ 70℃，存储湿度：10% ~ 90%RH（无凝露）                                                                                                                                              |
| 接口参数 | 网络   | 以太网、WiFi/蓝牙可通过 PCIe、USB 链路扩展                                                                                                                                                                       |
|      | 视频输入 | 5 × 4Lane MIPI-CSI，支持D-PHY v1.2/C-PHY v1.2，支持5路摄像头同步采集                                                                                                                                             |
|      | 视频输出 | 1 × MIPI-DSI（D-PHY 1.2 或 C-PHY 1.2，4lane，最高分辨率4K@30Hz）<br>1 × USB DisplayPort1.4（最高分辨率 4K@30Hz）<br>1 × eDP1.4（最高分辨率 4K@30Hz）<br>支持双屏异显/双屏同显                                                        |
|      | 音频   | 2 × SoundWire、3 × DMIC、5 × MI2S（每组具备 2 路数据通道，可实现全双工立体声，或最多 4 通道收发应用）、1 × MI2S（支持 4 路数据通道，可实现最高 8 通道收发应用）                                                                                           |
|      | PCIe | 1 × PCIe Gen3x1、1 × PCIe Gen3x2（支持 NVMe）                                                                                                                                                           |
|      | USB  | 1 × USB3.0 Gen1（5Gbps，支持 DP 视频输出）、1 × USB2.0                                                                                                                                                       |
|      | SDIO | 2 × SDIO3.0                                                                                                                                                                                        |
|      | 其他接口 | 16 × QUPv3 Bus（QUPv3 可灵活配置为 UART、SPI 或 I2C 功能，存在复用关系，不能全部使用，根据实际情况分配）                                                                                                                              |

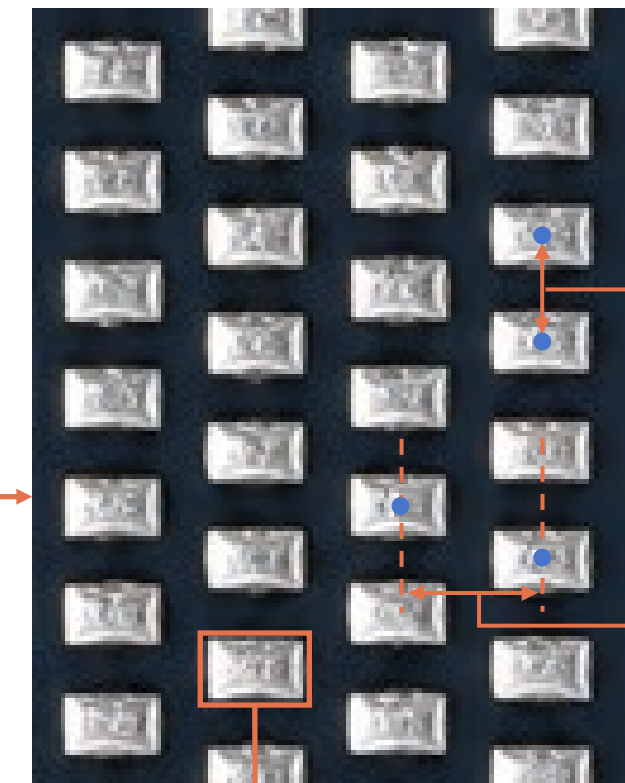
# 接口描述 Interface description



Qualcomm QCS6490  
主频最高 2.7GHz

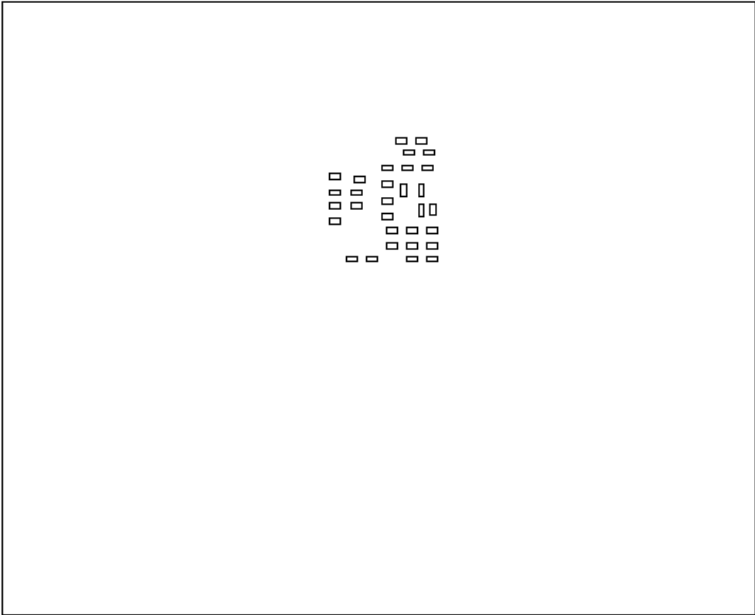
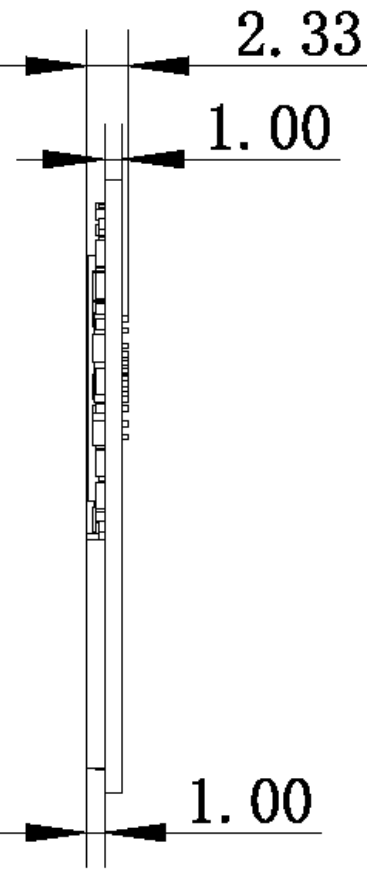
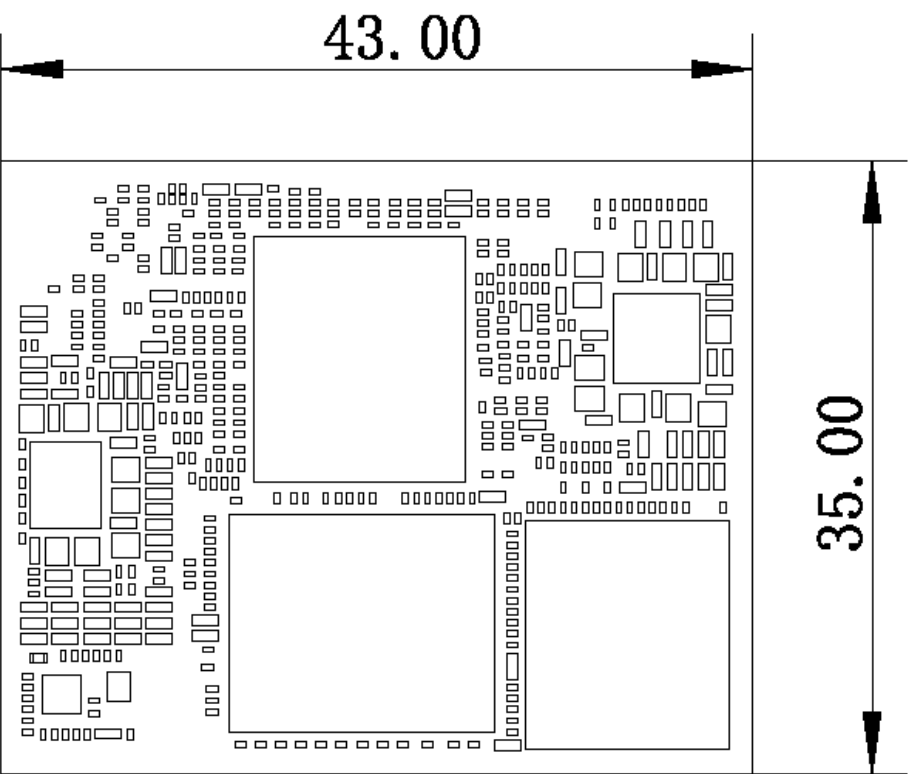


LGA封装  
463Pin, 模块平整度管控: 0.15mm



焊盘尺寸  
1mm x 0.6mm

# 产品尺寸 Dimension





# 接口定义 Interface definition

| <b>Notes1:</b><br>Pin type: I = input, O = output, I/O = input/output (bidirectional), G= Ground , P = power supply , DOWN = Internal pull down , UP = Internal pull UP , L = Lowe Level , H = High level |      |                               |                  |          |                 |          |                                   |                              |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------|-------------------------------|------------------|----------|-----------------|----------|-----------------------------------|------------------------------|
| Part                                                                                                                                                                                                      | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | I/O Pull | Function for Floor (CORE-6490JD4) | Default function description |
| J1A                                                                                                                                                                                                       | CA1  | DSI0_B1_CLK_P                 | AC43             | I/O      | -               |          | DSI0_B1_CLK_P                     | DSI0_B1_CLK_P                |
|                                                                                                                                                                                                           | CB1  | DSI0_C1_CLK_M                 | AC45             | I/O      | -               |          | DSI0_C1_CLK_M                     | DSI0_C1_CLK_M                |
|                                                                                                                                                                                                           | CA2  | DSI0_A0_LN0_P                 | AG43             | I/O      | -               |          | DSI0_A0_LN0_P                     | DSI0_A0_LN0_P                |
|                                                                                                                                                                                                           | CB2  | DSI0_B0_LN0_M                 | AG45             | I/O      | -               |          | DSI0_B0_LN0_M                     | DSI0_B0_LN0_M                |
|                                                                                                                                                                                                           | CA3  | DSI0_C0_LN1_P                 | AE43             | I/O      | -               |          | DSI0_C0_LN1_P                     | DSI0_C0_LN1_P                |
|                                                                                                                                                                                                           | CB3  | DSI0_A1_LN1_M                 | AD44             | I/O      | -               |          | DSI0_A1_LN1_M                     | DSI0_A1_LN1_M                |
|                                                                                                                                                                                                           | CA4  | DSI0_A2_LN2_P                 | AF44             | I/O      | -               |          | DSI0_A2_LN2_P                     | DSI0_A2_LN2_P                |
|                                                                                                                                                                                                           | CB4  | DSI0_B2_LN2_M                 | AF46             | I/O      | -               |          | DSI0_B2_LN2_M                     | DSI0_B2_LN2_M                |
|                                                                                                                                                                                                           | CA5  | DSI0_C2_LN3_P                 | AE45             | I/O      | -               |          | DSI0_C2_LN3_P                     | DSI0_C2_LN3_P                |
|                                                                                                                                                                                                           | CB5  | DSI0_NC_LN3_M                 | AD46             | I/O      | -               |          | DSI0_NC_LN3_M                     | DSI0_NC_LN3_M                |
|                                                                                                                                                                                                           | BA15 | EDP0_AUX_CONN_P               | AN43             | B        | -               |          | EDP0_AUX_CONN_P                   | EDP0_AUX_CONN_P              |
|                                                                                                                                                                                                           | BB15 | EDP0_AUX_CONN_M               | AN45             | B        | -               |          | EDP0_AUX_CONN_M                   | EDP0_AUX_CONN_M              |
|                                                                                                                                                                                                           | BA16 | EDP0_TX0_FLT_P                | AM44             | O        | -               |          | EDP0_TX0_FLT_P                    | EDP0_TX0_FLT_P               |
|                                                                                                                                                                                                           | BB16 | EDP0_TX0_FLT_M                | AM46             | O        | -               |          | EDP0_TX0_FLT_M                    | EDP0_TX0_FLT_M               |
|                                                                                                                                                                                                           | BA17 | EDP0_TX1_FLT_P                | AL45             | O        | -               |          | EDP0_TX1_FLT_P                    | EDP0_TX1_FLT_P               |
|                                                                                                                                                                                                           | BB17 | EDP0_TX1_FLT_M                | AL47             | O        | -               |          | EDP0_TX1_FLT_M                    | EDP0_TX1_FLT_M               |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | I/O Pull | Function for Floor (CORE-6490JD4)   | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|----------|-------------------------------------|------------------------------|
| J1A  | BA18 | EDP0_TX2_FLT_P                | AK44             | O        | -               |          | EDP0_TX2_FLT_P                      | EDP0_TX2_FLT_P               |
|      | BB18 | EDP0_TX2_FLT_M                | AK46             | O        | -               |          | EDP0_TX2_FLT_M                      | EDP0_TX2_FLT_M               |
|      | BA19 | EDP0_TX3_FLT_P                | AJ43             | O        | -               |          | EDP0_TX3_FLT_P                      | EDP0_TX3_FLT_P               |
|      | BB19 | EDP0_TX3_FLT_M                | AJ45             | O        | -               |          | EDP0_TX3_FLT_M                      | EDP0_TX3_FLT_M               |
| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull  | Function for Floor (MB-JD4-QCS8550) | Default function description |
| J1B  | AA1  | CSI1_NC_CLK_P                 | AN7              | I/O      | -               |          | CSI1_NC_CLK_P                       | CSI1_NC_CLK_P                |
|      | AB1  | CSI1_A0_CLK_M                 | AN5              | I/O      | -               |          | CSI1_A0_CLK_M                       | CSI1_A0_CLK_M                |
|      | AA2  | CSI1_B0_LN0_P                 | AP6              | I/O      | -               |          | CSI1_B0_LN0_P                       | CSI1_B0_LN0_P                |
|      | AB2  | CSI1_C0_LN0_M                 | AP4              | I/O      | -               |          | CSI1_C0_LN0_M                       | CSI1_C0_LN0_M                |
|      | AA3  | CSI1_A1_LN1_P                 | AN1              | I/O      | -               |          | CSI1_A1_LN1_P                       | CSI1_A1_LN1_P                |
|      | AB3  | CSI1_B1_LN1_M                 | AN3              | I/O      | -               |          | CSI1_B1_LN1_M                       | CSI1_B1_LN1_M                |
|      | AA4  | CSI1_C1_LN2_P                 | AR1              | I/O      | -               |          | CSI1_C1_LN2_P                       | CSI1_C1_LN2_P                |
|      | AB4  | CSI1_A2_LN2_M                 | AR3              | I/O      | -               |          | CSI1_A2_LN2_M                       | CSI1_A2_LN2_M                |
|      | AA5  | CSI1_B2_LN3_P                 | AR5              | I/O      | -               |          | CSI1_B2_LN3_P                       | CSI1_B2_LN3_P                |
|      | AB5  | CSI1_C2_LN3_M                 | AR7              | I/O      | -               |          | CSI1_C2_LN3_M                       | CSI1_C2_LN3_M                |
|      | AA7  | CSI0_NC_CLK_P                 | AU7              | I/O      | -               |          | CSI0_NC_CLK_P                       | CSI0_NC_CLK_P                |
|      | AB7  | CSI0_A0_CLK_M                 | AU5              | I/O      | -               |          | CSI0_A0_CLK_M                       | CSI0_A0_CLK_M                |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1B  | AA8  | CSIO_B0_LN0_P                 | AV6              | I/O      | -               |         | CSIO_B0_LN0_P                       | CSIO_B0_LN0_P                |
|      | AB8  | CSIO_C0_LN0_M                 | AV4              | I/O      | -               |         | CSIO_C0_LN0_M                       | CSIO_C0_LN0_M                |
|      | AA9  | CSIO_A1_LN1_P                 | AU1              | I/O      | -               |         | CSIO_A1_LN1_P                       | CSIO_A1_LN1_P                |
|      | AB9  | CSIO_B1_LN1_M                 | AU3              | I/O      | -               |         | CSIO_B1_LN1_M                       | CSIO_B1_LN1_M                |
|      | AA10 | CSIO_C1_LN2_P                 | AW1              | I/O      | -               |         | CSIO_C1_LN2_P                       | CSIO_C1_LN2_P                |
|      | AB10 | CSIO_A2_LN2_M                 | AW3              | I/O      | -               |         | CSIO_A2_LN2_M                       | CSIO_A2_LN2_M                |
|      | AA11 | CSIO_B2_LN3_P                 | AW5              | I/O      | -               |         | CSIO_B2_LN3_P                       | CSIO_B2_LN3_P                |
|      | AB11 | CSIO_C2_LN3_M                 | AW7              | I/O      | -               |         | CSIO_C2_LN3_M                       | CSIO_C2_LN3_M                |
|      | DA6  | CSI4_NC_CLK_P                 | AC7              | I/O      | -               |         | CSI4_NC_CLK_P                       | CSI4_NC_CLK_P                |
|      | DB6  | CSI4_A0_CLK_M                 | AC5              | I/O      | -               |         | CSI4_A0_CLK_M                       | CSI4_A0_CLK_M                |
|      | DA7  | CSI4_B0_LN0_P                 | AB8              | I/O      | -               |         | CSI4_B0_LN0_P                       | CSI4_B0_LN0_P                |
|      | DB7  | CSI4_C0_LN0_M                 | AB6              | I/O      | -               |         | CSI4_C0_LN0_M                       | CSI4_C0_LN0_M                |
|      | DA8  | CSI4_A1_LN1_P                 | AA7              | I/O      | -               |         | CSI4_A1_LN1_P                       | CSI4_A1_LN1_P                |
|      | DB8  | CSI4_B1_LN1_M                 | AA5              | I/O      | -               |         | CSI4_B1_LN1_M                       | CSI4_B1_LN1_M                |
|      | DA9  | CSI4_C1_LN2_P                 | Y6               | I/O      | -               |         | CSI4_C1_LN2_P                       | CSI4_C1_LN2_P                |
|      | DB9  | CSI4_A2_LN2_M                 | Y4               | I/O      | -               |         | CSI4_A2_LN2_M                       | CSI4_A2_LN2_M                |
|      | DA10 | CSI4_B2_LN3_P                 | W5               | I/O      | -               |         | CSI4_B2_LN3_P                       | CSI4_B2_LN3_P                |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1B  | DB10 | CSI4_C2_LN3_M                 | W7               | I/O      | -               |         | CSI4_C2_LN3_M                       | CSI4_C2_LN3_M                |
|      | DA12 | CSI3_NC_CLK_P                 | AB2              | I/O      | -               |         | CSI3_NC_CLK_P                       | CSI3_NC_CLK_P                |
|      | DB12 | CSI3_A0_CLK_M                 | AC3              | I/O      | -               |         | CSI3_A0_CLK_M                       | CSI3_A0_CLK_M                |
|      | DA13 | CSI3_B0_LN0_P                 | AC1              | I/O      | -               |         | CSI3_B0_LN0_P                       | CSI3_B0_LN0_P                |
|      | DB13 | CSI3_C0_LN0_M                 | AD2              | I/O      | -               |         | CSI3_C0_LN0_M                       | CSI3_C0_LN0_M                |
|      | DA14 | CSI3_A1_LN1_P                 | AE1              | I/O      | -               |         | CSI3_A1_LN1_P                       | CSI3_A1_LN1_P                |
|      | DB14 | CSI3_B1_LN1_M                 | AE3              | I/O      | -               |         | CSI3_B1_LN1_M                       | CSI3_B1_LN1_M                |
|      | DA15 | CSI3_C1_LN2_P                 | AF2              | I/O      | -               |         | CSI3_C1_LN2_P                       | CSI3_C1_LN2_P                |
|      | DB15 | CSI3_A2_LN2_M                 | AF4              | I/O      | -               |         | CSI3_A2_LN2_M                       | CSI3_A2_LN2_M                |
|      | DA16 | CSI3_B2_LN3_P                 | AG3              | I/O      | -               |         | CSI3_B2_LN3_P                       | CSI3_B2_LN3_P                |
|      | DB16 | CSI3_C2_LN3_M                 | AG5              | I/O      | -               |         | CSI3_C2_LN3_M                       | CSI3_C2_LN3_M                |
|      | DA18 | CSI2_NC_CLK_P                 | AJ7              | I/O      | -               |         | CSI2_NC_CLK_P                       | CSI2_NC_CLK_P                |
|      | DB18 | CSI2_A0_CLK_M                 | AJ5              | I/O      | -               |         | CSI2_A0_CLK_M                       | CSI2_A0_CLK_M                |
|      | DA19 | CSI2_B0_LN0_P                 | AK6              | I/O      | -               |         | CSI2_B0_LN0_P                       | CSI2_B0_LN0_P                |
|      | DB19 | CSI2_C0_LN0_M                 | AK4              | I/O      | -               |         | CSI2_C0_LN0_M                       | CSI2_C0_LN0_M                |
|      | DA20 | CSI2_A1_LN1_P                 | AJ1              | I/O      | -               |         | CSI2_A1_LN1_P                       | CSI2_A1_LN1_P                |
|      | DB20 | CSI2_B1_LN1_M                 | AJ3              | I/O      | -               |         | CSI2_B1_LN1_M                       | CSI2_B1_LN1_M                |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1B  | DA21 | CSI2_C1_LN2_P                 | AL1              | I/O      | -               |         | CSI2_C1_LN2_P                       | CSI2_C1_LN2_P                |
|      | DB21 | CSI2_A2_LN2_M                 | AL3              | I/O      | -               |         | CSI2_A2_LN2_M                       | CSI2_A2_LN2_M                |
|      | DA22 | CSI2_B2_LN3_P                 | AL5              | I/O      | -               |         | CSI2_B2_LN3_P                       | CSI2_B2_LN3_P                |
|      | DB22 | CSI2_C2_LN3_M                 | AL7              | I/O      | -               |         | CSI2_C2_LN3_M                       | CSI2_C2_LN3_M                |
| J1C  | CA16 | UFS_L0_RX_P                   | B24              | I        | -               |         | UFS_L0_RX_P                         | UFS_L0_RX_P                  |
|      | CB16 | UFS_L0_RX_M                   | C25              | I        | -               |         | UFS_L0_RX_M                         | UFS_L0_RX_M                  |
|      | CA17 | UFS_L0_TX_P                   | E25              | O        | -               |         | UFS_L0_TX_P                         | UFS_L0_TX_P                  |
|      | CB17 | UFS_L0_TX_M                   | D24              | O        | -               |         | UFS_L0_TX_M                         | UFS_L0_TX_M                  |
|      | CA18 | UFS_L1_RX_P                   | B22              | I        | -               |         | UFS_L1_RX_P                         | UFS_L1_RX_P                  |
|      | CB18 | UFS_L1_RX_M                   | C23              | I        | -               |         | UFS_L1_RX_M                         | UFS_L1_RX_M                  |
|      | CA19 | UFS_L1_TX_P                   | E23              | O        | -               |         | UFS_L1_TX_P                         | UFS_L1_TX_P                  |
|      | CB19 | UFS_L1_TX_M                   | D22              | O        | -               |         | UFS_L1_TX_M                         | UFS_L1_TX_M                  |
|      | CD15 | UFS_REF_CLK                   | B26              | O        | -               |         | UFS_REF_CLK                         | UFS_REF_CLK                  |
|      | CD16 | UFS_RESET_N                   | D26              | O        | -               |         | UFS_RESET_N                         | UFS_RESET_N                  |
|      | BC2  | SDC2_DATA_3                   | AT42             | BH       | -               |         | SDC2_DATA_3                         | SDC2_DATA_3                  |
|      | BC3  | SDC2_DATA_2                   | AT46             | BH       | -               |         | SDC2_DATA_2                         | SDC2_DATA_2                  |
|      | BC4  | SDC2_DATA_1                   | AR45             | BH       | -               |         | SDC2_DATA_1                         | SDC2_DATA_1                  |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor((MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1C  | BD2  | SDC2_DATA_0                   | AR43             | BH       | -               |         | SDC2_DATA_0                         | SDC2_DATA_0                  |
|      | BD3  | SDC2_CMD                      | AR41             | BH       | -               |         | SDC2_CMD                            | SDC2_CMD                     |
|      | BD4  | SDC2_CLK                      | AT44             | O        | -               |         | SDC2_CLK                            | SDC2_CLK                     |
| J1D  | AA13 | QLINK0_CLK_P                  | BM12             | I/O      | -               |         | QLINK0_CLK_P                        | QLINK0_CLK_P                 |
|      | AB13 | QLINK0_CLK_M                  | BK12             | I/O      | -               |         | QLINK0_CLK_M                        | QLINK0_CLK_M                 |
|      | AA14 | QLINK0_L0_P                   | BL13             | I/O      | -               |         | QLINK0_L0_P                         | QLINK0_L0_P                  |
|      | AB14 | QLINK0_L0_M                   | BJ13             | I/O      | -               |         | QLINK0_L0_M                         | QLINK0_L0_M                  |
|      | AA15 | QLINK0_L1_P                   | BK16             | I/O      | -               |         | QLINK0_L1_P                         | QLINK0_L1_P                  |
|      | AB15 | QLINK0_L1_M                   | BH16             | I/O      | -               |         | QLINK0_L1_M                         | QLINK0_L1_M                  |
|      | AA16 | QLINK0_L2_P                   | BK14             | I/O      | -               |         | QLINK0_L2_P                         | QLINK0_L2_P                  |
|      | AB16 | QLINK0_L2_M                   | BH14             | I/O      | -               |         | QLINK0_L2_M                         | QLINK0_L2_M                  |
|      | AA17 | QLINK0_L3_P                   | BJ15             | I/O      | -               |         | QLINK0_L3_P                         | QLINK0_L3_P                  |
|      | AB17 | QLINK0_L3_M                   | BL15             | I/O      | -               |         | QLINK0_L3_M                         | QLINK0_L3_M                  |
|      | AA18 | QLINK0_L4_P                   | BJ11             | I/O      | -               |         | QLINK0_L4_P                         | QLINK0_L4_P                  |
|      | AB18 | QLINK0_L4_M                   | BL11             | I/O      | -               |         | QLINK0_L4_M                         | QLINK0_L4_M                  |
|      | AA20 | QLINK1_CLK_P                  | BP20             | DNC      | -               |         | QLINK1_CLK_P                        | QLINK1_CLK_P                 |
|      | AB20 | QLINK1_CLK_M                  | BM20             | DNC      | -               |         | QLINK1_CLK_M                        | QLINK1_CLK_M                 |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor((MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1D  | AA21 | QLINK1_L0_P                   | BL21             | DNC      | -               |         | QLINK1_L0_P                         | QLINK1_L0_P                  |
|      | AB21 | QLINK1_L0_M                   | BN21             | DNC      | -               |         | QLINK1_L0_M                         | QLINK1_L0_M                  |
|      | AA22 | QLINK1_L1_P                   | BN19             | DNC      | -               |         | QLINK1_L1_P                         | QLINK1_L1_P                  |
|      | AB22 | QLINK1_L1_M                   | BL19             | DNC      | -               |         | QLINK1_L1_M                         | QLINK1_L1_M                  |
|      | AA23 | QLINK1_L2_P                   | BM18             | DNC      | -               |         | QLINK1_L2_P                         | QLINK1_L2_P                  |
|      | AB23 | QLINK1_L2_M                   | BK18             | DNC      | -               |         | QLINK1_L2_M                         | QLINK1_L2_M                  |
|      | AA24 | QLINK1_L3_P                   | BM24             | DNC      | -               |         | QLINK1_L3_P                         | QLINK1_L3_P                  |
|      | AB24 | QLINK1_L3_M                   | BP24             | DNC      | -               |         | QLINK1_L3_M                         | QLINK1_L3_M                  |
|      | AA25 | QLINK1_L4_P                   | BN25             | DNC      | -               |         | QLINK1_L4_P                         | QLINK1_L4_P                  |
|      | AB25 | QLINK1_L4_M                   | BL25             | DNC      | -               |         | QLINK1_L4_M                         | QLINK1_L4_M                  |
|      | AA26 | QLINK1_L7_P                   | BM22             | DNC      | -               |         | QLINK1_L7_P                         | QLINK1_L7_P                  |
|      | AB26 | QLINK1_L7_M                   | BP22             | DNC      | -               |         | QLINK1_L7_M                         | QLINK1_L7_M                  |
|      | AA27 | QLINK1_L8_P                   | BR23             | DNC      | -               |         | QLINK1_L8_P                         | QLINK1_L8_P                  |
|      | AB27 | QLINK1_L8_M                   | BN23             | DNC      | -               |         | QLINK1_L8_M                         | QLINK1_L8_M                  |
| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor((MB-JD4-QCS8550) | Default function description |
| J1E  | DA2  | PCIE0_REFCLK_P                | R3               | I/O      | -               |         | PCIE0_REFCLK_P                      | PCIE0_REFCLK_P               |
|      | DB2  | PCIE0_REFCLK_M                | T2               | I/O      | -               |         | PCIE0_REFCLK_M                      | PCIE0_REFCLK_M               |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1E  | DA3  | PCIE0_RX_P                    | U1               | I        | -               |         | PCIE0_RX_P                          | PCIE0_RX_P                   |
|      | DB3  | PCIE0_RX_M                    | U3               | I        | -               |         | PCIE0_RX_M                          | PCIE0_RX_M                   |
|      | DA4  | PCIE0_TX_P                    | V4               | O        | -               |         | PCIE0_TX_P                          | PCIE0_TX_P                   |
|      | DB4  | PCIE0_TX_M                    | V2               | O        | -               |         | PCIE0_TX_M                          | PCIE0_TX_M                   |
|      | CD21 | GPIO_87/PCIE0_RST             | BN9              | I/O      | -               |         | QCS_GPIO_87                         | QCS_GPIO_87                  |
|      | AD11 | GPIO_89/PCIE0_WAKE            | BJ9              | I/O      | -               |         | QCS_GPIO_89                         | QCS_GPIO_89                  |
|      | AD12 | GPIO_88/PCIE0_CLKREQ          | BL9              | I/O      | -               |         | QCS_GPIO_88                         | QCS_GPIO_88                  |
|      | BA1  | PCIE1_REFCLK_P                | BM36             | I/O      | -               |         | PCIE1_REFCLK_P                      | PCIE1_REFCLK_P               |
|      | BB1  | PCIE1_REFCLK_M                | BP36             | I/O      | -               |         | PCIE1_REFCLK_M                      | PCIE1_REFCLK_M               |
|      | BA2  | PCIE1_RX0_P                   | BP40             | I        | -               |         | PCIE1_RX0_P                         | PCIE1_RX0_P                  |
|      | BB2  | PCIE1_RX0_M                   | BM40             | I        | -               |         | PCIE1_RX0_M                         | PCIE1_RX0_M                  |
|      | BA3  | PCIE1_TX0_P                   | BR39             | O        | -               |         | PCIE1_TX0_P                         | PCIE1_TX0_P                  |
|      | BB3  | PCIE1_TX0_M                   | BN39             | O        | -               |         | PCIE1_TX0_M                         | PCIE1_TX0_M                  |
|      | BA4  | PCIE1_RX1_P                   | BL37             | I        | -               |         | PCIE1_RX1_P                         | PCIE1_RX1_P                  |
|      | BB4  | PCIE1_RX1_M                   | BN37             | I        | -               |         | PCIE1_RX1_M                         | PCIE1_RX1_M                  |
|      | BA5  | PCIE1_TX1_P                   | BP38             | O        | -               |         | PCIE1_TX1_P                         | PCIE1_TX1_P                  |
|      | BB5  | PCIE1_TX1_M                   | BM38             | O        | -               |         | PCIE1_TX1_M                         | PCIE1_TX1_M                  |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1E  | DD18 | GPIO_2/PCIE1_RST/QUP0_SE7_L4  | AE5              | I/O      | 1.8V            |         | QCS_GPIO_2                          | QCS_GPIO_2                   |
|      | DC19 | GPIO_3/PCIE1_WAKE/QUP0_SE7_L5 | AE7              | I/O      | 1.8V            |         | QCS_GPIO_3                          | QCS_GPIO_3                   |
|      | AC6  | GPIO_79/PCIE1_CLKREQ          | BH6              | I/O      | 1.8V            |         | QCS_GPIO_79                         | QCS_GPIO_79                  |
| J1F  | BB7  | EDP_AUX_P                     | BK30             | I/O      | -               |         | EDP_AUX_P                           | EDP_AUX_P                    |
|      | BB8  | EDP_AUX_M                     | BM30             | I/O      | -               |         | EDP_AUX_M                           | EDP_AUX_M                    |
|      | BA9  | USB0_HS_D_P                   | BH28             | I/O      | -               |         | USB0_HS_D_P                         | USB0_HS_D_P                  |
|      | BB9  | USB0_HS_D_M                   | BJ27             | I/O      | -               |         | USB0_HS_D_M                         | USB0_HS_D_M                  |
|      | BA10 | USB0_SS_RX0_P                 | BM34             | I        | -               |         | USB0_SS_RX0_P                       | USB0_SS_RX0_P                |
|      | BB10 | USB0_SS_RX0_M                 | BK34             | I        | -               |         | USB0_SS_RX0_M                       | USB0_SS_RX0_M                |
|      | BA11 | USB0_SS_TX0_P                 | BN33             | O        | -               |         | USB0_SS_TX0_P                       | USB0_SS_TX0_P                |
|      | BB11 | USB0_SS_TX0_M                 | BL33             | O        | -               |         | USB0_SS_TX0_M                       | USB0_SS_TX0_M                |
|      | BA12 | USB0_SS_RX1_P                 | BJ31             | I        | -               |         | USB0_SS_RX1_P                       | USB0_SS_RX1_P                |
|      | BB12 | USB0_SS_RX1_M                 | BL31             | I        | -               |         | USB0_SS_RX1_M                       | USB0_SS_RX1_M                |
|      | BA13 | USB0_SS_TX1_P                 | BM32             | O        | -               |         | USB0_SS_TX1_P                       | USB0_SS_TX1_P                |
|      | BB13 | USB0_SS_TX1_M                 | BK32             | O        | -               |         | USB0_SS_TX1_M                       | USB0_SS_TX1_M                |
|      | BA7  | USB1_HS_D_P                   | BH26             | B        | -               |         | USB1_HS_D_P                         | USB1_HS_D_P                  |
|      | BA8  | USB1_HS_D_M                   | BJ25             | B        | -               |         | USB1_HS_D_M                         | USB1_HS_D_M                  |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1G  | AC14 | QCM_JTAG_SRST_N               | BR27             | I        | -               |         | QCM_JTAG_SRST_N                     | QCM_JTAG_SRST_N              |
|      | AC15 | QCM_JTAG_TCK                  | BR29             | I        | -               |         | QCM_JTAG_TCK                        | QCM_JTAG_TCK                 |
|      | AC16 | QCM_JTAG_TDI                  | BN27             | I        | -               |         | QCM_JTAG_TDI                        | QCM_JTAG_TDI                 |
|      | AD14 | QCM_JTAG_TDO                  | BN29             | I        | -               |         | QCM_JTAG_TDO                        | QCM_JTAG_TDO                 |
|      | AD15 | QCM_JTAG_TMS                  | BM28             | O        | -               |         | QCM_JTAG_TMS                        | QCM_JTAG_TMS                 |
|      | AD16 | QCM_JTAG_TRST_N               | BP28             | O        | -               |         | QCM_JTAG_TRST_N                     | QCM_JTAG_TRST_N              |
|      | EA1  | SPMI_CLK                      | V44              | O        | -               |         | SPMI_CLK                            | SPMI_CLK                     |
|      | EA2  | SPMI_DATA                     | W43              | BH       | -               |         | SPMI_DATA                           | SPMI_DATA                    |
|      | CC4  | QCM_RESOUT_N                  | W47              | O        | -               |         | QCM_RESOUT_N                        | QCM_RESOUT_N                 |
|      | EJ7  | PM_RESIN_N                    | -                | I/O      | -               |         | PM_RESIN_N                          | PM_RESIN_N                   |
|      | EH8  | PHONE_ON_N                    | -                | I/O      | -               |         | PHONE_ON_N                          | PHONE_ON_N                   |
|      | EJ8  | PS_HOLD                       | C47              | O        | -               |         | PS_HOLD                             | PS_HOLD                      |
|      | EH6  | QCM_RESIN_N                   | V42              | O        | -               |         | QCM_RESIN_N                         | QCM_RESIN_N                  |
|      | EH9  | SLEEP_CLK                     | B46              | I        | -               |         | SLEEP_CLK                           | SLEEP_CLK                    |
| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
| J1H  | DD4  | IRIS_GREEN                    | -                | I/O      | -               |         | IRIS_GREEN                          | IRIS_GREEN                   |
|      | DC5  | IRIS_RED                      | -                | I/O      | -               |         | IRIS_RED                            | IRIS_RED                     |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor((MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1H  | DD3  | IRIS_BLUE                     | -                | I/O      | -               |         | IRIS_BLUE                           | IRIS_BLUE                    |
|      | DC4  | VSENSE_S1C_P                  | -                | I/O      | -               |         | VSENSE_S1C_P                        | VSENSE_S1C_P                 |
|      | DD1  | FLASH_LED1                    | -                | I/O      | -               |         | FLASH_LED1                          | FLASH_LED1                   |
|      | DC2  | FLASH_LED2                    | -                | I/O      | -               |         | FLASH_LED2                          | FLASH_LED2                   |
|      | DC3  | FLASH_LED3                    | -                | I/O      | -               |         | FLASH_LED3                          | FLASH_LED3                   |
|      | DD2  | FLASH_LED4                    | -                | I/O      | -               |         | FLASH_LED4                          | FLASH_LED4                   |
|      | EC1  | PMC_GPIO_01                   | -                | I/O      | -               |         | PMC_GPIO_01                         | PMC_GPIO_01                  |
|      | AC3  | PMC_GPIO_02                   | -                | I/O      | -               |         | PMC_GPIO_02                         | PMC_GPIO_02                  |
|      | EB2  | PMC_GPIO_03                   | -                | I/O      | -               |         | PMC_GPIO_03                         | PMC_GPIO_03                  |
|      | EB1  | PMC_GPIO_04                   | -                | I/O      | -               |         | PMC_GPIO_04                         | PMC_GPIO_04                  |
|      | AD2  | PMC_GPIO_05                   | -                | I/O      | -               |         | PMC_GPIO_05                         | PMC_GPIO_05                  |
|      | AC1  | PMC_GPIO_06                   | -                | I/O      | -               |         | PMC_GPIO_06                         | PMC_GPIO_06                  |
|      | AC2  | PMC_GPIO_07                   | -                | I/O      | -               |         | PMC_GPIO_07                         | PMC_GPIO_07                  |
|      | AD1  | PMC_GPIO_08                   | -                | I/O      | -               |         | PMC_GPIO_08                         | PMC_GPIO_08                  |
|      | AD3  | PMC_GPIO_09                   | -                | I/O      | -               |         | PMC_GPIO_09                         | PMC_GPIO_09                  |
|      | EG3  | RF_CLK1                       | -                | I/O      | -               |         | RF_CLK1                             | RF_CLK1                      |
|      | CA24 | RF_CLK2                       | -                | I/O      | -               |         | RF_CLK2                             | RF_CLK2                      |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor((MB-JD4-QCS8550) | Default function description  |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|-------------------------------|
| J1H  | CA23 | RF_CLK3                       | -                | I/O      | -               |         | RF_CLK3                             | RF_CLK3                       |
|      | EF3  | RF_CLK4                       | -                | I/O      | -               |         | RF_CLK4                             | RF_CLK4                       |
|      | DC8  | RF_CLK5                       | -                | I/O      | -               |         | RF_CLK5                             | RF_CLK5                       |
|      | DD10 | LN_BB_CLK2                    | -                | I/O      | -               |         | LN_BB_CLK2                          | LN_BB_CLK2                    |
|      | DD11 | LN_BB_CLK3                    | -                | I/O      | -               |         | LN_BB_CLK3                          | LN_BB_CLK3                    |
|      | BC13 | PMK_GPIO_03                   | -                | I/O      | 1.8V            |         | PMK_GPIO_03                         | PMK_GPIO_03                   |
|      | BA21 | PM7325_AMUX_2_CAM_FLASH_THERM | -                | I/O      | -               |         | PM7325_AMUX_2_CAM_FLASH_THERM       | PM7325_AMUX_2_CAM_FLASH_THERM |
|      | BB21 | PM7325_AMUX_4_WIDE_RFC_THERM  | -                | I/O      | -               |         | PM7325_AMUX_4_WIDE_RFC_THERM        | PM7325_AMUX_4_WIDE_RFC_THERM  |
|      | CC9  | PM7325_GPIO_01                | -                | I/O      | -               |         | PM7325_GPIO_01                      | PM7325_GPIO_01                |
|      | CA12 | PM7325_GPIO_02                | -                | I/O      | -               |         | PM7325_GPIO_02                      | PM7325_GPIO_02                |
|      | CB11 | PM7325_GPIO_03                | -                | I/O      | -               |         | PM7325_GPIO_03                      | PM7325_GPIO_03                |
|      | BB22 | PM7325_GPIO_04                | -                | I/O      | -               |         | PM7325_GPIO_04                      | PM7325_GPIO_04                |
|      | BA22 | PM7325_GPIO_05                | -                | I/O      | -               |         | PM7325_GPIO_05                      | PM7325_GPIO_05                |
|      | BA23 | PM7325_GPIO_06                | -                | I/O      | -               |         | PM7325_GPIO_06                      | PM7325_GPIO_06                |
|      | CA11 | PM7325_GPIO_07                | -                | I/O      | -               |         | PM7325_GPIO_07                      | PM7325_GPIO_07                |
|      | CB10 | PM7325_GPIO_08                | -                | I/O      | -               |         | PM7325_GPIO_08                      | PM7325_GPIO_08                |
|      | CC8  | PM7325_GPIO_09                | -                | I/O      | -               |         | PM7325_GPIO_09                      | PM7325_GPIO_09                |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition  | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|--------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J11  | DC21 | GPIO_0/QUP0_SE2_L0             | AG7              | I/O      | 1.8V            | DOWN    | QCS_GPIO_0                          | QCS_GPIO_0                   |
|      | DC20 | GPIO_1/QUP0_SE0_L1             | AF6              | I/O      | 1.8V            | DOWN    | QCS_GPIO_1                          | QCS_GPIO_1                   |
|      | CC25 | GPIO_4/QUP0_SE1_L0             | BP2              | I/O      | 1.8V            | DOWN    | QCS_GPIO_4                          | QCS_GPIO_4                   |
|      | CD24 | GPIO_5/QUP0_SE1_L1             | BN3              | I/O      | 1.8V            | DOWN    | QCS_GPIO_5                          | QCS_GPIO_5                   |
|      | CC24 | GPIO_6/QUP0_SE1_L2/QUP0_SE7_L0 | BN1              | I/O      | 1.8V            | DOWN    | QCS_GPIO_6                          | QCS_GPIO_6                   |
|      | CB25 | GPIO_7/QUP0_SE1_L3             | BM2              | I/O      | 1.8V            | DOWN    | QCS_GPIO_7                          | QCS_GPIO_7                   |
|      | AC4  | GPIO_8/QUP0_SE2_L0             | BK4              | I/O      | 1.8V            | DOWN    | QCS_GPIO_8                          | QCS_GPIO_8                   |
|      | CB22 | GPIO_9/QUP0_SE2_L1             | BK2              | I/O      | 1.8V            | DOWN    | QCS_GPIO_9                          | QCS_GPIO_9                   |
|      | CB21 | GPIO_10/QUP0_SE2_L2            | BJ3              | I/O      | 1.8V            | DOWN    | QCS_GPIO_10                         | QCS_GPIO_10                  |
|      | CC20 | GPIO_11/QUP0_SE2_L3            | BJ1              | I/O      | 1.8V            | DOWN    | QCS_GPIO_11                         | QCS_GPIO_11                  |
|      | EG2  | GPIO_12/QUP0_SE3_L0            | M2               | I/O      | 1.8V            | DOWN    | QCS_GPIO_12                         | QCS_GPIO_12                  |
|      | EE2  | GPIO_13/QUP0_SE3_L1            | M4               | I/O      | 1.8V            | DOWN    | QCS_GPIO_13                         | QCS_GPIO_13                  |
|      | EJ3  | GPIO_14/QUP0_SE3_L2            | L3               | I/O      | 1.8V            | DOWN    | QCS_GPIO_14                         | QCS_GPIO_14                  |
|      | EJ1  | GPIO_15/QUP0_SE3_L3            | L1               | I/O      | 1.8V            | DOWN    | QCS_GPIO_15                         | QCS_GPIO_15                  |
|      | ED2  | GPIO_16/QUP0_SE4_L0            | P4               | I/O      | 1.8V            | DOWN    | QCS_GPIO_16                         | QCS_GPIO_16                  |
|      | EC2  | GPIO_17/QUP0_SE4_L1            | N5               | I/O      | 1.8V            | DOWN    | QCS_GPIO_17                         | QCS_GPIO_17                  |
|      | EH2  | GPIO_18/QUP0_SE4_L2            | N1               | I/O      | 1.8V            | DOWN    | QCS_GPIO_18                         | QCS_GPIO_18                  |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J11  | EF2  | GPIO_19/QUP0_SE4_L3           | N3               | I/O      | 1.8V            | DOWN    | QCS_GPIO_19                         | QCS_GPIO_19                  |
|      | AC8  | GPIO_20/QUP0_SE5_L0           | BG5              | I/O      | 1.8V            | DOWN    | QCS_GPIO_20                         | QCS_GPIO_20                  |
|      | CD20 | GPIO_21/QUP0_SE5_L1           | BG3              | I/O      | 1.8V            | DOWN    | QCS_GPIO_21                         | QCS_GPIO_21                  |
|      | AD7  | GPIO_22/QUP0_SE5_L2           | BF6              | I/O      | 1.8V            | DOWN    | QCS_GPIO_22                         | QCS_GPIO_22                  |
|      | AD6  | GPIO_23/QUP0_SE5_L3           | BF4              | I/O      | 1.8V            | DOWN    | QCS_GPIO_23                         | QCS_GPIO_23                  |
|      | CD19 | GPIO_24/QUP0_SE6_L0           | BF2              | I/O      | 1.8V            | DOWN    | QCS_GPIO_24                         | QCS_GPIO_24                  |
|      | CB20 | GPIO_25/QUP0_SE6_L1           | BE1              | I/O      | 1.8V            | DOWN    | QCS_GPIO_25                         | QCS_GPIO_25                  |
|      | CA20 | GPIO_26/QUP0_SE6_L2           | BE3              | I/O      | 1.8V            | DOWN    | QCS_GPIO_26                         | QCS_GPIO_26                  |
|      | CA21 | GPIO_27/QUP0_SE6_L3           | BD2              | I/O      | 1.8V            | DOWN    | QCS_GPIO_27                         | QCS_GPIO_27                  |
|      | AD5  | GPIO_28/QUP0_SE7_L0           | BE5              | I/O      | 1.8V            | DOWN    | QCS_GPIO_28                         | QCS_GPIO_28                  |
|      | AD4  | GPIO_29/QUP0_SE7_L1           | BD6              | I/O      | 1.8V            | DOWN    | QCS_GPIO_29                         | QCS_GPIO_29                  |
|      | AD8  | GPIO_30/QUP0_SE7_L2           | BD4              | I/O      | 1.8V            | UP      | QCS_GPIO_30                         | QCS_GPIO_30                  |
|      | EE3  | GPIO_31/QUP0_SE7_L3           | BC5              | I/O      | 1.8V            | DOWN    | QCS_GPIO_31                         | QCS_GPIO_31                  |
|      | EB10 | GPIO_32/QUP1_SE0_L0           | AW43             | I/O      | 1.8V            | DOWN    | QCS_GPIO_32                         | QCS_GPIO_32                  |
|      | EA9  | GPIO_33/QUP1_SE0_L1           | AW41             | I/O      | 1.8V            | DOWN    | QCS_GPIO_33                         | QCS_GPIO_33                  |
|      | EB11 | GPIO_34/QUP1_SE0_L2           | AY42             | I/O      | 1.8V            | DOWN    | QCS_GPIO_34                         | QCS_GPIO_34                  |
|      | ED11 | GPIO_35/QUP1_SE0_L3           | BA43             | I/O      | 1.8V            | DOWN    | QCS_GPIO_35                         | QCS_GPIO_35                  |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition   | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|---------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J11  | EF9  | GPIO_36/QUP1_SE1_L0             | BF42             | I/O      | 1.8V            | DOWN    | QCS_GPIO_36                         | QCS_GPIO_36                  |
|      | EF11 | GPIO_37/QUP1_SE1_L1             | BG43             | I/O      | 1.8V            | DOWN    | QCS_GPIO_37                         | QCS_GPIO_37                  |
|      | EJ11 | GPIO_38/QUP1_SE1_L2/QUP1_SE4_L6 | BH46             | I/O      | 1.8V            | UP      | QCS_GPIO_38                         | QCS_GPIO_38                  |
|      | AC19 | GPIO_39/QUP1_SE1_L3             | BH44             | I/O      | 1.8V            | DOWN    | QCS_GPIO_39                         | QCS_GPIO_39                  |
|      | CB13 | GPIO_40/QUP1_SE2_L0             | AY46             | I/O      | 1.8V            | DOWN    | QCS_GPIO_40                         | QCS_GPIO_40                  |
|      | AD23 | GPIO_41/QUP1_SE2_L1             | AY44             | I/O      | 1.8V            | DOWN    | QCS_GPIO_41                         | QCS_GPIO_41                  |
|      | AC23 | GPIO_42/QUP1_SE2_L2             | BA45             | I/O      | 1.8V            | DOWN    | QCS_GPIO_42                         | QCS_GPIO_42                  |
|      | CB14 | GPIO_43/QUP1_SE2_L3             | BB46             | I/O      | 1.8V            | DOWN    | QCS_GPIO_43                         | QCS_GPIO_43                  |
|      | EA10 | GPIO_44/QUP1_SE3_L0             | BA41             | I/O      | 1.8V            | DOWN    | QCS_GPIO_44                         | QCS_GPIO_44                  |
|      | ED10 | GPIO_45/QUP1_SE3_L1             | BB42             | I/O      | 1.8V            | DOWN    | QCS_GPIO_45                         | QCS_GPIO_45                  |
|      | EA11 | GPIO_46/QUP1_SE3_L2             | BC41             | I/O      | 1.8V            | DOWN    | QCS_GPIO_46                         | QCS_GPIO_46                  |
|      | EE10 | GPIO_47/QUP1_SE3_L3             | BC43             | I/O      | 1.8V            | DOWN    | QCS_GPIO_47                         | QCS_GPIO_47                  |
|      | AD22 | GPIO_48/QUP1_SE4_L0             | BB44             | I/O      | 1.8V            | DOWN    | QCS_GPIO_48                         | QCS_GPIO_48                  |
|      | CC10 | GPIO_49/QUP1_SE4_L1             | BC47             | I/O      | 1.8V            | DOWN    | QCS_GPIO_49                         | QCS_GPIO_49                  |
|      | AC22 | GPIO_50/QUP1_SE4_L2/QUP1_SE6_L7 | BC45             | I/O      | 1.8V            | DOWN    | QCS_GPIO_50                         | QCS_GPIO_50                  |
|      | CD9  | GPIO_51/QUP1_SE4_L3             | BD46             | I/O      | 1.8V            | DOWN    | QCS_GPIO_51                         | QCS_GPIO_51                  |
|      | AC21 | GPIO_52/QUP1_SE5_L0             | BD44             | I/O      | 1.8V            | DOWN    | QCS_GPIO_52                         | QCS_GPIO_52                  |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition   | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|---------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1I  | AD21 | GPIO_53/QUP1_SE5_L1             | BE45             | I/O      | 1.8V            | DOWN    | QCS_GPIO_53                         | QCS_GPIO_53                  |
|      | EC9  | GPIO_54/QUP1_SE5_L2/QUP1_SE4_L5 | BD42             | I/O      | 1.8V            | DOWN    | QCS_GPIO_54                         | QCS_GPIO_54                  |
|      | EE11 | GPIO_55/QUP1_SE5_L3/QUP1_SE4_L4 | BE43             | I/O      | 1.8V            | DOWN    | QCS_GPIO_55                         | QCS_GPIO_55                  |
|      | CD10 | GPIO_56/QUP1_SE6_L0             | BF46             | I/O      | 1.8V            | DOWN    | QCS_GPIO_56                         | QCS_GPIO_56                  |
|      | AC20 | GPIO_57/QUP1_SE6_L1             | BF44             | I/O      | 1.8V            | DOWN    | QCS_GPIO_57                         | QCS_GPIO_57                  |
|      | CC11 | GPIO_58/QUP1_SE6_L2             | BG47             | I/O      | 1.8V            | DOWN    | QCS_GPIO_58                         | QCS_GPIO_58                  |
|      | AD20 | GPIO_59/QUP1_SE6_L3             | BG45             | I/O      | 1.8V            | UP      | QCS_GPIO_59                         | QCS_GPIO_59                  |
|      | ED9  | GPIO_60/QUP1_SE7_L0             | BH42             | I/O      | 1.8V            | DOWN    | QCS_GPIO_60                         | QCS_GPIO_60                  |
|      | EF10 | GPIO_61/QUP1_SE7_L2             | BJ43             | I/O      | 1.8V            | DOWN    | QCS_GPIO_61                         | QCS_GPIO_61                  |
|      | AD19 | GPIO_62/QUP1_SE7_L2/QUP1_SE6_L4 | BJ45             | I/O      | 1.8V            | DOWN    | QCS_GPIO_62                         | QCS_GPIO_62                  |
|      | CC13 | GPIO_63/QUP1_SE7_L3/QUP1_SE6_L5 | BK46             | I/O      | 1.8V            | DOWN    | QCS_GPIO_63                         | QCS_GPIO_63                  |
|      | EE1  | GPIO_64                         | AA1              | I/O      | 1.8V            | DOWN    | QCS_GPIO_64                         | QCS_GPIO_64                  |
|      | EF1  | GPIO_65                         | Y2               | I/O      | 1.8V            | DOWN    | QCS_GPIO_65                         | QCS_GPIO_65                  |
|      | ED1  | GPIO_66                         | AA3              | I/O      | 1.8V            | DOWN    | QCS_GPIO_66                         | QCS_GPIO_66                  |
|      | EG1  | GPIO_67                         | W1               | I/O      | 1.8V            | DOWN    | QCS_GPIO_67                         | QCS_GPIO_67                  |
|      | EH1  | GPIO_68                         | P2               | I/O      | 1.8V            | DOWN    | QCS_GPIO_68                         | QCS_GPIO_68                  |
|      | CC18 | GPIO_69                         | BC3              | I/O      | 1.8V            | DOWN    | QCS_GPIO_69                         | QCS_GPIO_69                  |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1I  | CC19 | GPIO_70                       | BB2              | I/O      | 1.8V            | DOWN    | QCS_GPIO_70                         | QCS_GPIO_70                  |
|      | EA3  | GPIO_71                       | BB4              | I/O      | 1.8V            | DOWN    | QCS_GPIO_71                         | QCS_GPIO_71                  |
|      | ED3  | GPIO_72                       | BB6              | I/O      | 1.8V            | DOWN    | QCS_GPIO_72                         | QCS_GPIO_72                  |
|      | CD17 | GPIO_73                       | BA1              | I/O      | 1.8V            | DOWN    | QCS_GPIO_73                         | QCS_GPIO_73                  |
|      | CD18 | GPIO_74                       | BA3              | I/O      | 1.8V            | DOWN    | QCS_GPIO_74                         | QCS_GPIO_74                  |
|      | EB3  | GPIO_75                       | BA5              | I/O      | 1.8V            | DOWN    | QCS_GPIO_75                         | QCS_GPIO_75                  |
|      | EC3  | GPIO_76                       | BA7              | I/O      | 1.8V            | DOWN    | QCS_GPIO_76                         | QCS_GPIO_76                  |
|      | AC7  | GPIO_77                       | BH4              | I/O      | 1.8V            | DOWN    | QCS_GPIO_77                         | QCS_GPIO_77                  |
|      | CC21 | GPIO_78                       | BH2              | I/O      | 1.8V            | DOWN    | QCS_GPIO_78                         | QCS_GPIO_78                  |
|      | CA13 | GPIO_80                       | AG47             | I/O      | 1.8V            | DOWN    | QCS_GPIO_80                         | QCS_GPIO_80                  |
|      | CD8  | GPIO_81                       | AR47             | I/O      | 1.8V            | DOWN    | QCS_GPIO_81                         | QCS_GPIO_81                  |
|      | AC18 | GPIO_82                       | BK44             | I/O      | 1.8V            | DOWN    | QCS_GPIO_82                         | QCS_GPIO_82                  |
|      | EJ2  | GPIO_83                       | K2               | I/O      | 1.8V            | DOWN    | QCS_GPIO_83                         | QCS_GPIO_83                  |
| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
| J1J  | DC1  | GPIO_84                       | BR11             | I/O      | 1.8V            | DOWN    | QCS_GPIO_84                         | QCS_GPIO_84                  |
|      | CA22 | GPIO_85                       | BP10             | I/O      | 1.8V            | DOWN    | QCS_GPIO_85                         | QCS_GPIO_85                  |
|      | CD22 | GPIO_86                       | BR9              | I/O      | 1.8V            | DOWN    | QCS_GPIO_86                         | QCS_GPIO_86                  |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1J  | CC23 | GPIO_90                       | BP8              | I/O      | 1.8V            | DOWN    | QCS_GPIO_90                         | QCS_GPIO_90                  |
|      | EJ4  | GPIO_91                       | K4               | I/O      | 1.8V            | DOWN    | QCS_GPIO_91                         | QCS_GPIO_91                  |
|      | EH3  | GPIO_93                       | L5               | I/O      | 1.8V            | DOWN    | QCS_GPIO_93                         | QCS_GPIO_93                  |
|      | EJ6  | GPIO_94                       | W45              | I/O      | 1.8V            | DOWN    | QCS_GPIO_94                         | QCS_GPIO_94                  |
|      | CC2  | GPIO_95                       | Y46              | I/O      | 1.8V            | DOWN    | QCS_GPIO_95                         | QCS_GPIO_95                  |
|      | CD1  | GPIO_96                       | AA45             | I/O      | 1.8V            | DOWN    | QCS_GPIO_96                         | QCS_GPIO_96                  |
|      | CC3  | GPIO_97                       | AA47             | I/O      | 1.8V            | DOWN    | QCS_GPIO_97                         | QCS_GPIO_97                  |
|      | CC1  | GPIO_98                       | AB44             | I/O      | 1.8V            | DOWN    | QCS_GPIO_98                         | QCS_GPIO_98                  |
|      | CD2  | GPIO_99                       | AB46             | I/O      | 1.8V            | DOWN    | QCS_GPIO_99                         | QCS_GPIO_99                  |
|      | CA14 | GPIO_100                      | AH46             | I/O      | 1.8V            | DOWN    | QCS_GPIO_100                        | QCS_GPIO_100                 |
|      | AC25 | GPIO_101                      | AU45             | I/O      | 1.8V            | DOWN    | QCS_GPIO_101                        | QCS_GPIO_101                 |
|      | CB12 | GPIO_102                      | AV46             | I/O      | 1.8V            | DOWN    | QCS_GPIO_102                        | QCS_GPIO_102                 |
|      | AD24 | GPIO_103                      | AV44             | I/O      | 1.8V            | DOWN    | QCS_GPIO_103                        | QCS_GPIO_103                 |
|      | CD7  | GPIO_104                      | AW47             | I/O      | 1.8V            | DOWN    | QCS_GPIO_104                        | QCS_GPIO_104                 |
|      | AC24 | GPIO_105                      | AW45             | I/O      | 1.8V            | DOWN    | QCS_GPIO_105                        | QCS_GPIO_105                 |
|      | EC11 | GPIO_106                      | AU43             | I/O      | 1.8V            | DOWN    | QCS_GPIO_106                        | QCS_GPIO_106                 |
|      | EB9  | GPIO_107                      | AU41             | I/O      | 1.8V            | DOWN    | QCS_GPIO_107                        | QCS_GPIO_107                 |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1J  | EC10 | GPIO_108                      | AV42             | I/O      | 1.8V            | DOWN    | QCS_GPIO_108                        | QCS_GPIO_108                 |
|      | CC12 | GPIO_109                      | BL47             | I/O      | 1.8V            | DOWN    | QCS_GPIO_109                        | QCS_GPIO_109                 |
|      | EH11 | GPIO_110                      | BM46             | I/O      | 1.8V            | DOWN    | QCS_GPIO_110                        | QCS_GPIO_110                 |
|      | EJ10 | GPIO_111                      | BP46             | I/O      | 1.8V            | DOWN    | QCS_GPIO_111                        | QCS_GPIO_111                 |
|      | EH10 | GPIO_112                      | BN45             | I/O      | 1.8V            | DOWN    | QCS_GPIO_112                        | QCS_GPIO_112                 |
|      | BC21 | GPIO_113                      | BR45             | I/O      | 1.8V            | DOWN    | QCS_GPIO_113                        | QCS_GPIO_113                 |
|      | EG11 | GPIO_114                      | BN43             | I/O      | 1.8V            | DOWN    | QCS_GPIO_114                        | QCS_GPIO_114                 |
|      | EG9  | GPIO_115                      | BM42             | I/O      | 1.8V            | DOWN    | QCS_GPIO_115                        | QCS_GPIO_115                 |
|      | AD17 | GPIO_116                      | BM44             | I/O      | 1.8V            | DOWN    | QCS_GPIO_116                        | QCS_GPIO_116                 |
|      | CB23 | GPIO_117                      | BP6              | I/O      | 1.8V            | DOWN    | QCS_GPIO_117                        | QCS_GPIO_117                 |
|      | AC10 | GPIO_118                      | BM6              | I/O      | 1.8V            | DOWN    | QCS_GPIO_118                        | QCS_GPIO_118                 |
|      | AC9  | GPIO_119                      | BK6              | I/O      | 1.8V            | DOWN    | QCS_GPIO_119                        | QCS_GPIO_119                 |
|      | CA25 | GPIO_121                      | BR5              | I/O      | 1.8V            | DOWN    | QCS_GPIO_121                        | QCS_GPIO_121                 |
|      | CD23 | GPIO_123                      | BM4              | I/O      | 1.8V            | DOWN    | QCS_GPIO_123                        | QCS_GPIO_123                 |
|      | AD9  | GPIO_125                      | BL7              | I/O      | 1.8V            | DOWN    | QCS_GPIO_125                        | QCS_GPIO_125                 |
|      | AD10 | GPIO_126                      | BJ7              | I/O      | 1.8V            | DOWN    | QCS_GPIO_126                        | QCS_GPIO_126                 |
|      | AC12 | GPIO_127                      | BM8              | I/O      | 1.8V            | DOWN    | QCS_GPIO_127                        | QCS_GPIO_127                 |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1J  | AC11 | GPIO_128                      | BK8              | I/O      | 1.8V            | DOWN    | QCS_GPIO_128                        | QCS_GPIO_128                 |
|      | CB26 | GPIO_129                      | BP14             | I/O      | 1.8V            | DOWN    | QCS_GPIO_129                        | QCS_GPIO_129                 |
|      | CA27 | GPIO_130                      | BR13             | I/O      | 1.8V            | DOWN    | QCS_GPIO_130                        | QCS_GPIO_130                 |
|      | AC5  | GPIO_131                      | BJ5              | I/O      | 1.8V            | DOWN    | QCS_GPIO_131                        | QCS_GPIO_131                 |
|      | CB24 | GPIO_132                      | BN5              | I/O      | 1.8V            | DOWN    | QCS_GPIO_132                        | QCS_GPIO_132                 |
|      | CA26 | GPIO_133                      | BP12             | I/O      | 1.8V            | DOWN    | QCS_GPIO_133                        | QCS_GPIO_133                 |
|      | CA29 | GPIO_134                      | BN15             | I/O      | 1.8V            | DOWN    | QCS_GPIO_134                        | QCS_GPIO_134                 |
|      | CA28 | GPIO_135                      | BR15             | I/O      | 1.8V            | DOWN    | QCS_GPIO_135                        | QCS_GPIO_135                 |
|      | CB28 | GPIO_136                      | BP16             | I/O      | 1.8V            | DOWN    | QCS_GPIO_136                        | QCS_GPIO_136                 |
|      | CB27 | GPIO_137                      | BR17             | I/O      | 1.8V            | DOWN    | QCS_GPIO_137                        | QCS_GPIO_137                 |
|      | CC22 | GPIO_138                      | BN7              | I/O      | 1.8V            | DOWN    | QCS_GPIO_138                        | QCS_GPIO_138                 |
|      | EE9  | GPIO_140                      | BK42             | I/O      | 1.8V            | DOWN    | QCS_GPIO_140                        | QCS_GPIO_140                 |
|      | AD18 | GPIO_141                      | BL45             | I/O      | 1.8V            | DOWN    | QCS_GPIO_141                        | QCS_GPIO_141                 |
|      | EG10 | GPIO_142                      | BL43             | I/O      | 1.8V            | DOWN    | QCS_GPIO_142                        | QCS_GPIO_142                 |
|      | CA8  | GPIO_144                      | N45              | I/O      | 1.8V            | DOWN    | QCS_GPIO_144                        | QCS_GPIO_144                 |
|      | CD6  | GPIO_145                      | P46              | I/O      | 1.8V            | DOWN    | QCS_GPIO_145                        | QCS_GPIO_145                 |
|      | CC7  | GPIO_146                      | P44              | I/O      | 1.8V            | DOWN    | QCS_GPIO_146                        | QCS_GPIO_146                 |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1J  | CD5  | GPIO_147                      | N47              | I/O      | 1.8V            | DOWN    | QCS_GPIO_147                        | QCS_GPIO_147                 |
|      | CA7  | GPIO_148                      | L45              | I/O      | 1.8V            | DOWN    | QCS_GPIO_148                        | QCS_GPIO_148                 |
|      | CB7  | GPIO_149                      | M46              | I/O      | 1.8V            | DOWN    | QCS_GPIO_149                        | QCS_GPIO_149                 |
|      | CC17 | GPIO_150                      | P42              | I/O      | 1.8V            | DOWN    | QCS_GPIO_150                        | QCS_GPIO_150                 |
|      | CC16 | GPIO_151                      | R43              | I/O      | 1.8V            | DOWN    | QCS_GPIO_151                        | QCS_GPIO_151                 |
|      | EH5  | GPIO_152                      | T44              | I/O      | 1.8V            | DOWN    | QCS_GPIO_152                        | QCS_GPIO_152                 |
|      | EJ5  | GPIO_153                      | U43              | I/O      | 1.8V            | DOWN    | QCS_GPIO_153                        | QCS_GPIO_153                 |
|      | CC6  | GPIO_154                      | T46              | I/O      | 1.8V            | DOWN    | QCS_GPIO_154                        | QCS_GPIO_154                 |
|      | CD3  | GPIO_155                      | U47              | I/O      | 1.8V            | DOWN    | QCS_GPIO_155                        | QCS_GPIO_155                 |
|      | EH7  | GPIO_156                      | U45              | I/O      | 1.8V            | DOWN    | QCS_GPIO_156                        | QCS_GPIO_156                 |
|      | CC5  | GPIO_157                      | V46              | I/O      | 1.8V            | DOWN    | QCS_GPIO_157                        | QCS_GPIO_157                 |
|      | EH4  | GPIO_158                      | R45              | I/O      | 1.8V            | DOWN    | QCS_GPIO_158                        | QCS_GPIO_158                 |
|      | CD14 | GPIO_159                      | F46              | I/O      | 1.8V            | DOWN    | QCS_GPIO_159                        | QCS_GPIO_159                 |
|      | CC15 | GPIO_160                      | G47              | I/O      | 1.8V            | DOWN    | QCS_GPIO_160                        | QCS_GPIO_160                 |
|      | CB8  | GPIO_161                      | G45              | I/O      | 1.8V            | DOWN    | QCS_GPIO_161                        | QCS_GPIO_161                 |
|      | CC14 | GPIO_162                      | H46              | I/O      | 1.8V            | DOWN    | QCS_GPIO_162                        | QCS_GPIO_162                 |
|      | CD11 | GPIO_163                      | J47              | I/O      | 1.8V            | DOWN    | QCS_GPIO_163                        | QCS_GPIO_163                 |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1J  | CA9  | GPIO_164                      | J45              | I/O      | 1.8V            | DOWN    | QCS_GPIO_164                        | QCS_GPIO_164                 |
|      | CD12 | GPIO_165                      | K46              | I/O      | 1.8V            | DOWN    | QCS_GPIO_165                        | QCS_GPIO_165                 |
|      | CD4  | GPIO_166                      | L47              | I/O      | 1.8V            | DOWN    | QCS_GPIO_166                        | QCS_GPIO_166                 |
|      | CA10 | GPIO_171                      | C45              | I/O      | 1.8V            | DOWN    | QCS_GPIO_171                        | QCS_GPIO_171                 |
|      | EJ9  | GPIO_172                      | D46              | I/O      | 1.8V            | DOWN    | QCS_GPIO_172                        | QCS_GPIO_172                 |
|      | CD13 | GPIO_173                      | E47              | I/O      | 1.8V            | DOWN    | QCS_GPIO_173                        | QCS_GPIO_173                 |
|      | CB9  | GPIO_174                      | E45              | I/O      | 1.8V            | DOWN    | QCS_GPIO_174                        | QCS_GPIO_174                 |
| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
| J1K  | DD13 | VPH_PWR_1                     | -                | P        |                 |         | VPH_PWR                             | VPH_PWR                      |
|      | DD14 | VPH_PWR_2                     | -                | P        |                 |         | VPH_PWR                             | VPH_PWR                      |
|      | DD15 | VPH_PWR_3                     | -                | P        |                 |         | VPH_PWR                             | VPH_PWR                      |
|      | DC16 | VPH_PWR_4                     | -                | P        |                 |         | VPH_PWR                             | VPH_PWR                      |
|      | DC7  | VREG_L12C_1P8                 | -                | I/O      |                 |         | VREG_L12C_1P8                       | VREG_L12C_1P8                |
|      | DC9  | VREG_L4C_1P8_3P0              | BE37             | I/O      |                 |         | VREG_L4C_1P8_3P0                    | VREG_L4C_1P8_3P0             |
|      | DC10 | VREG_L5C_1P8_3P0              | BE39             | I/O      |                 |         | VREG_L5C_1P8_3P0                    | VREG_L5C_1P8_3P0             |
|      | DC11 | VREG_L9C_2P96                 | -                | I/O      |                 |         | VREG_L9C_2P96                       | VREG_L9C_2P96                |
|      | DC12 | VREG_L8C_1P8                  | -                | I/O      |                 |         | VREG_L8C_1P8                        | VREG_L8C_1P8                 |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor((MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1K  | DC13 | VREG_L7C_3P0                  | -                | I/O      |                 |         | VREG_L7C_3P0                        | VREG_L7C_3P0                 |
|      | DC14 | VREG_L3C_3P0                  | -                | I/O      |                 |         | VREG_L3C_3P0                        | VREG_L3C_3P0                 |
|      | DC15 | VREG_L13C_3P0                 | -                | I/O      |                 |         | VREG_L13C_3P0                       | VREG_L13C_3P0                |
|      | DD6  | VREG_BOB                      | -                | I/O      |                 |         | VREG_BOB                            | VREG_BOB                     |
|      | DD7  | VREG_L6C_2P96                 | AT40             | I/O      |                 |         | VREG_L6C_2P96                       | VREG_L6C_2P96                |
|      | DD8  | VREG_L2C_1P8                  | -                | I/O      |                 |         | VREG_L2C_1P8                        | VREG_L2C_1P8                 |
|      | DD9  | VREG_L11C_2P8                 | -                | I/O      |                 |         | VREG_L11C_2P8                       | VREG_L11C_2P8                |
|      | DD12 | VREG_S1C_2P2                  | -                | I/O      |                 |         | VREG_S1C_2P2                        | VREG_S1C_2P2                 |
|      | BD8  | VPH_PWR_5                     | -                | P        |                 |         | VPH_PWR                             | VPH_PWR                      |
|      | BD9  | VPH_PWR_6                     | -                | P        |                 |         | VPH_PWR                             | VPH_PWR                      |
|      | BD10 | VPH_PWR_7                     | -                | P        |                 |         | VPH_PWR                             | VPH_PWR                      |
|      | BC8  | VPH_PWR_8                     | -                | P        |                 |         | VPH_PWR                             | VPH_PWR                      |
|      | BC9  | VPH_PWR_9                     | -                | P        |                 |         | VPH_PWR                             | VPH_PWR                      |
|      | BC10 | VPH_PWR_10                    | -                | P        |                 |         | VPH_PWR                             | VPH_PWR                      |
|      | BC11 | VREG_S1B_1P856                | -                | I/O      |                 |         | VREG_S1B_1P856                      | VREG_S1B_1P856               |
|      | BC12 | VREG_S7B_0P952                | -                | I/O      |                 |         | VREG_S7B_0P952                      | VREG_S7B_0P952               |
|      | BC14 | VREG_S8B_1P256                | -                | I/O      |                 |         | VREG_S8B_1P256                      | VREG_S8B_1P256               |



# 接口定义 Interface definition

| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1K  | BC15 | VREG_L13B_0P8                 | -                | I/O      |                 |         | VREG_L13B_0P8                       | VREG_L13B_0P8                |
|      | BC16 | VREG_L14B_1P2                 | -                | I/O      |                 |         | VREG_L14B_1P2                       | VREG_L14B_1P2                |
|      | BC17 | VREG_L9B_1P2                  | L23              | I/O      |                 |         | VREG_L9B_1P2                        | VREG_L9B_1P2                 |
|      | BC18 | VREG_L11B_1P776               | -                | I/O      |                 |         | VREG_L11B_1P776                     | VREG_L11B_1P776              |
|      | BC19 | VREG_L19B_1P8                 | -                | I/O      |                 |         | VREG_L19B_1P8                       | VREG_L19B_1P8                |
|      | BC20 | VREG_L18B_1P8                 | -                | I/O      |                 |         | VREG_L18B_1P8                       | VREG_L18B_1P8                |
|      | BD11 | VREG_L2B_3P072                | BF26             | I/O      |                 |         | VREG_L2B_3P072                      | VREG_L2B_3P072               |
|      | BD12 | VREG_L7B_2P5                  | -                | I/O      |                 |         | VREG_L7B_2P5                        | VREG_L7B_2P5                 |
|      | BD13 | VREG_L15B_0P88                | -                | I/O      |                 |         | VREG_L15B_0P88                      | VREG_L15B_0P88               |
|      | BD14 | VREG_L12B_0P8                 | -                | I/O      |                 |         | VREG_L12B_0P8                       | VREG_L12B_0P8                |
|      | BD15 | VREG_L16B_1P2                 | -                | I/O      |                 |         | VREG_L16B_1P2                       | VREG_L16B_1P2                |
|      | BD16 | VREG_L17B_1P8                 | -                | I/O      |                 |         | VREG_L17B_1P8                       | VREG_L17B_1P8                |
|      | BD17 | VCOIN                         | -                | I/O      |                 |         | VCOIN                               | VCOIN                        |
|      | BD18 | VREG_SYS_1P8                  | -                | I/O      |                 |         | VREG_SYS_1P8                        | VREG_SYS_1P8                 |
| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
| J1L  | A1   | GND1                          | -                | G        |                 |         | GND                                 | GND                          |
|      | B1   | GND2                          | -                | G        |                 |         | GND                                 | GND                          |

# 接口定义 Interface definition



| Part | Pin | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|-----|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1L  | C1  | GND3                          | -                | G        |                 |         | GND                                 | GND                          |
|      | D1  | GND4                          | -                | G        |                 |         | GND                                 | GND                          |
|      | EE4 | GND20                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EE5 | GND21                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EE6 | GND22                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EE7 | GND23                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EE8 | GND24                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EF4 | GND27                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EF5 | GND28                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EF6 | GND29                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EF7 | GND30                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EF8 | GND31                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EG4 | GND34                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EG5 | GND35                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EG6 | GND36                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EG7 | GND37                         | -                | G        |                 |         | GND                                 | GND                          |
|      | EG8 | GND38                         | -                | G        |                 |         | GND                                 | GND                          |

# 接口定义 Interface definition



| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1L  | AA6  | GND40                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AB6  | GND41                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AA12 | GND42                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AB12 | GND43                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AA19 | GND44                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AB19 | GND45                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AA28 | GND46                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AB28 | GND47                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AA29 | GND48                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AC13 | GND49                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AD13 | GND50                         | -                | G        |                 |         | GND                                 | GND                          |
|      | AC17 | GND51                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BC1  | GND52                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BD1  | GND53                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BB6  | GND54                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BC5  | GND55                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BC6  | GND56                         | -                | G        |                 |         | GND                                 | GND                          |

# 接口定义 Interface definition



| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1L  | BC7  | GND57                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BD5  | GND58                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BD6  | GND59                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BD7  | GND60                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BA14 | GND61                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BB14 | GND62                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BA20 | GND63                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BB20 | GND64                         | -                | G        |                 |         | GND                                 | GND                          |
|      | CA6  | GND65                         | -                | G        |                 |         | GND                                 | GND                          |
|      | CB6  | GND66                         | -                | G        |                 |         | GND                                 | GND                          |
|      | CA15 | GND67                         | -                | G        |                 |         | GND                                 | GND                          |
|      | CB15 | GND68                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DA1  | GND69                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DB1  | GND70                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DA5  | GND71                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DB5  | GND72                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DC6  | GND73                         | -                | G        |                 |         | GND                                 | GND                          |

# 接口定义 Interface definition



| Part | Pin  | CORE-6490L SoM Pin definition | 6490L Pin Number | Pin type | IO Power domain | IO Pull | Function for Floor (MB-JD4-QCS8550) | Default function description |
|------|------|-------------------------------|------------------|----------|-----------------|---------|-------------------------------------|------------------------------|
| J1L  | DC6  | GND73                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DD5  | GND74                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DA11 | GND75                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DB11 | GND76                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DA17 | GND77                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DB17 | GND78                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DC17 | GND79                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DD16 | GND80                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DC18 | GND81                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DD17 | GND82                         | -                | G        |                 |         | GND                                 | GND                          |
|      | DA23 | GND83                         | -                | G        |                 |         | GND                                 | GND                          |
|      | BA6  | GND84                         | -                | G        |                 |         | GND                                 | GND                          |



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